

"Full Bridge" IGBT MTP (Warp Speed IGBT), 50 A



PRODUCT SUMMARY				
V _{CES}	600 V			
I _C DC	69 A			
V _{CE(on)}	2.22 V			
Speed	8 kHz to 30 kHz			
Package	MTP			
Circuit	Full bridge			

FEATURES

- Gen 4 warp speed IGBT technology
- HEXFRED® antiparallel diodes with ultrasoft reverse recovery



RoHS COMPLIANT

- Very low conduction and switching losses
- Optional SMT thermistor
- Al₂O₃ DBC
- Very low stray inductance design for high speed operation
- Speed 8 kHz to 30 kHz > 20 kHz hard switching, > 200 kHz resonant mode
- UL approved file E78996



- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

BENEFITS

- · Optimized for welding, UPS and SMPS applications
- Low EMI, requires less snubbing
- Direct mounting to heatsink
- PCB solderable terminals
- Very low junction to case thermal resistance

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Collector to emitter voltage	V _{CES}		600	V	
Continuous collector current	,	T _C = 25 °C	69		
Continuous collector current	I _C	T _C = 80 °C	46		
Pulsed collector current	I _{CM}		200	A	
Peak switching current	I _{LM}		200	A	
Diode continuous forward current	I _F	T _C = 100 °C	25		
Peak diode forward current	I _{FM}		200		
Gate to emitter voltage	V_{GE}		± 20	V	
RMS isolation voltage	V _{ISOL}	Any terminal to case, t = 1 minute	2500		
Maximum power dissipation	В	T _C = 25 °C	195	w	
per single IGBT	P _D	T _C = 100 °C	78		



ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V _{(BR)CES}	$V_{GE} = 0 \text{ V}, I_{C} = 250 \mu\text{A}$	600	-	-	٧
Temperature coefficient of breakdown voltage	$\Delta V_{(BR)CES}/\Delta T_J$	V_{GE} = 0 V, I_{C} = 4 mA (25 °C to 125 °C)	-	+0.6	-	V/°C
	V _{CE(on)}	V _{GE} = 15 V, I _C = 25 A	-	2.22	3.14	V
Collector to excitter acturation values		$V_{GE} = 15 \text{ V}, I_{C} = 50 \text{ A}$	-	2.43	3.25	
Collector to emitter saturation voltage		V _{GE} = 15 V, I _C = 25 A, T _J = 150 °C	-	1.65	1.93	
		V_{GE} = 15 V, I_{C} = 50 A, T_{J} = 150 °C	-	2.08	2.45	
Gate threshold voltage	V _{GE(th)}	$V_{CE} = V_{GE}$, $I_C = 250 \mu A$	3	-	6	
Temperature coefficient of threshold voltage	$\Delta V_{GE(th)}/\Delta T_{J}$	$V_{CE} = V_{GE}$, $I_C = 250 \mu\text{A}$ (25 °C to 125 °C)	-	- 17	-	mV/°C
Transconductance	g _{fe}	V_{CE} = 100 V, I_C = 25 A, PW = 80 μ s	-	43	-	S
7	I _{CES} (1)	$V_{GE} = 0 \text{ V}, V_{CE} = 600 \text{ V}, T_{J} = 25 ^{\circ}\text{C}$	-	-	250	μA
Zero gate voltage collector current		V_{GE} = 0 V, V_{CE} = 600 V, T_J = 150 °C	-	-	10	mA
Gate to emitter leakage current	I _{GES}	V _{GE} = ± 20 V	-	-	± 250	nA
Diode forward voltage drop	V _{FM}	I _C = 25 A	-	1.36	1.64	V
		I _C = 50 A	=	1.57	1.93	
		I _C = 25 A; T _J = 150 °C	-	1.19	1.42	
		I _C = 50 A; T _J = 150 °C	=	1.48	1.80	

Note

⁽¹⁾ I_{CES} includes also opposite leg overall leakage

SWITCHING CHARACTERISTICS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Total gate charge (turn-on)	Qg	I _C = 25 A	-	175	263	
Gate to emitter charge (turn-on)	Q _{ge}	V _{CC} = 480 V	-	27	41	nC
Gate to collector charge (turn-on)	Q _{gc}	V _{GE} = 15 V	-	71	107	
Turn-on switching loss	E _{on}	$R_a = 5 \Omega, I_C = 25 A$	-	0.13	0.20	· mJ
Turn-off switching loss	E _{off}	V _{CC} = 480 V	-	0.42	0.62	
Total switching loss	E _{tot}	$V_{GE} = \pm 15 \text{ V}, T_J = 25 ^{\circ}\text{C}$	-	0.55	0.82	
Turn-on switching loss	E _{on}	$R_g = 5 \Omega$, $I_C = 25 A$ $V_{CC} = 480 V$ $V_{GE} = \pm 15 V$, $T_J = 125 °C$	-	0.39	0.59	
Turn-off switching loss	E _{off}		-	0.49	0.74	
Total switching loss	E _{tot}		-	0.88	1.32	
Input capacitance	C _{ies}	V _{GE} = 0 V V _{CC} = 30 V f = 1.0 MHz	-	3610	5415	
Output capacitance	C _{oes}		-	714	1071	pF
Reverse transfer capacitance	C _{res}		-	58	87	
Diode reverse recovery time	t _{rr}	V _R = 200 V; I _C = 25 A; dI/dt = 200 A/μs	-	50	-	ns
Diode peak reverse current	I _{rr}		-	4.5	-	Α
Diode Recovery charge	Q _{rr}		-	112	-	nC
Diode peak rate of fall of recovery during t _b	dI _{(rec)M} /dt		-	250	-	A/µs

THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Operating junction temperature range	T_J		-40		150	°C
Storage temperature range	T _{Stg}	-	-40	-	125	
Junction to case	٥		-	-	0.64	
Diode	- R _{thJC}		-		0.9	°C/W
Case to sink per module	R _{thCS}	Heatsink compound thermal conductivity = 1 W/mK	-	0.06	-	
Clearance (1)		Externel shortest distance in air between 2 terminals	5.5	-	-	
Creepage (1)		Shortest distance along external surface of the insulating material between 2 terminals	8	-	-	mm
Weight				66	•	g

Note

⁽¹⁾ Standard version only i.e. without optional thermistor

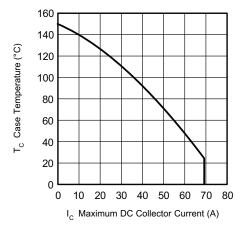


Fig. 1 - Maximum Collector Current vs. Case Temperature

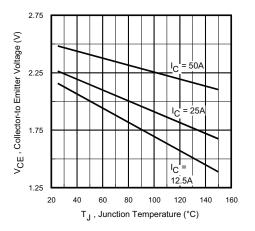


Fig. 2 - Typical Collector to Emitter Voltage vs. Junction Temperature

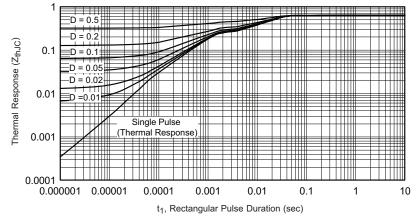


Fig. 3 - Maximum Transient Thermal Impedance, Junction to Case (IGBT)

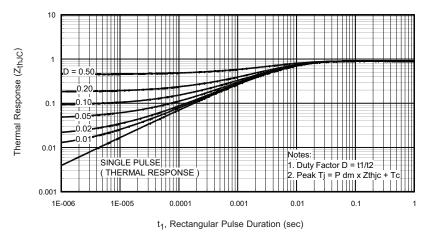


Fig. 4 - Maximum Transient Thermal Impedance, Junction to Case (Diode)

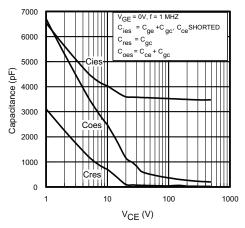


Fig. 5 - Typical Capacitance vs. Collector to Emitter Voltage

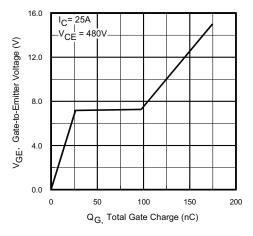


Fig. 6 - Typical Gate Charge vs. Gate to Emitter Voltage

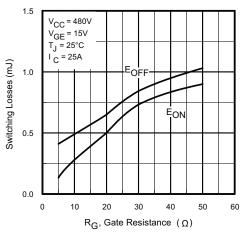


Fig. 7 - Typical Switching Losses vs. Gate Resistance

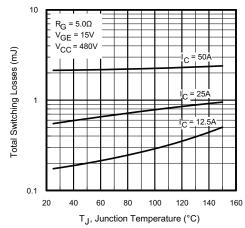


Fig. 8 - Typical Switching Losses vs. Junction Temperature



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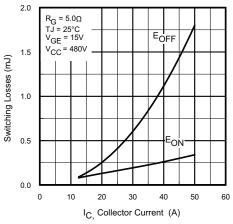
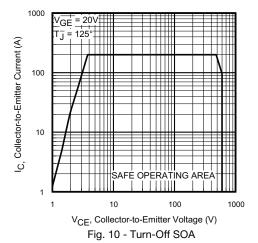


Fig. 9 - Typical Switching Losses vs. Collector to Emitter Current



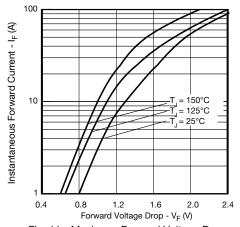


Fig. 11 - Maximum Forward Voltage Drop vs. Instantaneous Forward Current

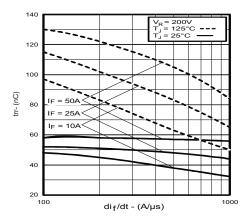


Fig. 12 - Typical Reverse Recovery Time vs. dI_F/dt

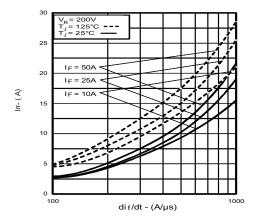


Fig. 13 - Typical Reverse Recovery Current vs. dI_F/dt

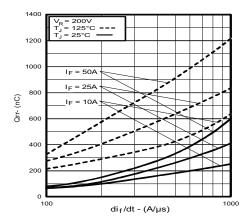


Fig. 14 - Typical Stored Charge vs. dl_F/dt

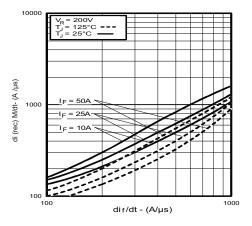


Fig. 15 - Typical $dI_{(rec)M}/dt$ vs. dI_F/dt

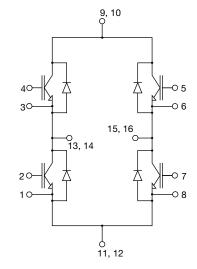
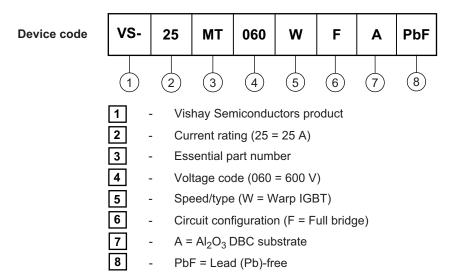


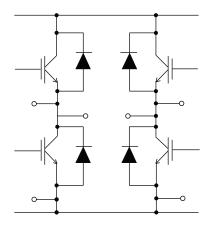
Fig. 16 - Electrical diagram

ORDERING INFORMATION TABLE





CIRCUIT CONFIGURATION

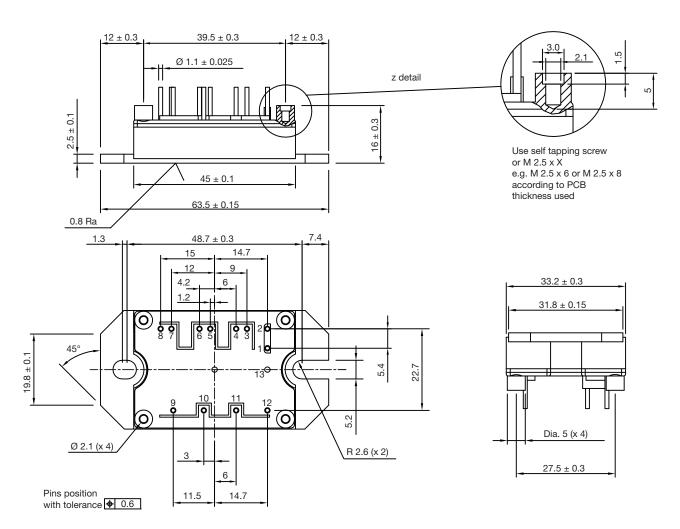


LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95245			



MTP

DIMENSIONS in millimeters



Note

· Unused terminals are not assembled in the package



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