

**iscN-Channel MOSFET Transistor**

**TK18A50D, ITK18A50D**

**• FEATURES**

- Low drain-source on-resistance:  
 $R_{DS(on)} = 0.22\Omega$  (typ.)
- Enhancement mode:  
 $V_{th} = 2.0$  to  $4.0V$  ( $V_{DS} = 10V, I_D = 1.0mA$ )
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

**• DESCRIPTION**

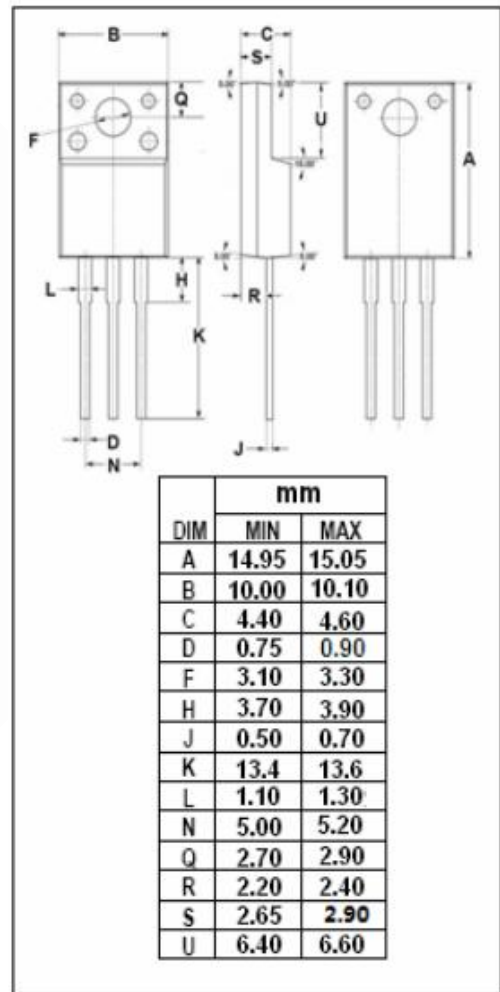
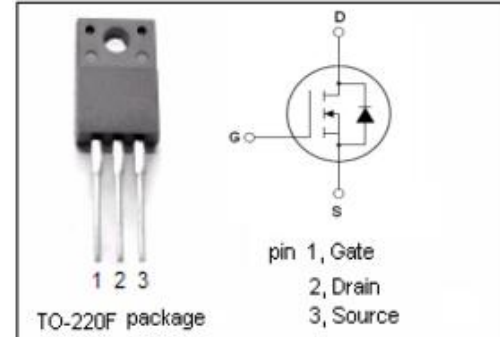
- Switching Voltage Regulators

**• ABSOLUTE MAXIMUM RATINGS( $T_a = 25^\circ C$ )**

SYMBOL	PARAMETER	VALUE	UNIT
$V_{DSS}$	Drain-Source Voltage	500	V
$V_{GS}$	Gate-Source Voltage	$\pm 30$	V
$I_D$	Drain Current-Continuous	18	A
$I_{DM}$	Drain Current-Single Pulsed	72	A
$P_D$	Total Dissipation @ $T_c = 25^\circ C$	50	W
$T_j$	Max. Operating Junction Temperature	150	$^\circ C$
$T_{stg}$	Storage Temperature	-55~150	$^\circ C$

**• THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	2.5	$^\circ C/W$
$R_{th(ch-a)}$	Channel-to-ambient thermal resistance	62.5	$^\circ C/W$



**iscN-Channel MOSFET Transistor**

**TK18A50D, ITK18A50D**

**ELECTRICAL CHARACTERISTICS**

T<sub>C</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V; I <sub>D</sub> = 10mA	500			V
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = 10V; I <sub>D</sub> =1.0mA	2.0		4.0	V
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> =10V; I <sub>D</sub> =9A		220	270	mΩ
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> = ±30V; V <sub>DS</sub> = 0V			±1	μA
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =500V; V <sub>GS</sub> = 0V			10	μA
V <sub>SDF</sub>	Diode forward voltage	I <sub>DR</sub> =18A, V <sub>GS</sub> = 0 V			1.7	V