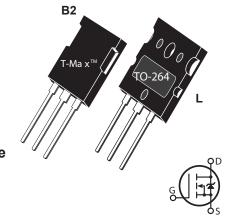
500V 58A 0.090Ω

# LINEAR MOSFET

Linear Mosfets are optimized for applications operating in the Linear region where concurrent high voltage and high current can occur at near DC conditions (>100 msec).

- Higher FBSOA
- Popular T-MAX™ or TO-264 Package
- Higher Power Dissipation
- RoHS Compliant



# **MAXIMUM RATINGS**

All Ratings:  $T_C = 25^{\circ}C$  unless otherwise specified.

Symbol	Parameter	APL502B2_L(G)	UNIT	
V <sub>DSS</sub>	Drain-Source Voltage	500	Volts	
I <sub>D</sub>	Continuous Drain Current @ T <sub>C</sub> = 25°C	58	Amps	
I <sub>DM</sub>	Pulsed Drain Current <sup>①</sup>	232		
$V_{GS}$	Gate-Source Voltage Continuous	±30	Volts	
V <sub>GSM</sub>	Gate-Source Voltage Transient	±40		
D	Total Power Dissipation @ T <sub>C</sub> = 25°C	730	Watts	
$P_{D}$	Linear Derating Factor	5.84	W/°C	
$T_J$ , $T_{STG}$	Operating and Storage Junction Temperature Range	-55 to 150	°C	
T <sub>L</sub>	Lead Temperature: 0.063" from Case for 10 Sec.	300	]	
I <sub>AR</sub>	Avalanche Current <sup>①</sup> (Repetitive and Non-Repetitive)	58	Amps	
E <sub>AR</sub>	Repetitive Avalanche Energy 1	50	mJ	
E <sub>AS</sub>	Single Pulse Avalanche Energy <sup>(4)</sup>	3000	1113	

### STATIC ELECTRICAL CHARACTERISTICS

Symbol	Characteristic / Test Conditions / Part Number	MIN	TYP	MAX	UNIT
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage ( $V_{GS}$ = 0V, $I_D$ = 250 $\mu$ A)	500			Volts
I <sub>D</sub> (ON)	On State Drain Current $^{\textcircled{2}}(V_{DS} > I_{D}(ON) \times R_{DS}(ON) \text{ Max, } V_{GS} = 15V)$	58			Amps
R <sub>DS</sub> (ON)	Drain-Source On-State Resistance <sup>②</sup> (V <sub>GS</sub> = 15V, 29A)			0.09	Ohms
I <sub>DSS</sub>	Zero Gate Voltage Drain Current $(V_{DS} = 500V, V_{GS} = 0V)$			25	μА
	Zero Gate Voltage Drain Current ( $V_{DS} = 400V$ , $V_{GS} = 0V$ , $T_{C} = 125$ °C)			250	
I <sub>GSS</sub>	Gate-Source Leakage Current (V <sub>GS</sub> = ±30V, V <sub>DS</sub> = 0V)			±100	nA
V <sub>GS</sub> (TH)	Gate Threshold Voltage $(V_{DS} = V_{GS}, I_{D} = 2.5 \text{mA})$	2		4	Volts

CAUTION: These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

## **DYNAMIC CHARACTERISTICS**

Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0V		7485	9000	
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> = 25V		1290	1810	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	f = 1 MHz		617	930	
t <sub>d</sub> (on)	Turn-on Delay Time	V <sub>GS</sub> = 15V		13	26	
t <sub>r</sub>	Rise Time	V <sub>DD</sub> = 250V		27	54	ns
t <sub>d</sub> (off)	Turn-off Delay Time	I <sub>D</sub> = 29A @ 25°C		56	84	
t <sub>f</sub>	Fall Time	$R_G = 0.6\Omega$		16	20	

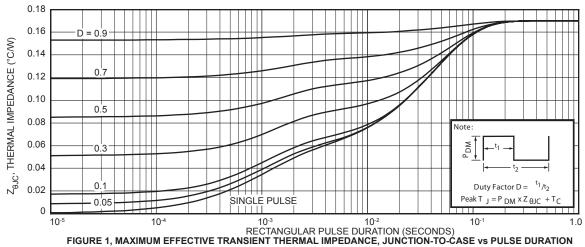
# THERMAL CHARACTERISTICS

Symbol	Characteristic	MIN	TYP	MAX	UNIT
R <sub>eJC</sub>	Junction to Case			.17	°C/W
W <sub>T</sub>	Package Weight		0.22		OZ
			5.9		g

 $<sup>^{\</sup>scriptsize \textcircled{\scriptsize 1}}$  Repetitive Rating: Pulse width limited by maximum junction

 $\stackrel{\textcircled{3}}{=}$  See MIL-STD-750 Method 3471  $\stackrel{\textcircled{4}}{=}$  Starting T  $_{\rm J}$  = +25°C, L = 1.78mH, R  $_{\rm G}$  = 25 $\Omega$ , Peak I  $_{\rm L}$  = 58A

② Pulse Test: Pulse width < 380 μS, Duty Cycle < 2% Microsemi reserves the right to change, without notice, the specifications and information contained herein.



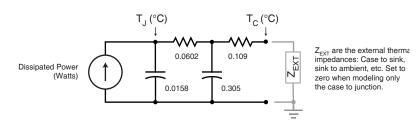
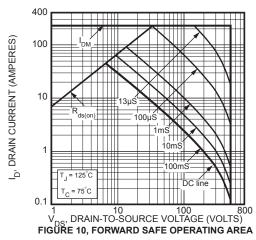
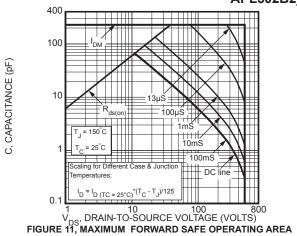
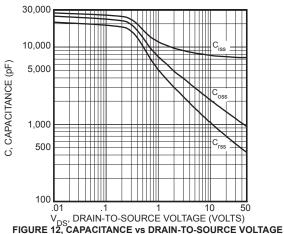
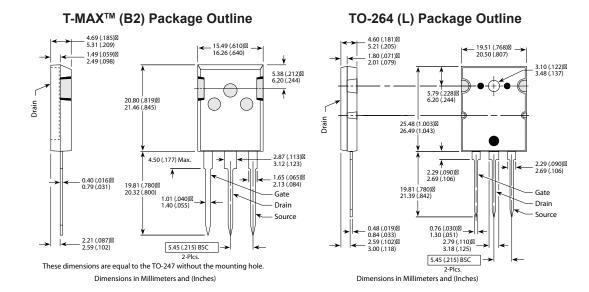


FIGURE 1a, TRANSIENT THERMAL IMPEDANCE MODEL









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