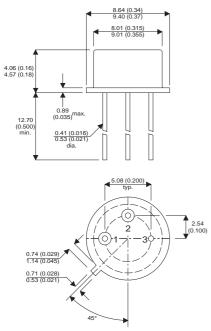




### **MECHANICAL DATA**

Dimensions in mm (inches)



### TO-39 PACKAGE (TO-205AF)

#### **Underside View**

PIN 2 - Gate PIN 1 - Source PIN 3 - Drain

# **TMOS FET ENHANCEMENT N-CHANNEL**

### **FEATURES**

- V<sub>(BR)DSS</sub> = 100V
- I<sub>D</sub> = 8A
- $R_{DSON} = 0.18\Omega$

## **ABSOLUTE MAXIMUM RATINGS** (T<sub>case</sub> = 25°C unless otherwise stated)

$\overline{V_{DS}}$	Drain-Source Voltage	100V				
$V_{DGR}$	Drain–Source Voltage ( $R_{GS} = 1.0 \text{ m}\Omega$ )	100V				
$V_{GS}$	Gate-Source Voltage	±20V				
$I_{D}$	Drain Current Continuous T <sub>Case</sub> = 25°C	8.0A				
$I_{DM}$	Drain Current Pulsed	32A				
$P_{D}$	Total Device Dissipation @ T <sub>Case</sub> = 25°C	25W				
	Derate above 25°C	0.2W/°C				
$T_J$ , $T_STG$	Operating and Storage Junction Temperature Range	−55 to +150°C				
THERMAL CHARACTERISTICS						
$R_{ heta JC}$	Thermal Resistance Junction to Case	5.0°C/W				
$R_{ hetaJA}$	Thermal Resistance Junction to Ambient	175°C/W				
T <sub>L</sub>	Maximum Lead Temperature 1.6mm from Case for 10 secs.	300°C				

Semelab PIc reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by Semelab is believed to be both accurate and reliable at the time of going to press. However Semelab assumes no responsibility for any errors or omissions discovered in its use. Semelab encourages customers to verify that datasheets are current before placing orders.

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## **ELECTRICAL CHARACTERISTICS** ( $T_J = 25$ °C unless otherwise stated)

	Parameter	Test Co	Test Conditions		Тур.	Max.	Unit
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0	$I_D = 0.25 \text{mA}$	100			V
V <sub>GS(th)</sub>	Gate Thresshold Voltage <sup>1</sup>	V <sub>DS</sub> =V <sub>GS</sub>	$I_D = 0.5 \text{mA}$	2.0		4.0	V
I <sub>GSS</sub>	Gate-Body Leakage	$V_{DS} = 0$	$V_{GS} = \pm 20V$			±100	nA
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = Rated	V <sub>DS</sub> = Rated V <sub>DSS</sub>			250	
		$V_{GS} = 0V$	T <sub>j</sub> = 125°C			1000	μΑ
r <sub>DS(on)</sub>	Drain–Source On–Resistance <sup>1</sup>	V <sub>GS</sub> = 10V	I <sub>D</sub> = 5.0A			0.18	Ω
			$T_A = 125$ °C			0.35	
V <sub>DS(on)</sub>	Drain-Source On-Voltage <sup>1</sup>	V <sub>GS</sub> = 10V	I <sub>D</sub> = 8.0A			1.56	V
gf <sub>S</sub>	Forward Transconductance <sup>1</sup>	V <sub>GS</sub> = 15V	I <sub>D</sub> = 5.0A	3.0		9.0	s(Ω)
C <sub>iss</sub>	Input Capacitance	\/ - 25\/	V 25V V 0	350		900	
C <sub>oss</sub>	Output capacitance		$V_{DS} = 25V$ $V_{GS} = 0$ $f = 1.0MH_Z$			500	pF
C <sub>rss</sub>	Reverse Transfer Capacitance					150	
t <sub>d(on)</sub>	Turn-On Delay Time <sup>1</sup>	$V_{DD} = 30V$	$R_{GEN} = 50\Omega$			30	
t <sub>r</sub>	RiseTime <sup>1</sup>	I <sub>D</sub> = 5.0A	I <sub>D</sub> = 5.0A			75	ns
t <sub>d(off)</sub>	Turn off Delay Time <sup>1</sup>	$R_G = 7.5$ ohms				40	
t <sub>f</sub>	FallTime <sup>1</sup>					45	
	SOURCE DRAIN DIODE RATING	CHARACTE	RISTICS				
V <sub>SD</sub>	Diode Forward Voltage <sup>1</sup>	I <sub>S</sub> = Rated I <sub>E</sub>	I <sub>S</sub> = Rated I <sub>D(on)</sub>			5.5	V
t <sub>on</sub>	Forward Turn OnTime <sup>1</sup>	V <sub>GS</sub> = 0				Negligible	
t <sub>rr</sub>	Reverse Recovery Time <sup>1</sup>					300	ns

1) Pulse test : Pulse Width <  $300\mu s$  ,Duty Cycle < 2%

2) Pulse width limited by maximum juction temperature

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