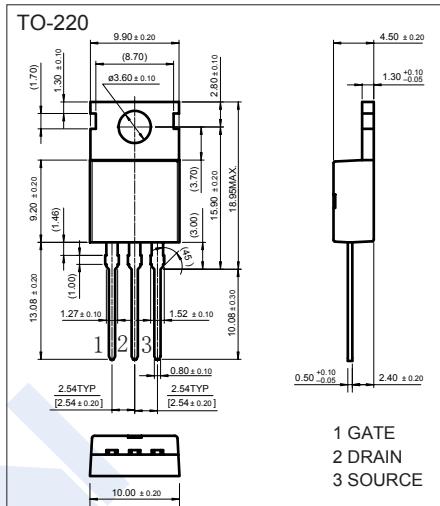
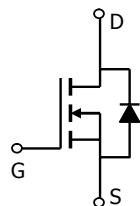


N-Channel MOSFET

KX120N06

■ Features

- $V_{DS} (V) = 60V$
- $I_D = 100 A (V_{GS} = 10V)$
- $R_{DS(ON)} < 6.5m \Omega (V_{GS} = 10V)$
- Special process technology for high ESD capability
- Fully characterized Avalanche voltage and current



■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current	I_D	100	A
$T_a=25^\circ C$		70	
Pulsed Drain Current	I_{DM}	320	W
Power Dissipation	P_D	150	
Derating factor		1.07	$W/^\circ C$
Single pulse avalanche energy	E_{AS}	550	mJ
Thermal Resistance.Junction- to-Case	R_{thJC}	0.94	$^\circ C/W$
Junction Temperature	T_J	175	$^\circ C$
Storage Temperature Range	T_{stg}	-55 to 175	

N-Channel MOSFET

KX120N06

■ Electrical Characteristics $T_a = 25^\circ\text{C}$

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V_{DSS}	$I_D=250 \mu\text{A}, V_{GS}=0\text{V}$	60	65		V
Zero Gate Voltage Drain Current	$I_{DS(on)}$	$V_{DS}=60\text{V}, V_{GS}=0\text{V}$			1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{DS}=0\text{V}, V_{GS}=\pm 20\text{V}$			± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250 \mu\text{A}$	2	3	4	V
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10\text{V}, I_D=40\text{A}$		5.7	6.5	$\text{m}\Omega$
Forward Transconductance	g_{FS}	$V_{DS}=5\text{V}, I_D=40\text{A}$	60			S
Input Capacitance	C_{iss}	$V_{GS}=0\text{V}, V_{DS}=30\text{V}, f=1\text{MHz}$		4800		pF
Output Capacitance	C_{oss}			440		
Reverse Transfer Capacitance	C_{rss}			260		
Total Gate Charge	Q_g	$V_{GS}=10\text{V}, V_{DS}=30\text{V}, I_D=30\text{A}$		85		nC
Gate Source Charge	Q_{gs}			18		
Gate Drain Charge	Q_{gd}			28		
Turn-On Delay Time	$t_{d(on)}$	$V_{GS}=10\text{V}, V_{DS}=30\text{V}, I_D=1\text{A}, R_G=2.5\Omega$		16.8		ns
Turn-On Rise Time	t_r			10.8		
Turn-Off Delay Time	$t_{d(off)}$			55		
Turn-Off Fall Time	t_f			13.6		
Body Diode Reverse Recovery Time	t_{rr}	$I_F= 40\text{A}, dI/dt= 100\text{A}/\mu\text{s}, T_J = 25^\circ\text{C}$ (Note.1)		38		
Body Diode Reverse Recovery Charge	Q_{rr}			53		nC
Maximum Body-Diode Continuous Current	I_S				90	A
Diode Forward Voltage	V_{SD}	$I_S=20\text{A}, V_{GS}=0\text{V}$			1.2	V

Note.1: Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.

N-Channel MOSFET

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■ Typical Characteristics

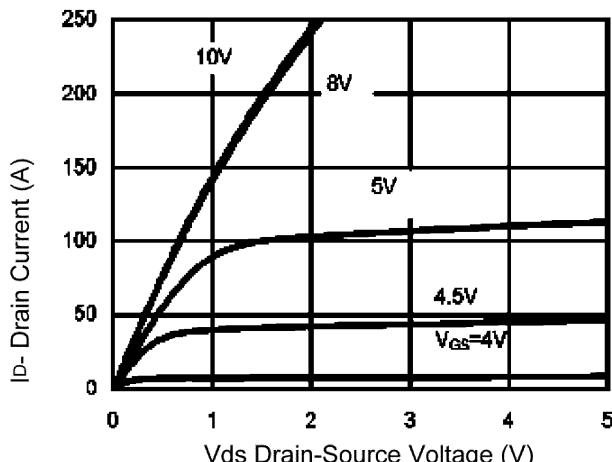


Figure 1 Output Characteristics

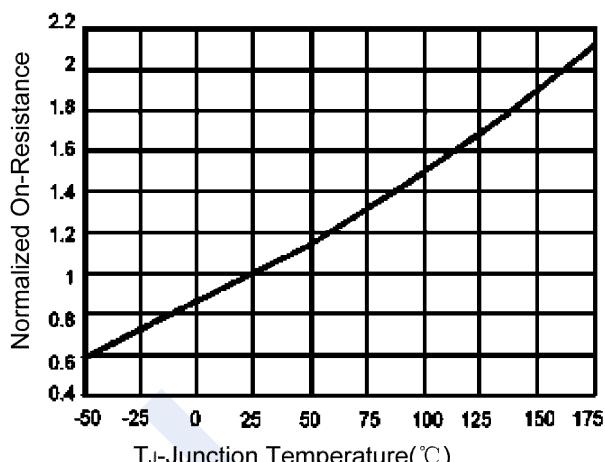


Figure 4 $R_{DS(on)}$ -Junction Temperature

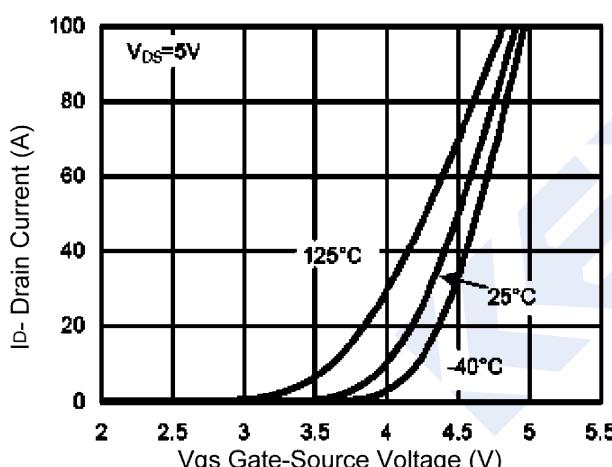


Figure 2 Transfer Characteristics

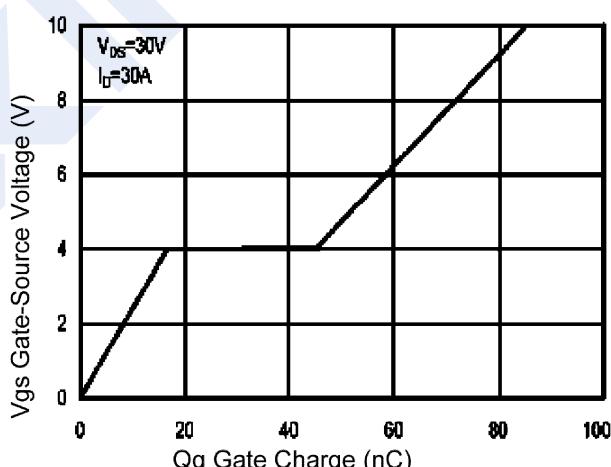


Figure 5 Gate Charge

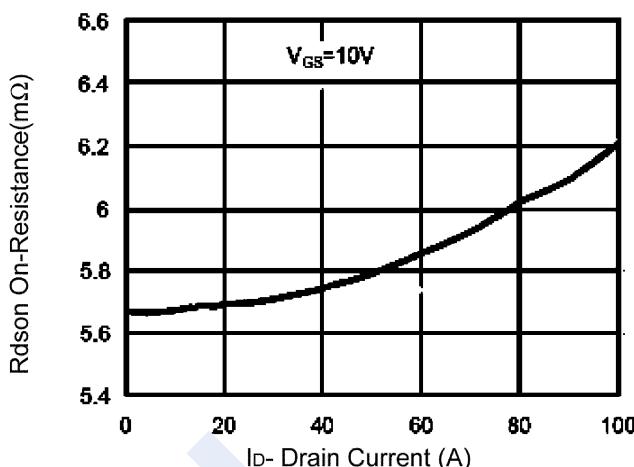


Figure 3 $R_{DS(on)}$ -Drain Current

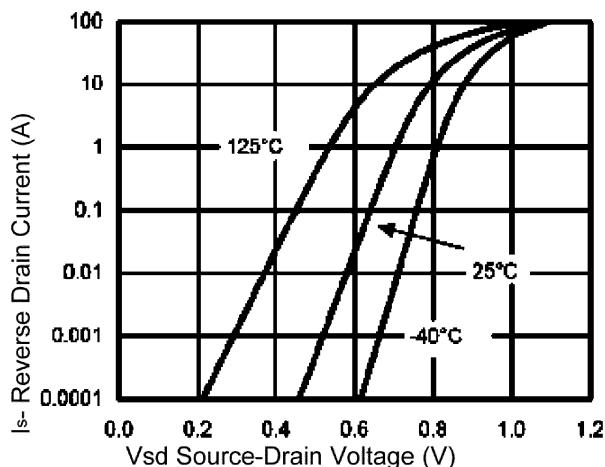


Figure 6 Source-Drain Diode Forward

N-Channel MOSFET

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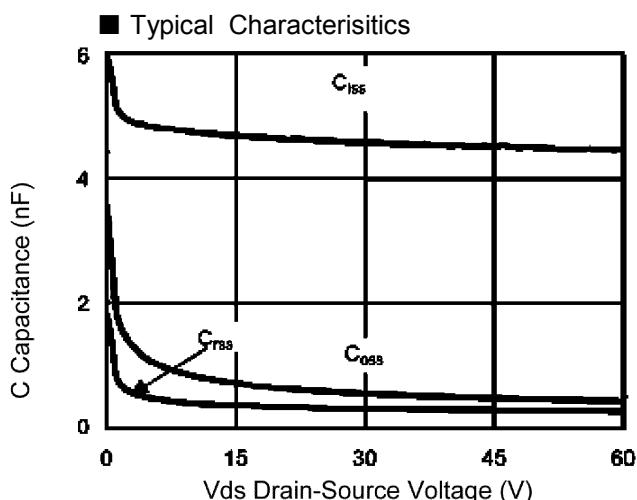


Figure 7 Capacitance vs Vds

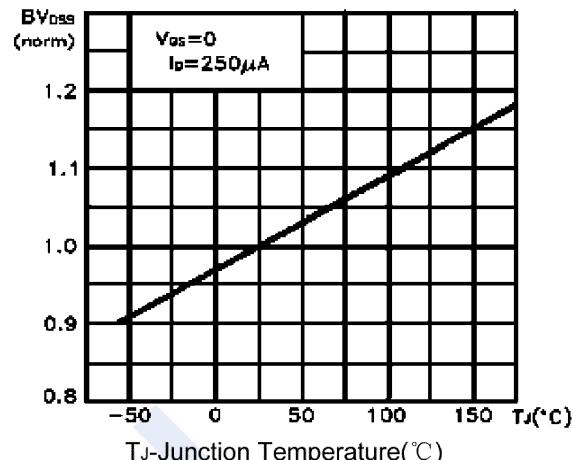
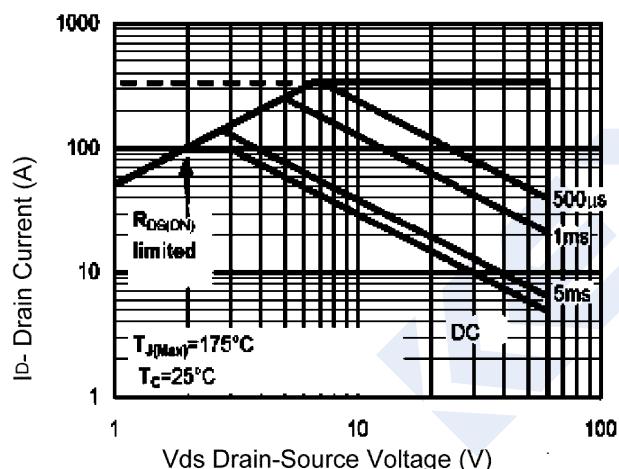
Figure 9 BV_{dss} vs Junction Temperature

Figure 8 Safe Operation Area

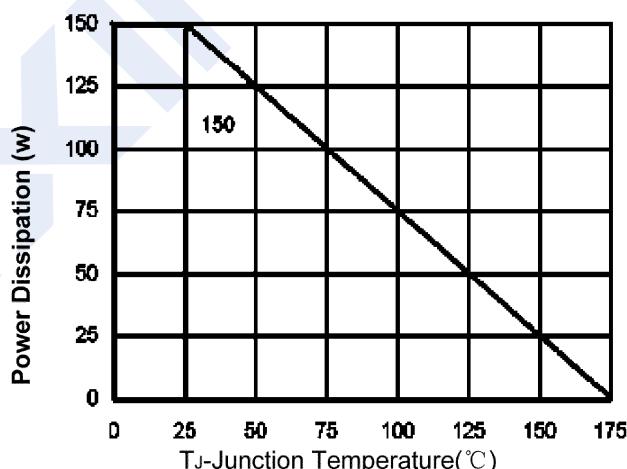


Figure 10 Ausemi De-rating

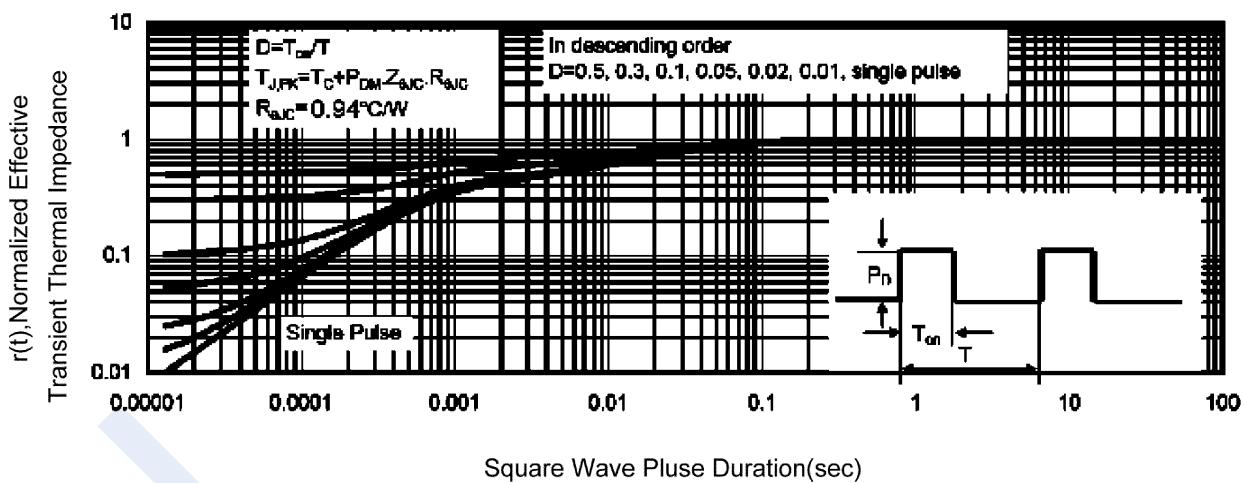


Figure 11 Normalized Maximum Transient Thermal Impedance