

isc N-Channel MOSFET Transistor

FDD3682

• FEATURES

- Static drain-source on-resistance:
 $R_{DS(on)} \leq 36m\Omega$
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

• DESCRIPTION

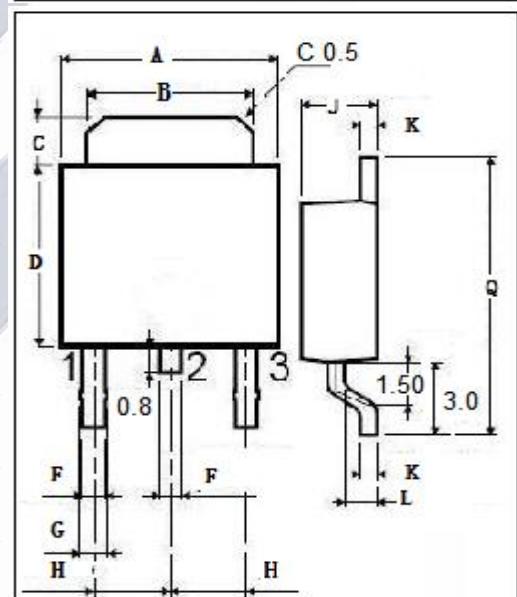
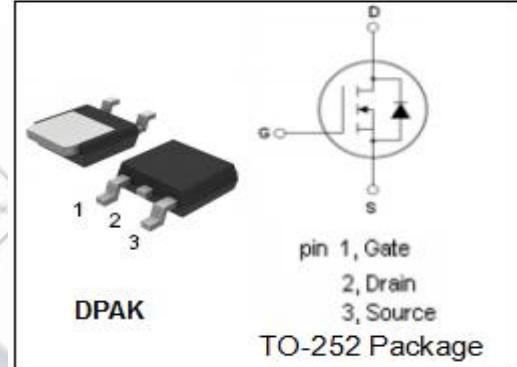
- DC-DC Converters and off-line UPS
- High Voltage Synchronous Rectifier

• ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{DSS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current-Continuous	32	A
I_{DM}	Drain Current-Single Pulsed	24	A
P_D	Total Dissipation @ $T_c=25^\circ C$	95	W
T_j	Max. Operating Junction Temperature	175	°C
T_{stg}	Storage Temperature	-55~175	°C

• THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(j-c)}$	Channel-to-case thermal resistance	1.58	°C/W



DIM	mm	
	MIN	MAX
A	6.40	6.60
B	5.20	5.40
C	1.15	1.35
D	5.70	6.10
E	0.65	
F	0.75	
H	2.10	2.50
J	2.10	2.40
K	0.40	0.60
L	0.90	1.10
M	1.50	
N	3.0	

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ELECTRICAL CHARACTERISTICS
 $T_c=25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}; \text{I}_D=250 \mu\text{A}$	100			V
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}; \text{I}_D=250 \mu\text{A}$	2		4	V
$\text{R}_{\text{DS(on)}}$	Drain-Source On-Resistance	$\text{V}_{\text{GS}}=10\text{V}; \text{I}_D=32\text{A}$			36	$\text{m}\Omega$
I_{GSS}	Gate-Source Leakage Current	$\text{V}_{\text{GS}}= \pm 20\text{V}$			± 100	nA
I_{DSS}	Drain-Source Leakage Current	$\text{V}_{\text{DS}}=80\text{V}; \text{V}_{\text{GS}}= 0\text{V}$			1	μA
		$\text{V}_{\text{DS}}=80\text{V}; \text{V}_{\text{GS}}= 0\text{V}; \text{T}_c=150^\circ\text{C}$			250	μA
V_{SD}	Diode forward voltage	$\text{I}_{\text{SD}}=32\text{A}, \text{V}_{\text{GS}} = 0\text{V}$			1.25	V