



CTH6106PS-T52

P-Channel Enhancement MOSFET

Features

- Drain-Source Breakdown Voltage V_{DSS} -60 V
- Drain-Source On-Resistance
 - $R_{DS(ON)}$ 14mΩ, at $V_{GS} = -10V$, $I_{DS} = -17A$
 - $R_{DS(ON)}$ 16mΩ, at $V_{GS} = -4.5V$, $I_{DS} = -14A$
- Continuous Drain Current at $T_c=25^\circ C$ $I_D = -61A$
- Advanced high cell density Trench Technology
- RoHS Compliance & Halogen Free

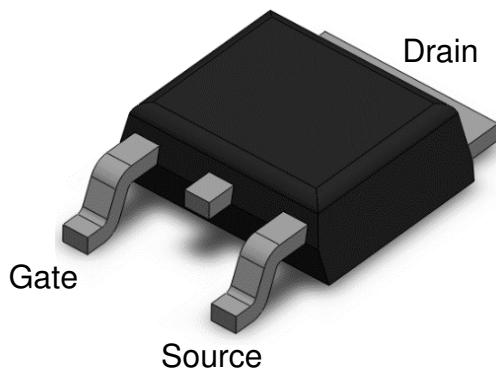
Description

The CTH6106PS-T52 uses high performance Trench Technology to provide excellent $R_{DS(ON)}$ and low gate charge which is suitable for most of the synchronous buck converter applications .

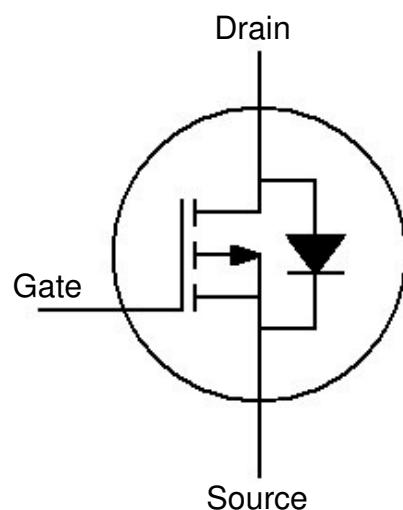
Applications

- Load Switch
- Power Management
- LCD Display inverter
- DC/DC Converter

Package Outline



Schematic





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Absolute Maximum Rating at 25°C

Symbol	Parameters	Ratings	Units	Notes
V_{DS}	Drain-Source Voltage	-60	V	
V_{GS}	Gate-Source Voltage	± 20	V	
I_D	Continuous Drain Current @ $T_c=25^\circ\text{C}$	-61	A	1
I_{DM}	Pulsed Drain Current	-240	A	1
P_D	Total Power Dissipation @ $T_c=25^\circ\text{C}$	114	W	2
T_{STG}	Storage Temperature Range	-55 to 150	°C	
T_J	Operating Junction Temperature Range	-55 to 150	°C	

Thermal Characteristics

Symbol	Parameters	Test Conditions	Min	Typ	Max	Units	Notes
$R_{\Theta JC}$	Thermal Resistance Junction-Case		-	-	1.1	C/W	1,2



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Electrical Characteristics $T_A = 25^\circ\text{C}$ (unless otherwise specified)

Static Characteristics

Symbol	Parameters	Test Conditions	Min	Typ	Max	Units	Notes
B _{VDSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D = -250μA	-60	-	-	V	
I _{DS}	Drain-Source Leakage Current	V _{DS} = -60V, V _{GS} = 0V	-	-	-1	μA	
I _{GSS}	Gate-Source Leakage Current	V _{GS} = ±20V, V _{DS} = 0V	-	-	±100	nA	

On Characteristics

Symbol	Parameters	Test Conditions	Min	Typ	Max	Units	Notes
R _{DSON}	Drain-Source On-Resistance	V _{GS} = -10V, I _D = -17A	-	14	17	m	
		V _{GS} = -4.5V, I _D = -14A	-	16	20	m	
V _{GS(TH)}	Gate-Source Threshold Voltage	V _{DS} =V _{GS} , I _D = -250μA	-1.0	-	-3.0	V	

Dynamic Characteristics

Symbol	Parameters	Test Conditions	Min	Typ	Max	Units	Notes
C _{ISS}	Input Capacitance	V _{DS} = -15V , V _{GS} = 0V, f=1Mhz	-	4120	-	pF	
C _{OSS}	Output Capacitance		-	415	-		
C _{RSS}	Reverse Transfer Capacitance		-	140	-		

Switching Characteristics

Symbol	Parameters	Test Conditions	Min	Typ	Max	Units	Notes
T _{D(ON)}	Turn-On Delay Time	V _{DS} = -30V , V _{GS} = -10V, R _L = 30 , R _G = 6 ,	-	52	-	ns	
T _R	Rise Time		-	19	-		
T _{D(OFF)}	Turn-Off Delay Time		-	220	-		
T _F	Fall Time		-	60	-		
Q _G	Total Gate Charge	V _{DS} = -30 , V _{GS} = -4.5V, I _D = -50A	-	45	-	nC	
Q _{GS}	Gate-Source Charge		-	19	-		
Q _{GD}	Gate-Drain (Miller) Charge		-	25	-		



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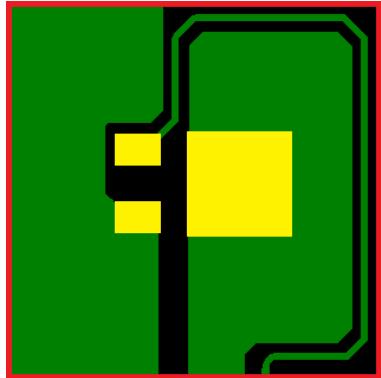
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Drain-Source Diode Characteristics

Symbol	Parameters	Test Conditions	Min	Typ	Max	Units	Notes
V _{SD}	Body Diode Forward Voltage	V _{GS} = 0V, I _D = -2A	-	-0.9	-1.2	V	
I _{SD}	Body Diode Continuous Current		-	-	-2	A	1

Note:

1. The power dissipation is limited by 150°C junction temperature.
2. Device mounted on a glass-epoxy board



FR-4
25.4 × 25.4 mm .
2 Oz Copper

Actual Size

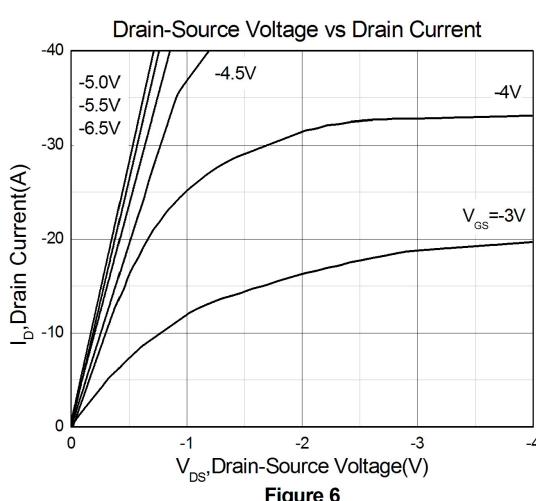
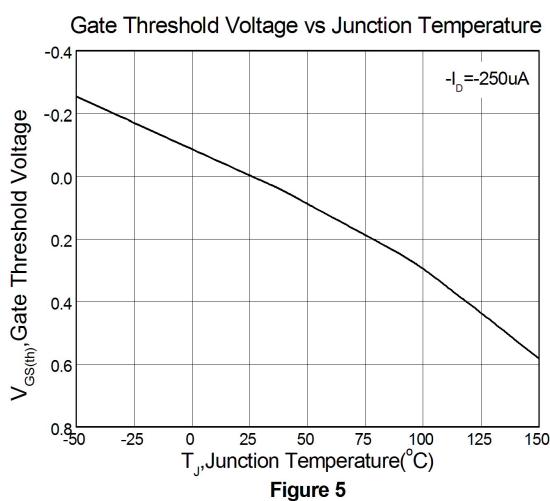
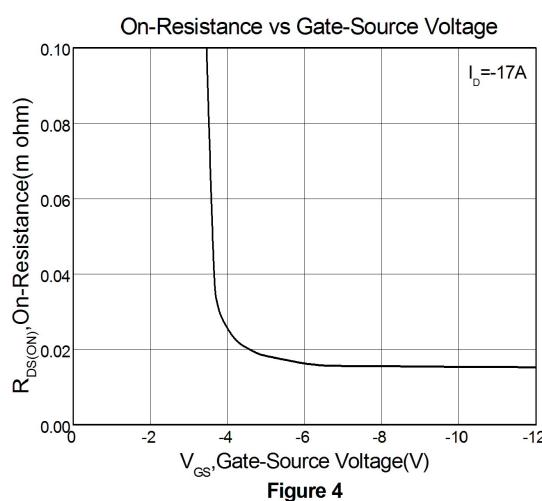
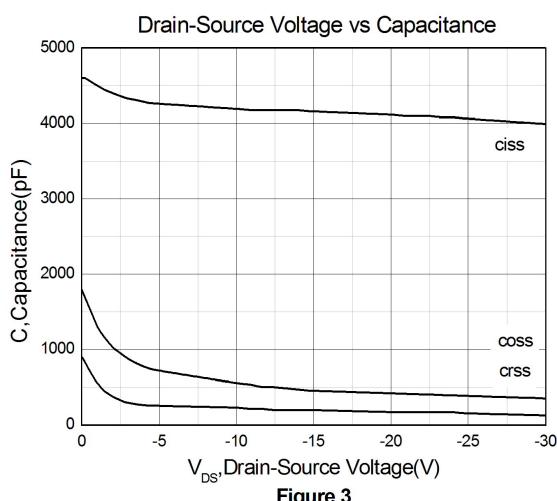
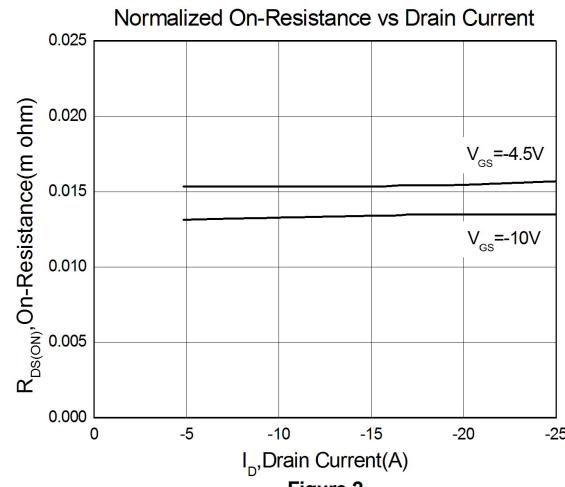
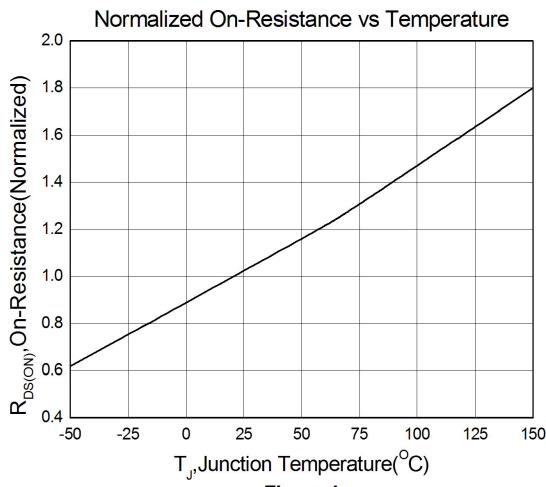
3. The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
4. Thermal Resistance follow JESD51-3.



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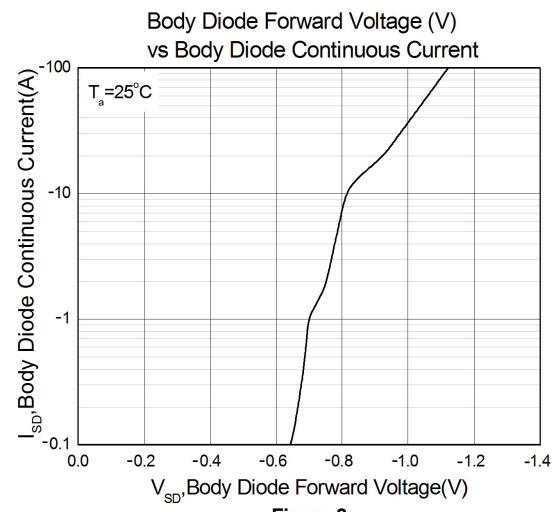
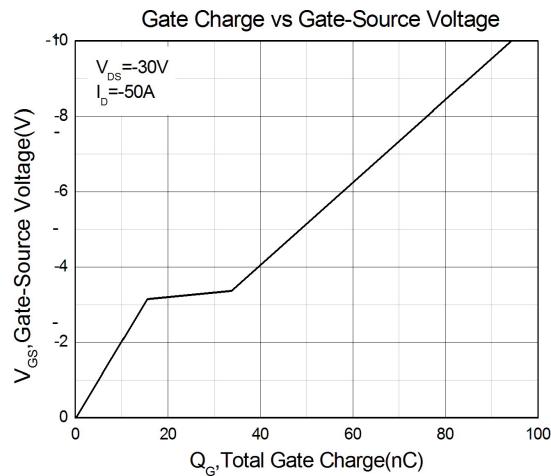
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Typical Characteristic Curves





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Test Circuits & Waveforms

Figure 9: Gate Charge Test Circuit

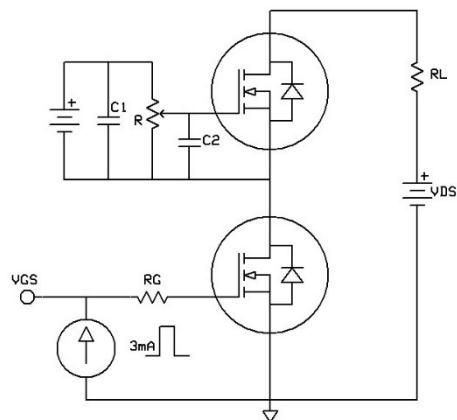


Figure 10: Gate Charge Waveform

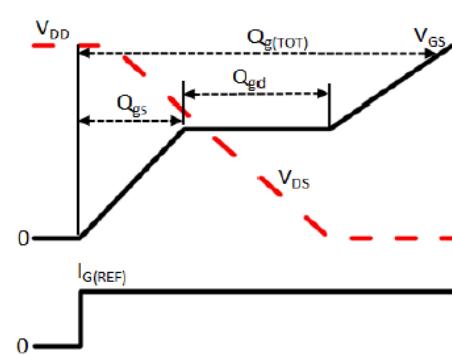


Figure 11: Switching Time Test Circuit

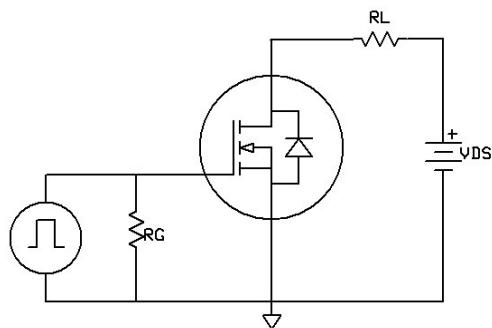
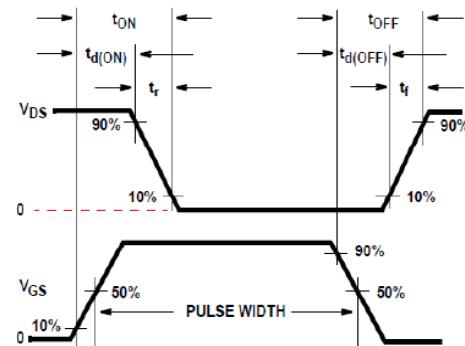


Figure 12: Switching Time Waveform

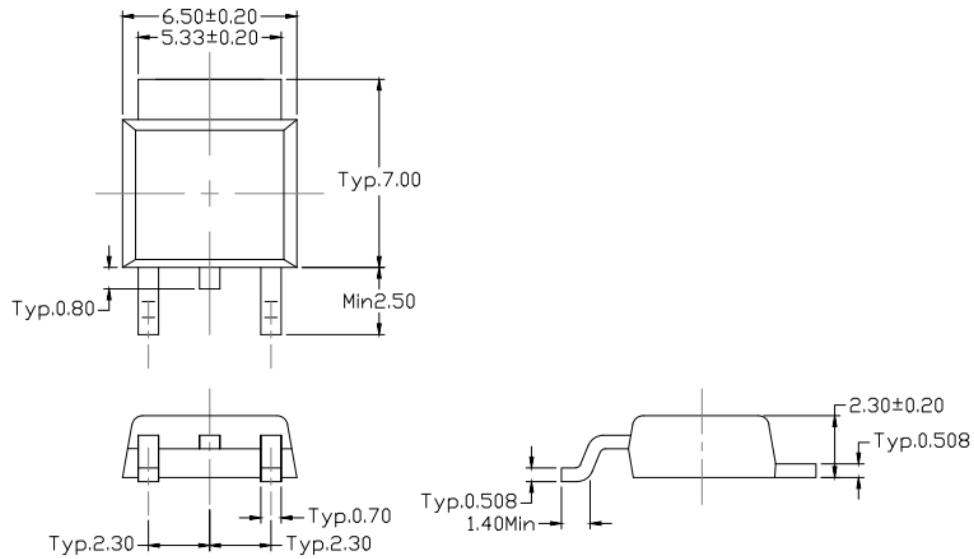




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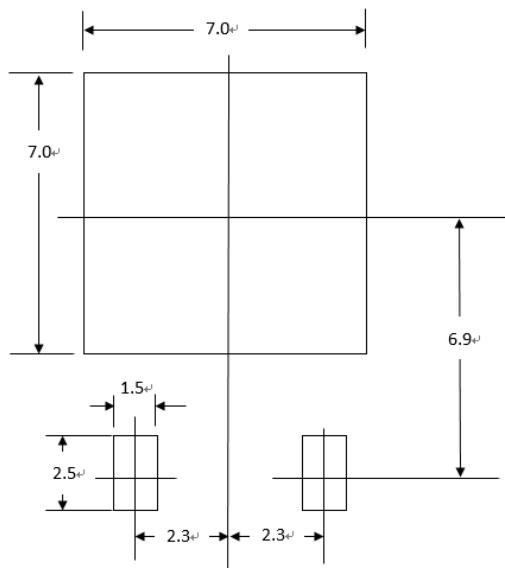
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Package Dimension (TO-252)



Dimensions in mm unless otherwise stated

Recommended pad layout for surface mount leadform



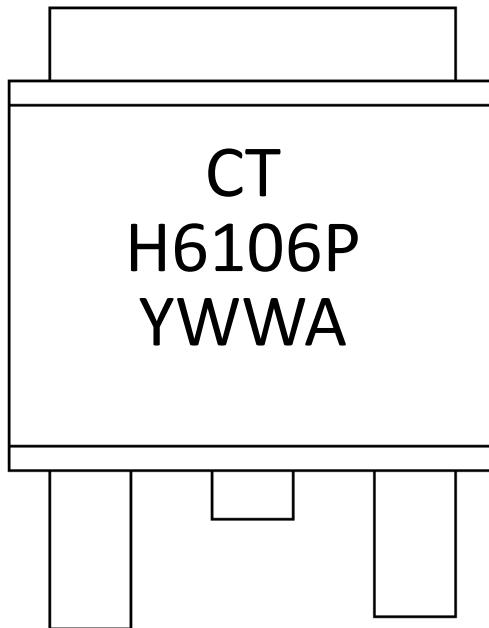
Dimensions in mm unless otherwise

stated



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Marking Information



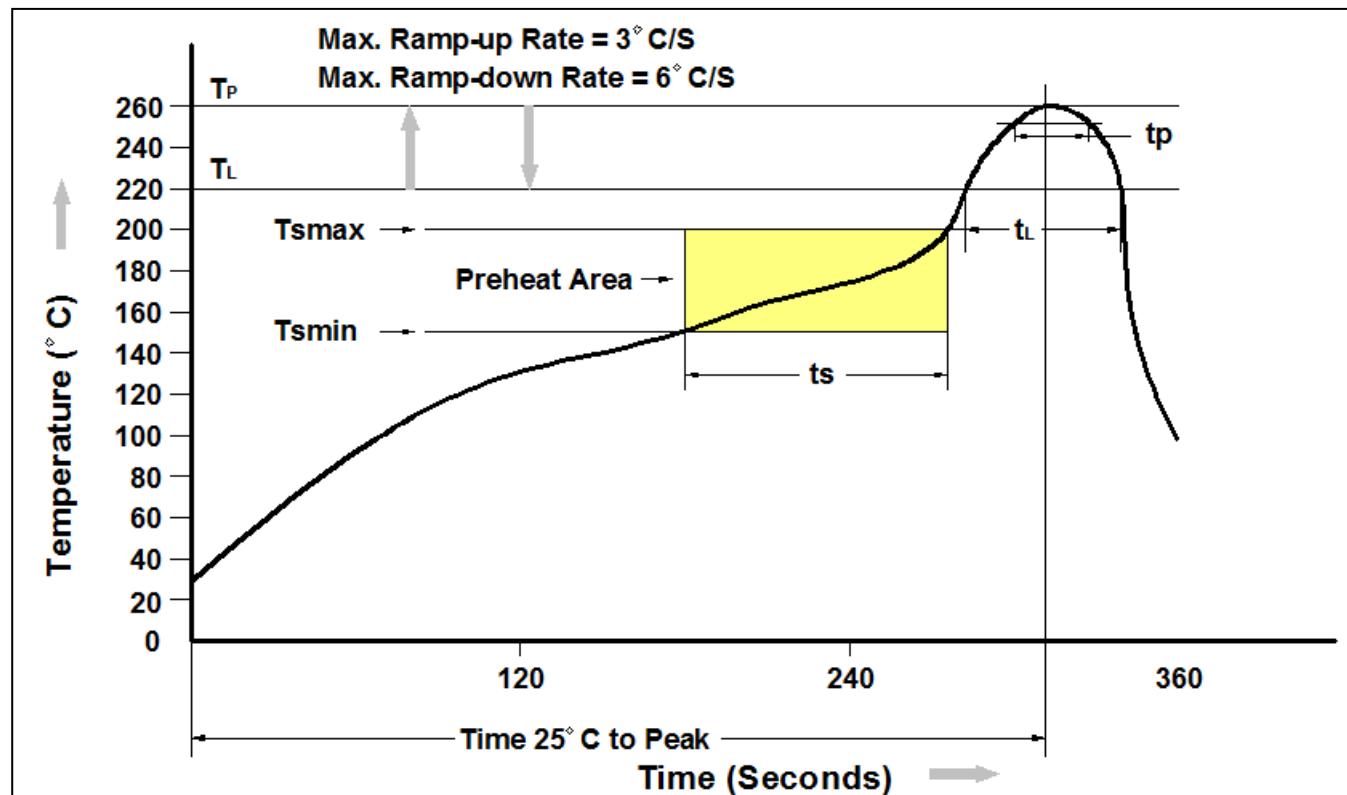
CT : Denotes "CT Micro"
H6106P : Device Number
Y : Fiscal Year
WW : Work Week
A : Production Code

Ordering Information

Part Number	Description	Quantity
CTH6106PS-T52	TO-252 Reel	2500 pcs



Reflow Profile



Profile Feature	Pb-Free Assembly Profile
Temperature Min. (T _{smin})	150°C
Temperature Max. (T _{smax})	200°C
Time (t _s) from (T _{smin} to T _{smax})	60-120 seconds
Ramp-up Rate (t _L to t _P)	3°C/second max.
Liquidous Temperature (T _L)	217°C
Time (t _L) Maintained Above (T _L)	60 – 150 seconds
Peak Body Package Temperature	260°C +0°C / -5°C
Time (t _P) within 5°C of 260°C	30 seconds
Ramp-down Rate (T _P to T _L)	6°C/second max
Time 25°C to Peak Temperature	8 minutes max.



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