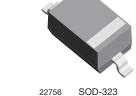


**Vishay Semiconductors** 

# Low Capacitance, Single-Line ESD-Protection Diode in SOD-323

# 



# MARKING (example only)

20503



#### XYZ = type code (see table below) bar = pin 1

### FEATURES

- For LIN-Bus applications
- Small SOD-323 package
- 1-line ESD-protection
- Working range: ± 26.5 V
- Low leakage current  $I_R < 0.05 \; \mu A$
- Low load capacitance C<sub>D</sub> < 16 pF</li>
- ESD-protection acc. IEC 61000-4-2 ± 30 kV contact discharge ± 30 kV air discharge
- ESD capability according to AEC-Q101: human body model: class H3B: > 8 kV
- e3 pins plated with tin (Sn)
- AEC-Q101 qualified available
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

ORDERING INFORMATION								
PART NUMBER (EXAMPLE)	ENVIRONMENTAL AND QUALITY CODE				PACKAG	ING CODE		
	AEC-Q101 QUALIFIED	RoHS-COMPLIANT + LEAD (Pb)-FREE TERMINATIONS		TIN PLATED	3K PER 7" REEL (8 mm TAPE)	10K PER 13" REEL (8 mm TAPE)	ORDERING CODE (EXAMPLE)	
	QUALIFIED	STANDARD	GREEN	FLATED	15K/BOX = MOQ	10K/BOX = MOQ		
VLIN2626-02G	-	E	-	3	-08	-	VLIN2626-02G-E3-08	
VLIN2626-02G	Н	E	-	3	-08	-	VLIN2626-02GHE3-08	
VLIN2626-02G	-	E	-	3	-	-18	VLIN2626-02G-E3-18	
VLIN2626-02G	Н	E	-	3	-	-18	VLIN2626-02GHE3-18	

PACKAGE DATA							
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS	
VLIN2626-02G	SOD-323	262	4.30 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	Peak temperature max. 260 °C	

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	TEST CONDITIONS	SYMBOL VALUE		UNIT			
Peak pulse current	$T_A$ = 25 °C; acc. IEC 61000-4-5; $t_p$ = 8/20 $\mu s;$ single shot	I <sub>PPM</sub>	4	А			
Peak pulse power	$T_A$ = 25 °C; acc. IEC 61000-4-5; $t_p$ = 8/20 $\mu s;$ single shot	P <sub>PP</sub>	200	W			
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses; $T_A = 25 \text{ °C}$	N/	± 30	kV			
	Air discharge acc. IEC 61000-4-2; 10 pulses; $T_A = 25 ^\circ\text{C}$	V <sub>ESD</sub>	± 30	kV			
Operating temperature	Junction temperature	TJ	-55 to +150	°C			
Storage temperature		T <sub>STG</sub>	-55 to +150	°C			





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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_{amb} = 25 \degree C$ , unless otherwise specified)								
PARAMETER	<b>TEST CONDITIONS / REMARKS</b>	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines		
Reverse stand-off voltage	Max. reverse working voltage	V <sub>RWM</sub>	-	-	26.5	V		
Reverse voltage	At I <sub>R</sub> = 0.05 μA	V <sub>R</sub>	26.5	-	-	V		
Reverse current	At V <sub>RWM</sub> = 26.5 V	I <sub>R</sub>	-	-	0.05	μA		
Reverse breakdown voltage	At I <sub>R</sub> = 1 mA	$V_{BR}$	28	30	32	V		
Reverse clamping voltage	At I <sub>PP</sub> 1 A; t <sub>p</sub> = 8/20 μs	V <sub>C</sub>	-	32	40	V		
	At $I_{PP} = I_{PPM} = 4$ A; $t_p = 8/20 \ \mu s$	V <sub>C</sub>	-	39	50	V		
Capacitance	At $V_R = 0 V$ , f = 1 MHz	CD	-	13.5	16	pF		

TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)

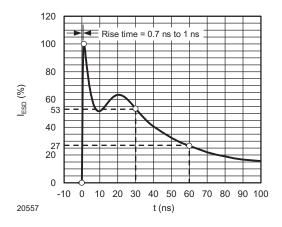


Fig. 1 - ESD Discharge Current Wave Form acc. IEC 61000-4-2 (330  $\Omega$  / 150 pF)

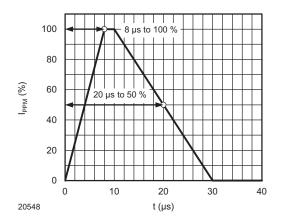


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5

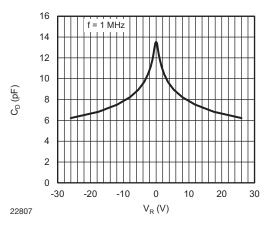


Fig. 3 - Typical Capacitance  $C_D$  vs. Reverse Voltage  $V_R$ 

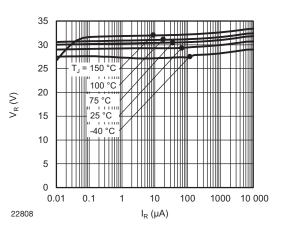


Fig. 4 - Typical Reverse Voltage  $V_R$  vs. Reverse Current  $I_R$ 

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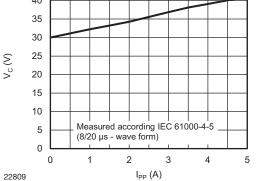


Fig. 5 - Typical Peak Clamping Voltage  $V_C$  vs. Peak Pulse Current I<sub>PP</sub>

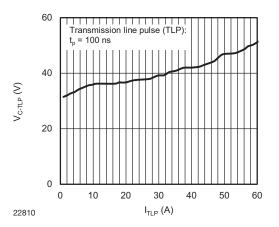
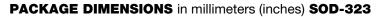
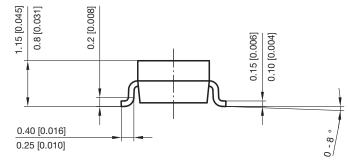
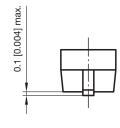
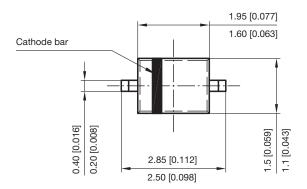


Fig. 6 - Typical Clamping Voltage V<sub>C-TLP</sub> vs. Pulse Current I<sub>TLP</sub>

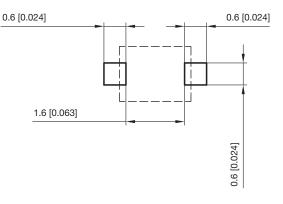












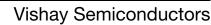
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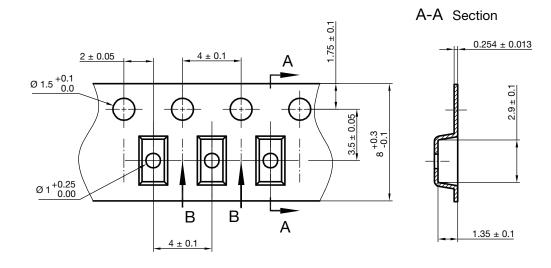
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#### **CARRIER TAPE SOD-323**

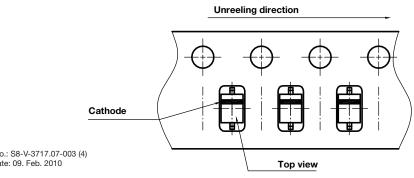


**B-B** Section



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#### **ORIENTATION IN CARRIER TAPE SOD-323**



Document no.: S8-V-3717.07-003 (4) Created - Date: 09. Feb. 2010 22772



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