AN6151K

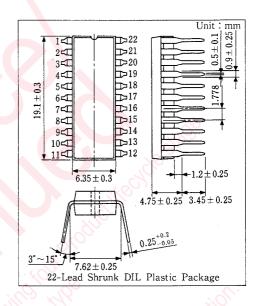
Telephone Set Communication Circuit

Outline

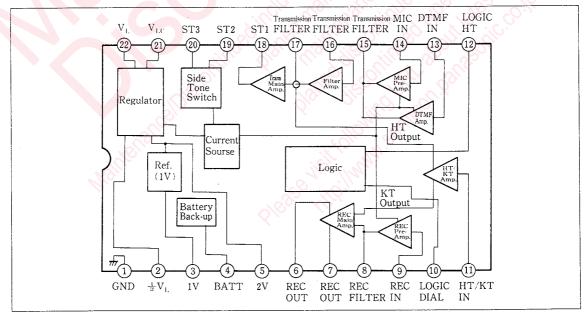
The AN6151K is an integrated circuit designed for telephone set channel. It has all the functions which are necessary to apply a sound signal onto the telephone line and performance similar to that of an ordinary telephone set. It is also applicable for use in both Japan and other countries.

■ Features

- Wide operating voltage range: 3 ~10V
- Built-in amplifiers for "Hold Tone", "Dial Tone", "DTMF"
- Amplifier output switchable
- Balance network circuit switchable depending on line current
- Each amplifier gain automatically changeable depending on line current
- Compact 22-lead shrunk dual-in-line plastic package



Block Diagram





■ Absolute Maximum Ratings (Ta=25°C)

Item	Symbol	Rating	Unit
Line Voltage	VL	12	V
Line Current	IL	120	mA
Power Dissipation	PD	1440	mW
Operating Ambient Temperature (Ta=60°C)	Topr	$-30 \sim +75$	°C
Storage Temperature	Tstg	$-55 \sim +150$	°C

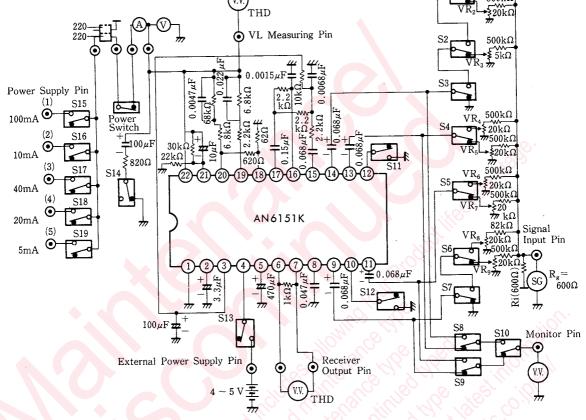
■ Electrical Characteristics ($I_L=40\text{mA}$, $f_{reg}=1\text{kHz}$, $T_a=25^{\circ}\text{C}$)

Item	Symbol	Test Circuit	Condition	min.	typ.	max.	Unit	
Reciver System					X	9		
Receiver Gain	G _{V-R} *1	1	$v_{i(REC)} = -47 dBV$	38.5	41	43.5	đВ	
Receiver Output Distortion	THD _R *1	1	$v_{i(REC)} = -47 dBV$	1	1.5	5	%	
Max. Receiver Level	V _{O-R} *1	1	THD=5%	-5.2	-1.2		dBV	
Auto Pad	⊿APr *1	1	Δ ÍL=100mA-40mA, $vi(REC)$ =-47dBV	-6	-3	-1.5	dB	
KEY IN TONE gain	G _V -KT *1	1	$v_{i(KEY)} = -42dBV (D \cdot M : ON)$	15	18	21	dB	
KEY IN TONE Output Distortion	THD _{KT} *1		$vi(KEY) = -42dBV (D \cdot M : ON)$		1		%	
Transmission System (wh	en LIN O	pen)						
Transmission Gain	GVKT *2	1	$v_{i(MIC)} = -37 dBV$	27.5	29.5	31.5	dB	
Transmission Output Distortion	THD kT *2	1	$v_{i(MIC)} = -37 dBV$		1	5	%	
Max. Transmisson Level	<i>v</i> ot *2	1	THD=5%	-2.2	2.8	<i>3</i> ,0	dBV	
Transmission Auto Pad	⊿ AP⊤ *2	1	Δ IL=100mA-40mA, v i(MIC)=-37dBV	-6.5	-3.5	100	dB	
HOLD TONE Gain	G _{V-HT} *2	1	$v_{\text{i(HOLD)}} = -32 \text{dBV (H : ON)}$	21	24	27	dB	
HOLD TONE Distortion	THD HT*2	1	$v_{\text{i(HOLD)}} = -32 \text{dBV (H : ON)}$		1	10	%	
DTMF Gain	G _{V-DT} *2	1	$v_{\text{i(TUCH)}} = -37 \text{dBV (D·MON)}$	30.5	32.5	34.5	dB	
DTMF TUCH TONE Output Distortion	THD DT *2	1	$v_{\text{i(TUCH)}} = -37 \text{dBV (D·MON)}$		10	7	%	
DTMF Auto Pad	⊿AP DT *2	1	Δ IL=100mA-40mA, v i(REC)=-36dBV	-6.5	-4.5	-2.5	dB	
BATTERY ON	<i>v</i> o _{BT} *2	1	IL=5mA, $v_{i(REC)}=-47dBV$	38	2//		dB	
Power Supply			On My On Ville St	700				
DC Impedance (1)	Z_{DC-1} *2		IL=20mA	(O)	150		Ω	
DC Impedance (2)	Z _{DC-2} *2		IL=100mA		80		Ω	
AC Impedance (1)	Z _{AC-1} *2		IL=40mA		2		kΩ	
AC Impedance (2)	Z _{AC-2} *2		IL=100mA 1.5		1.5		kΩ	

^{*1:} Measure between Pin 6 and 7. *2: Measure between Pin 2 and 1.

 $VR_1 \frac{150k\Omega}{20k\Omega}$

Test Circuit 1



Measuring Method

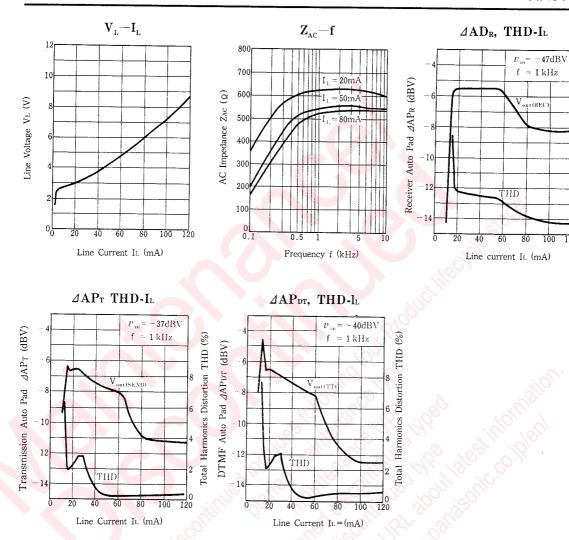
	Pin	Pin Relay Switch No.																			
Item	Symbol	No.	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19
Receiver Gain	Gv -R	6 ~ 7	—	_		70	7	-3	0	_		_	Ы.	7				_	0	—	
Receiver Output Distortion	THDR	6~7	_	-		4	_		0	6	þ		<u></u>			_		_	0	_	_
Receive Level	Vo-r	6~7		_			_	0	0		_	4	_	_	_	_		_	0	_	_
Receiver Auto Pad	⊿AP _R	6~7			_	-		5	0	Ī		_	—		_		0		_	_	_
KEY IN TONE Gain	Gv-kt	6 ~ 7	_		_	_	7	4,	_	4	0		_	0					0	_	_
Transmission Gain	Gv-т	22~1	_	0	0		9	ĺ	4	0	_	0	_						0	_	
Transmission Output Distortion	ТНDт	22~1		0	0	9	_		_	0	_	0	_						0		
Max. Transmission Level	<i>Vo</i> -т	22~1	0	0	0	_	-	X		0		0	_	_	-	_	_		0	_	
Transmission Auto Pad	⊿APτ	22~1	-	0	0		_	_	_	0	_	0		_		_	0	_	_	_	_
HOLD TONE OuputGain	Gv-нт	22~1	_	_	_		0		<u> </u>	_	0	_	0	_	-		_	_	0		
HOLD TONEOutput Distortion	THD _{HT}	22~1		_		_	0			<u> </u>	0	-	0	-		_		$\lfloor -$	0	_	
DTMF Output Gain	GVDT	22~1		_	_	-	_	_		_				0		_	_		0	_	
DTMF Output Distortion	THDDT	22~ 1	-	_	_				_		[_	_	_	0		_	_		0		_
DTMF Auto Pad	ДАРрт	28~ 1	_			_	_	-				_		0	-	_	0	_	_		_
BATTERY ON	V o вт	6~7						0	0		_	_		_	0			<u> -</u>	<u> </u>	<u> -</u>	0

○:SW ON, -:SW OFF



100

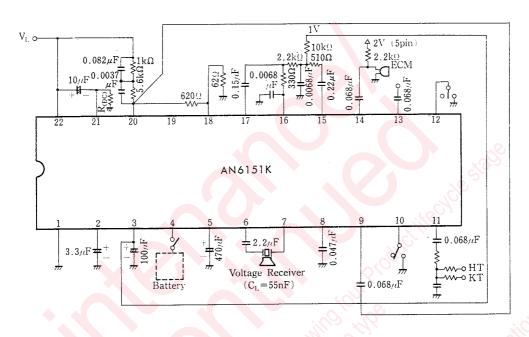
Total Harmonics Distortion THD (%)



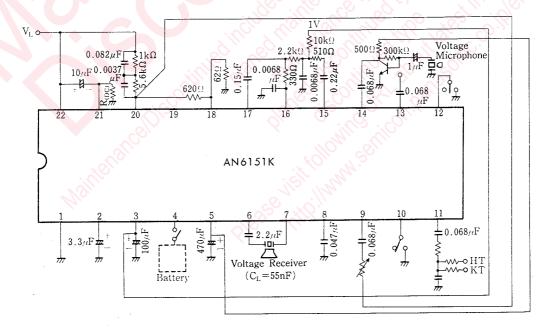
Pin

Pin No.	Pin Name	Pin No.	Pin Name
1	GND	12	Logic (Hold Tone)
2	1/2 Line Voltage	13	DTMF Input
3	1V Reference Voltage	14	Transmission MIC Input
4	Battery-Back up	15	Transmission MIC Filter (1)
5	2V Reference Voltage	16	Transmission MIC Filter (2)
6	Receiver Output (1)	17	Transmission MIC Filter (3)
7	Receiver Output (2)	18	Side Tone-SW (1)
8	Receive Filter	19	Side Tone-SW (2)
9	Receiver Input	20	Side Tone-SW (3)
10	Logic (Dial Tone)	21	Line Filter
11	Hold Tone/KEY in Tone Input	22	Line

- Application Circuits
- When ECM is used in the Transmission MIC



When ceramic is used in the Transmission MIC





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