

Silicon NPN Power Transistors

2N5972

DESCRIPTION

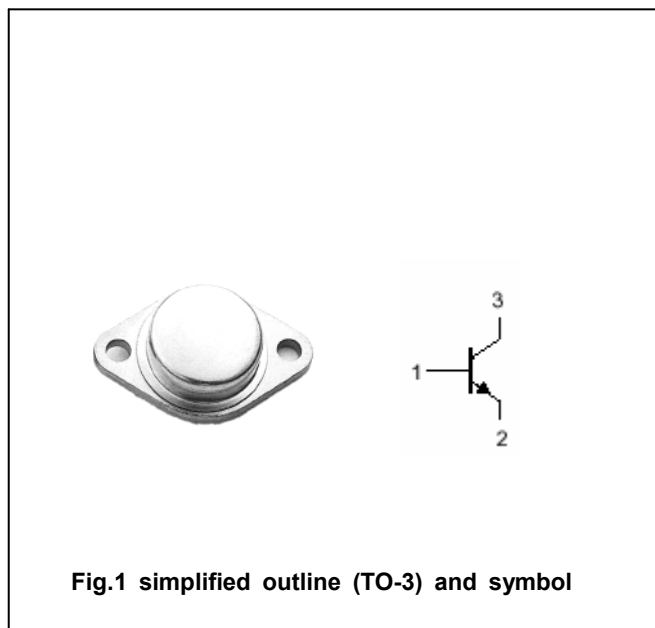
- With TO-3 package
- Low collector saturation voltage
- High power dissipations

APPLICATIONS

- Designed for general-purpose power amplifier and switching applications

PINNING

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

Absolute maximum ratings($T_a = \square$)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	100	V
V_{CEO}	Collector-emitter voltage	Open base	80	V
V_{EBO}	Emitter-base voltage	Open collector	5	V
I_C	Collector current		15	A
I_{CM}	Collector current-peak		30	A
I_B	Base current		5	A
P_D	Total Power Dissipation	$T_C = 25 \square$	150	W
T_j	Junction temperature		150	\square
T_{stg}	Storage temperature		-65~200	\square

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	VALUE	UNIT
$R_{th\ j-c}$	Thermal resistance junction to case	1.1	\square/W

Silicon NPN Power Transistors

2N5972

CHARACTERISTICS

T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO(SUS)}	Collector-emitter sustaining voltage	I _C =0.1A ; I _B =0	80			V
V _{CEsat-1}	Collector-emitter saturation voltage	I _C =7A ; I _B =0.7A			1.0	V
V _{CEsat-2}	Collector-emitter saturation voltage	I _C =15A; I _B =3.75A			4.0	V
V _{BEsat}	Base-emitter saturation voltage	I _C =15A; I _B =3.75A			2.5	V
I _{CEO}	Collector cut-off current	V _{CE} =40V; I _B =0			1.0	mA
I _{CEV}	Collector cut-off current	V _{CE} =100V; V _{BE(off)} =1.5V T _C =150°C			0.5 5.0	mA
I _{CBO}	Collector cut-off current	V _{CB} =100V; I _E =0			0.5	mA
I _{EBO}	Emitter cut-off current	V _{EB} =5V; I _C =0			1.0	mA
h _{FE-1}	DC current gain	I _C =5A ; V _{CE} =1.5V	25		75	
h _{FE-2}	DC current gain	I _C =15A ; V _{CE} =4V	4			
f _T	Transition frequency	I _C =1A; V _{CE} =10V	4			MHz

Silicon NPN Power Transistors

2N5972

PACKAGE OUTLINE

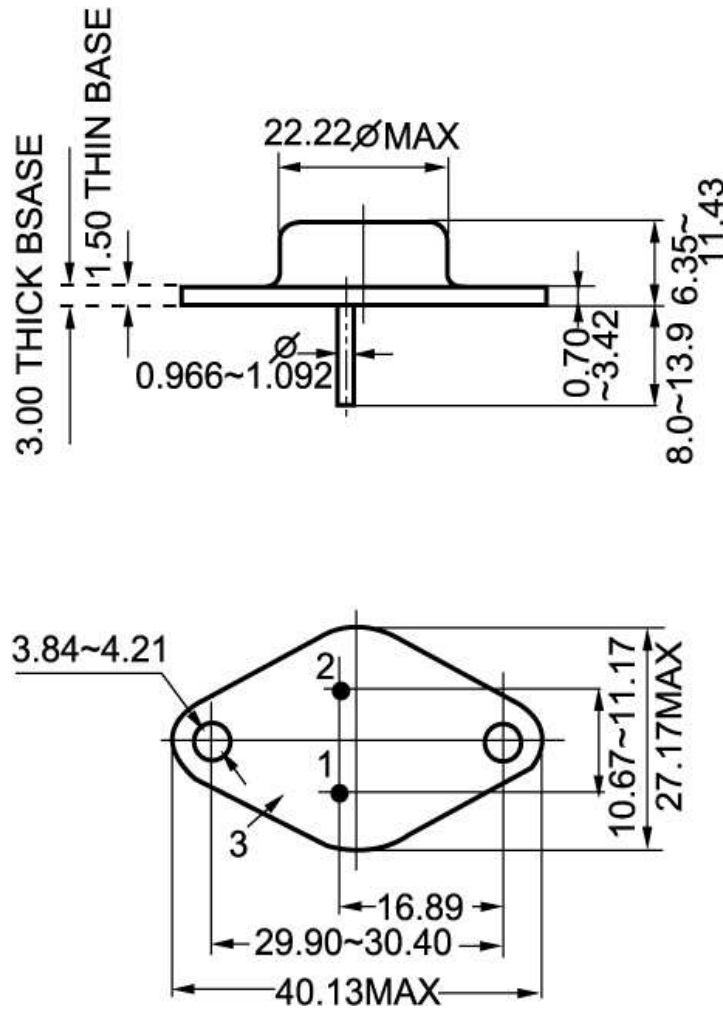


Fig.2 outline dimensions (unindicated tolerance:±0.10mm)