PBSS4041NZ



60 V, 7 A NPN low VCEsat (BISS) transistor Rev. 2 — 8 August 2012

Product data sheet

1. **Product profile**

1.1 General description

NPN low V_{CEsat} Breakthrough In Small Signal (BISS) transistor in a SOT223 (SC-73) medium power Surface-Mounted Device (SMD) plastic package.

PNP complement: PBSS4041PZ.

1.2 Features and benefits

- Very low collector-emitter saturation voltage V_{CEsat}
- High collector current capability I_C and
- High collector current gain (h_{FF}) at high I_C
- High energy efficiency due to less heat generation
- AEC-Q101 qualified
- Smaller required PCB area than for conventional transistors

1.3 Applications

- Loadswitch
- Battery-driven devices
- Power management

- Charging circuits
- Power switches (e.g. motors, fans)

1.4 Quick reference data

Table 1. **Quick reference data**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{CEO}	collector-emitter voltage	open base	-	-	60	V
I _C	collector current		-	-	7	Α
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	-	15	Α
R _{CEsat}	collector-emitter saturation resistance	$I_{C} = 6 \text{ A}; I_{B} = 600 \text{ mA}; \text{ pulsed};$ $t_{p} \le 300 \text{ µs}; \delta \le 0.02; T_{amb} = 25 ^{\circ}\text{C}$	-	17.5	25	mΩ



2. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base		
2	С	collector	4	C
3	Е	emitter		В
4	С	collector	1 2 3	Ë
			SOT223 (SC-73)	sym123

3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBSS4041NZ	SC-73	plastic surface-mounted package with increased heatsink; 4 leads	SOT223

4. Marking

Table 4. Marking codes

Type number	Marking code
PBSS4041NZ	PB4041NZ

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

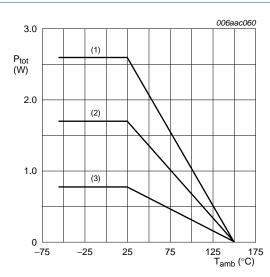
Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	60	V
V_{CEO}	collector-emitter voltage	open base		-	60	V
V _{EBO}	emitter-base voltage	open collector		-	5	V
I _C	collector current			-	7	Α
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	15	Α
I _B	base current			-	1	Α
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	<u>[1]</u>	-	770	mW
			[2]	-	1700	mW
			[3]	-	2600	mW

Table 5. Limiting values ...continued

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
Tj	junction temperature		-	150	°C
T _{amb}	ambient temperature		-55	150	°C
T _{stg}	storage temperature		-65	150	°C

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².
- [3] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.



- (1) Ceramic PCB, Al₂O₃, standard footprint
- (2) FR4 PCB, mounting pad for collector 6 cm²
- (3) FR4 PCB, standard footprint

Fig 1. Power derating curves

6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)} thermal resistance from junction to ambient		in free air	<u>[1]</u>	-	-	160	K/W
	-		[2]	-	-	75	K/W
	amblem		[3]	-	-	50	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	11	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².
- [3] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.

PBSS4041NZ

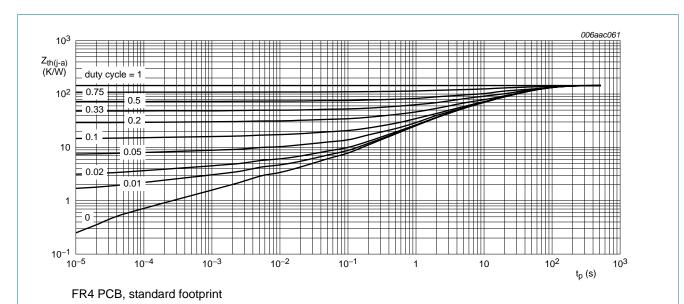


Fig 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

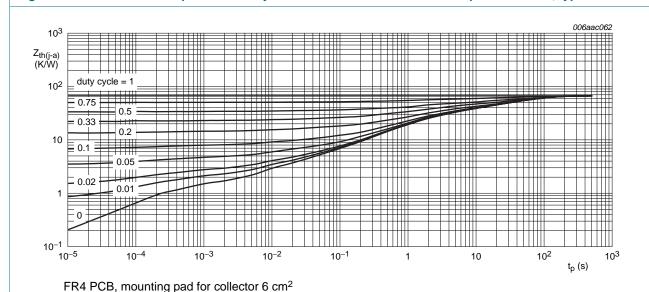
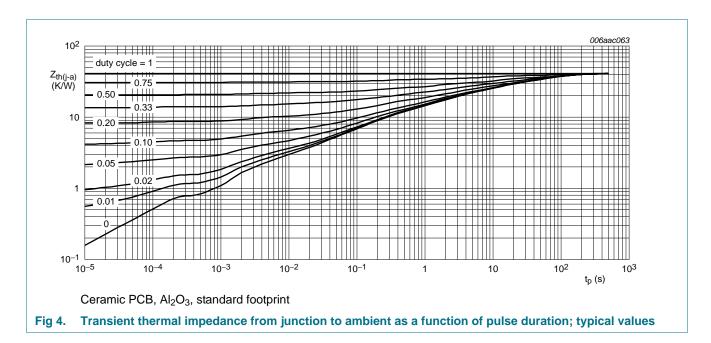


Fig 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



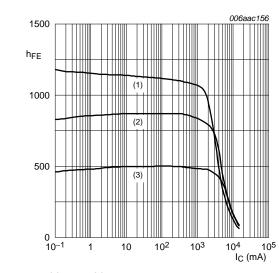
7. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off	V_{CB} = 60 V; I_E = 0 A; T_{amb} = 25 °C	-	-	100	nA
current	current	$V_{CB} = 60 \text{ V}; I_E = 0 \text{ A}; T_j = 150 \text{ °C}$	-	-	50	μΑ
I _{CES}	collector-emitter cut-off current	$V_{CE} = 48 \text{ V}; V_{BE} = 0 \text{ V}; T_{amb} = 25 \text{ °C}$	-	-	100	nA
I _{EBO}	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; I_{C} = 0 \text{ A}; T_{amb} = 25 \text{ °C}$	-	-	100	nA
h _{FE} DC curr	DC current gain	V_{CE} = 2 V; I_{C} = 500 mA; pulsed; $t_{p} \le 300 \ \mu s$; $\delta \le 0.02$; T_{amb} = 25 °C	300	500	-	
		V_{CE} = 2 V; I_{C} = 1 A; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02 ; T_{amb} = 25 °C	300	500	-	
		V_{CE} = 2 V; I_{C} = 2 A; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02 ; T_{amb} = 25 °C	300	500	-	
		V_{CE} = 2 V; I_{C} = 4 A; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02 ; T_{amb} = 25 °C	250	400	-	
		V_{CE} = 2 V; I_{C} = 6 A; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02 ; T_{amb} = 25 °C	100	200	-	
		V_{CE} = 2 V; I_{C} = 7 A; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	50	100	-	

 Table 7.
 Characteristics ... continued

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEsat}	collector-emitter saturation voltage	I_C = 1 A; I_B = 50 mA; pulsed; $t_p \le 300 \ \mu s; \ \delta \le 0.02 \ ; \ T_{amb}$ = 25 °C	-	25	35	mV
		I_C = 1 A; I_B = 10 mA; pulsed; $t_p \le 300 \ \mu s; \ \delta \le 0.02 \ ; \ T_{amb}$ = 25 °C	-	43	60	mV
		I_C = 2 A; I_B = 40 mA; pulsed; $t_p \le 300 \ \mu s$; $\delta \le 0.02$; T_{amb} = 25 °C	-	53	75	mV
		$I_{C} = 4 \text{ A}; I_{B} = 200 \text{ mA}; \text{ pulsed};$ $t_{p} \le 300 \text{ µs}; \delta \le 0.02 ; T_{amb} = 25 \text{ °C}$	-	78	110	mV
	$I_C = 4$ A; $I_B = 40$ mA; pulsed; $t_p \le 300$ µs; $\delta \le 0.02$; $T_{amb} = 25$ °C	-	115	160	mV	
	I_C = 7 A; I_B = 350 mA; pulsed; $t_p \le$ 300 µs; $\delta \le$ 0.02 ; T_{amb} = 25 °C	-	130	195	mV	
R _{CEsat}	collector-emitter saturation resistance	$I_{C} = 6 \text{ A}; I_{B} = 600 \text{ mA}; \text{ pulsed}; $ $t_{p} \le 300 \text{ µs}; \delta \le 0.02 ; T_{amb} = 25 \text{ °C}$	-	17.5	25	mΩ
DLoat	base-emitter saturation voltage	$I_{C} = 1 \text{ A}; I_{B} = 100 \text{ mA}; \text{ pulsed};$ $t_{p} \le 300 \text{ µs}; \delta \le 0.02 ; T_{amb} = 25 \text{ °C}$	-	0.83	0.9	V
		$I_{C} = 4 \text{ A}; I_{B} = 400 \text{ mA}; \text{ pulsed}; $ $t_{p} \le 300 \text{ µs}; \delta \le 0.02 ; T_{amb} = 25 \text{ °C}$	-	0.98	1.05	V
V_{BEon}	base-emitter turn-on voltage	V_{CE} = 2 V; I_{C} = 2 A; pulsed; $t_{p} \le 300 \ \mu s$; $\delta \le 0.02$; T_{amb} = 25 °C	-	0.72	0.85	V
t _d	delay time	$V_{CC} = 12.5 \text{ V}; I_C = 1 \text{ A}; I_{Bon} = 0.05 \text{ A};$	-	55	-	ns
t _r	rise time	$I_{Boff} = -0.05 \text{ A}; T_{amb} = 25 ^{\circ}\text{C}$	-	55	-	ns
t _{on}	turn-on time		-	110	-	ns
t _s	storage time		-	1220	-	ns
t _f	fall time		-	230	-	ns
t _{off}	turn-off time		-	1450	-	ns
f _T	transition frequency	$V_{CE} = 10 \text{ V}; I_{C} = 100 \text{ mA}; f = 100 \text{ MHz};$ $T_{amb} = 25 \text{ °C}$	-	105	-	MHz
C _c	collector capacitance	$V_{CB} = 10 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A};$ f = 1 MHz; $T_{amb} = 25 \text{ °C}$	-	50	-	pF



$$V_{CE} = 2 V$$

(1)
$$T_{amb} = 100 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = -55 \, ^{\circ}C$$

Fig 5. DC current gain as a function of collector current; typical values

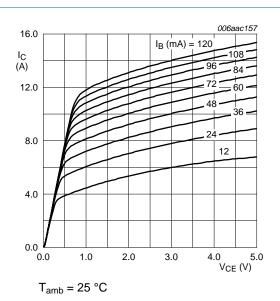
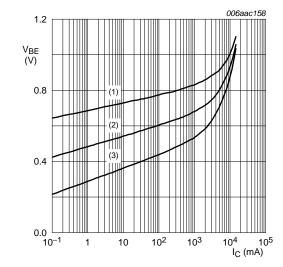


Fig 6. Collector current as a function of collector-emitter voltage; typical values



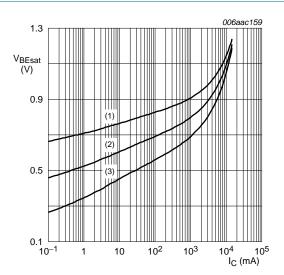
 $V_{CE} = 2 V$

(1)
$$T_{amb} = -55 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = 100 \, ^{\circ}C$$

Fig 7. Base-emitter voltage as a function of collector current; typical values



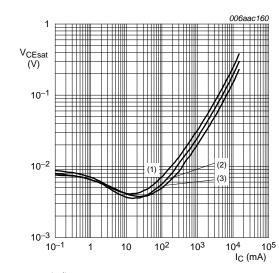
$$I_{\rm C}/I_{\rm B} = 20$$

(1)
$$T_{amb} = -55 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = 100 \, ^{\circ}C$$

Fig 8. Base-emitter saturation voltage as a function of collector current; typical values



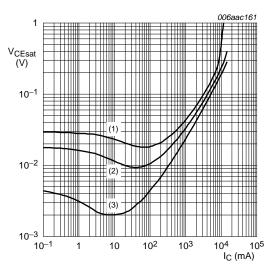
$$I_C/I_B = 20$$

(1)
$$T_{amb} = 100 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = -55 \, ^{\circ}C$$

Fig 9. Collector-emitter saturation voltage as a function of collector current; typical values



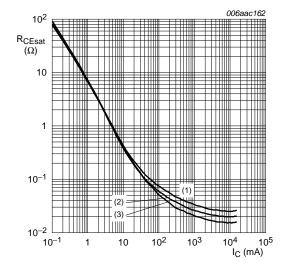
$$T_{amb} = 25 \, ^{\circ}C$$

(1)
$$I_C/I_B = 100$$

(2)
$$I_C/I_B = 50$$

(3)
$$I_C/I_B = 10$$

Fig 10. Collector-emitter saturation voltage as a function of collector current; typical values

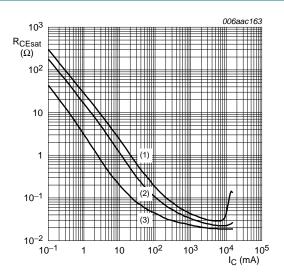


 $I_{\rm C}/I_{\rm B}=20$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = -55 \, ^{\circ}C$$

Fig 11. Collector-emitter saturation resistance as a function of collector current; typical values



$$T_{amb} = 25 \, ^{\circ}C$$

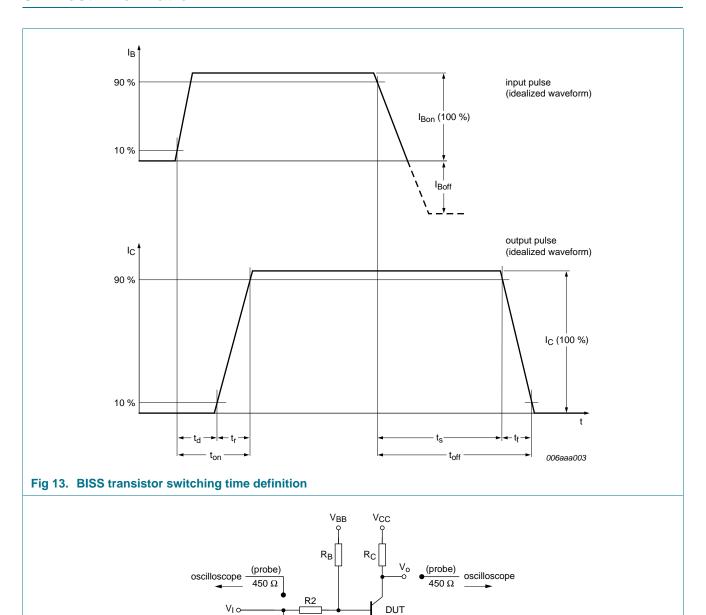
(1)
$$I_C/I_B = 100$$

(2)
$$I_C/I_B = 50$$

(3)
$$I_C/I_B = 10$$

Fig 12. Collector-emitter saturation resistance as a function of collector current; typical values

8. Test information



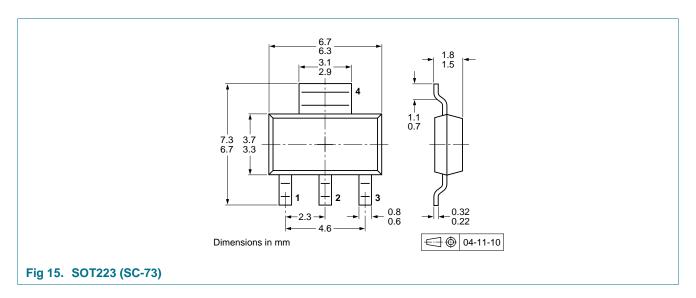
8.1 Quality information

Fig 14. Test circuit for switching times

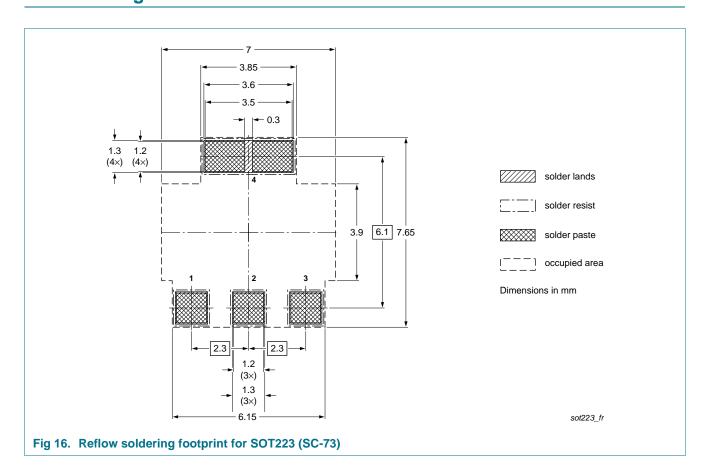
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

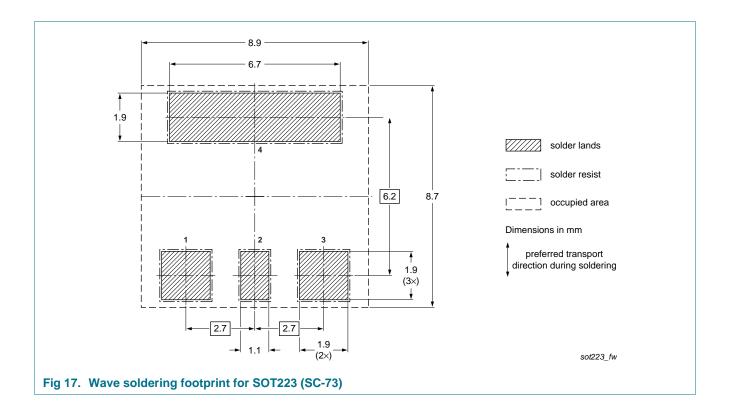
mlb826

9. Package outline



10. Soldering





11. Revision history

Table 8. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
PBSS4041NZ v.2	20120808	Product data sheet	-	PBSS4041NZ v.1
Modifications:	7 "Characteristics"12 "Legal information	02001		
PBSS4041NZ v.1	20100331	Product data sheet	-	-

12. Legal information

12.1 Data sheet status

Document status[1] [2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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60 V, 7 A NPN low VCEsat (BISS) transistor

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PBSS4041NZ

60 V, 7 A NPN low VCEsat (BISS) transistor

14. Contents

1	Product profile
1.1	General description
1.2	Features and benefits1
1.3	Applications
1.4	Quick reference data1
2	Pinning information2
3	Ordering information2
4	Marking
5	Limiting values2
6	Thermal characteristics3
7	Characteristics5
8	Test information9
8.1	Quality information
9	Package outline10
10	Soldering10
11	Revision history12
12	Legal information13
12.1	Data sheet status
12.2	Definitions13
12.3	Disclaimers
12.4	Trademarks14
13	Contact information14

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