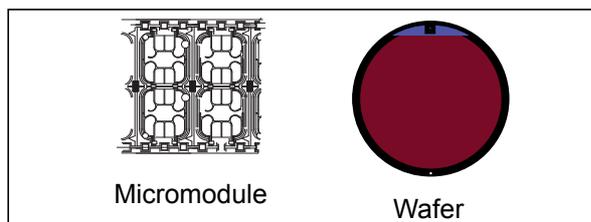


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**Secure MCU**  
**with enhanced security, crypto-processor and 80-Kbyte EEPROM**

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Data brief



## Features

### Hardware features

- Enhanced 8/16-bit ST23 CPU core with 16 Mbytes linear addressable memory
- 396 Kbytes of User ROM
- 6 Kbytes of User RAM
- 2 Kbytes of NESCRIPT RAM
- 80 Kbytes of User EEPROM including 128 bytes of User OTP area:
  - Highly reliable CMOS EEPROM submicron technology
  - 30-year data retention
  - 500,000 Erase/Write cycles endurance typical at 25° C
  - 1 to 64 bytes Erase or Program in 1.5 ms
- Operating temperature: –25° to +85° C
- Three 8-bit timers with watchdog and interrupt capability
- 3 V and 5 V supply voltage ranges
- External clock frequency up to 10 MHz
- High performance provided by:
  - CPU clock frequency up to 29 MHz
- Power-saving Standby state
- Contact assignment compatible with ISO/IEC 7816-3 standards
- Asynchronous receiver transmitter (IART) for high speed serial data support (ISO/IEC 7816-3 and EMV™ compliant)
- ESD protection greater than 5 kV (HBM)

### Security features

- Active shield
- Enhanced NESCRIPT crypto-processor for public key cryptography
- Hardware security enhanced DES accelerator
- Monitoring of environmental parameters
- Protection mechanisms against faults
- AIS-31 class P2 compliant true random number generator (TRNG)
- ISO 3309 CRC calculation block
- Memory protection unit (MPU)
- Unique serial number on each die

### Development environment

- Software development and firmware generation are supported by a comprehensive set of development tools dedicated to software design and validation: C compiler, simulator and emulator

### Applications

ST23YL80 major applications include:

- Banking, IT and Pay TV applications

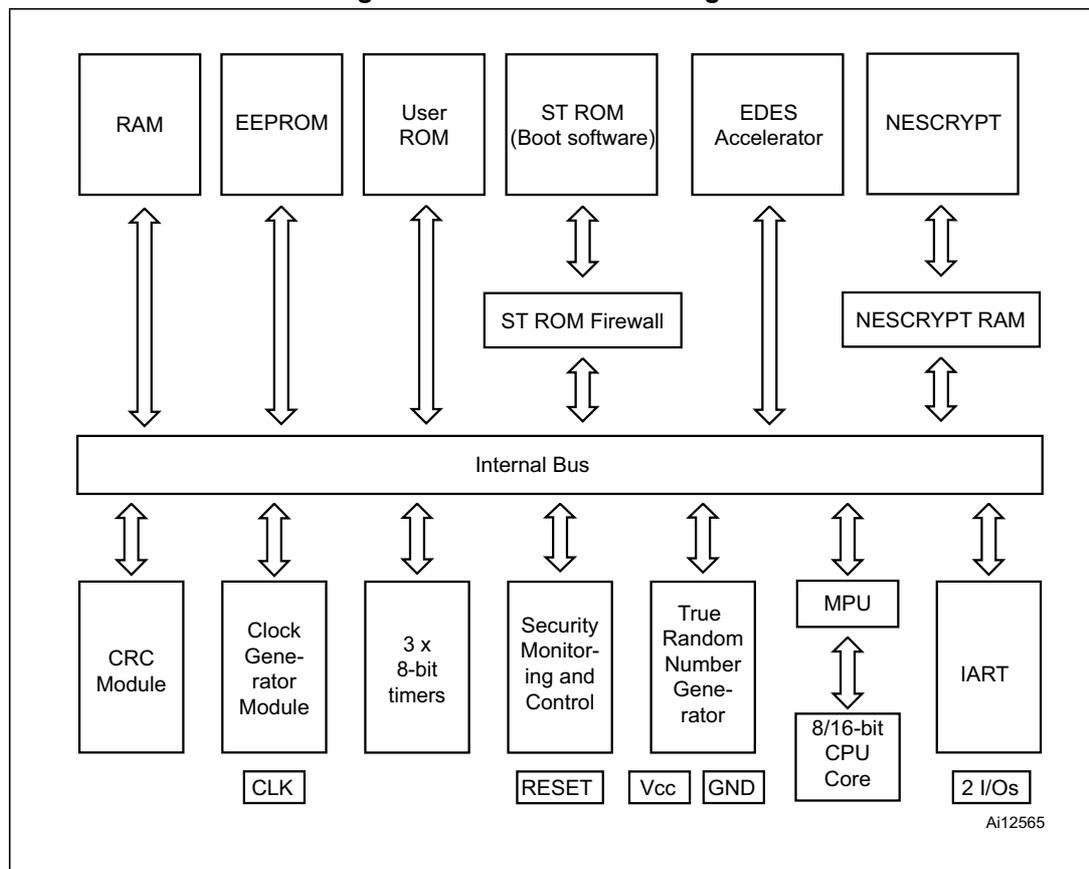
# 1 Description

The ST23YL80 is a serial access secure microcontroller custom-designed for secure smartcard applications.

It is based on an enhanced STMicroelectronics 8/16-bit CPU core offering 16 Mbytes linear addressing space. ST23YL80 devices are manufactured using an advanced highly reliable ST CMOS EEPROM technology.

Moreover, an ISO/IEC 7816-3 EMV-compliant asynchronous receiver transmitter (IART) communication peripheral is available.

**Figure 1. ST23YL80 block diagram**



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## 1.1 Development environment

Development tools for secure MCU products include a complete range of hardware systems and software tools from STMicroelectronics and third-party tool suppliers. The range of tools includes solutions to help you to develop and debug your application and evaluate secure MCU products and their peripherals.

An Integrated Development Environment (IDE), the ST Visual Develop (STVD), provides a set of tools for developing embedded applications. This interface manages the project configuration, code edition, code generation and program debugging.

A Smartcard ICS emulator and simulator are available for developing and validating your application code.

All the information needed to generate the application code and personalization will be collected in a delivery file (.DLV extension). This file is created using the Delivery menu of the STMicroelectronics configuration software tool, SCool.

## 2 Revision history

**Table 1. List of modifications**

Date	Revision	Changes
28-Feb-2008	1	First release.
10-Sep-2008	2	Updated maximum external clock frequency.
16-Aug-2013	3	Updated logo information on page 2.

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