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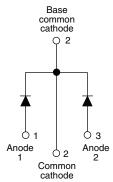
Vishay Semiconductors

HALOGEN

FREE

Schottky Rectifier, 2 x 15 A

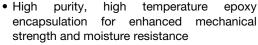


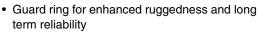


PRODUCT SUMMARY						
Package	TO-247AC					
I _{F(AV)}	2 x 15 A					
V_{R}	50 V, 60 V					
V _F at I _F	0.56 V					
I _{RM} max.	45 mA at 125 °C					
T _J max.	150 °C					
Diode variation	Common cathode					
E _{AS}	13 mJ					

FEATURES

- 150 °C T_J operation
- · Very low forward voltage drop
- · High frequency operation





- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)



The VS-30CPQ... center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

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MAJOR RATINGS AND CHARACTERISTICS										
SYMBOL	DL CHARACTERISTICS VALUES UNITS									
I _{F(AV)}	Rectangular waveform	30	Α							
V _{RRM}		50/60	V							
I _{FSM}	t _p = 5 μs sine	1020	Α							
V _F	15 Apk, T _J = 125 °C (per leg)	0.56	V							
T _J		- 55 to 150	°C							

VOLTAGE RATINGS										
PARAMETER	SYMBOL	VS-30CPQ050PbF	VS-30CPQ050-N3	VS-30CPQ060PbF	VS-30CPQ060-N3	UNITS				
Maximum DC reverse voltage	V_R									
Maximum working peak reverse voltage	V _{RWM}	50	50	60	60	V				

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS			
Maximum average forward current See fig. 5	I _{F(AV)}	I _{F(AV)} 50 % duty cycle at T _C = 112 °C, rectangular waveform						
Maximum peak one cycle non-repetitive surge current per leg	Irou	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	1020	Α			
See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	265				
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 1.50 A, L = 11	13	mJ				
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero Frequency limited by T_J maxim	1.50	Α				



VS-30CPQ0.0PbF, VS-30CPQ0.0-N3

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ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS			
Maximum forward voltage drop per leg See fig. 1		15 A	T _{.1} = 25 °C	0.60				
	V _{FM} ⁽¹⁾	30 A	1j=25 C	0.80	v			
		15 A	T 105 °C	0.56	V			
		30 A	T _J = 125 °C	0.70				
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	0.80	mA			
See fig. 2	IRM ('')	T _J = 125 °C	v _R = nateu v _R	45	IIIA			
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25 °C		720	pF			
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		7.5	nΗ			
Maximum voltage rate of change	dV/dt	Rated V _R	Rated V _R					

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 150	°C		
Maximum thermal resistance, junction to case per leg		D	DC operation See fig. 4	2.20			
Maximum thermal resistance, junction to case per package		R_{thJC}	DC operation	1.10	°C/W		
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.24			
Approximate weight				6	g		
Approximate weight				0.21	OZ.		
Mauration to annua			Non-lubricated threads	6 (5)	kgf · cm		
Mounting torque	maximum		inon-iubilicateu tilleaus	12 (10)	(lbf · in)		
Marking device			Case et de TO 247AC (JEDEC)	30CPQ050			
			Case style TO-247AC (JEDEC)		Q060		

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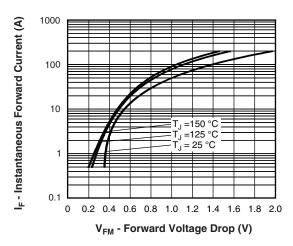


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

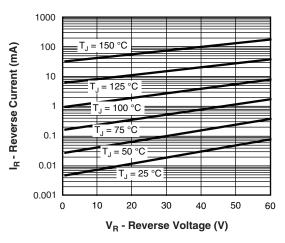


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

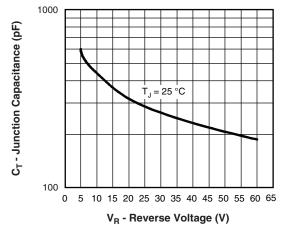


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

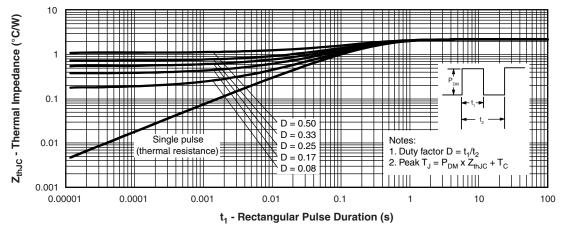


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)



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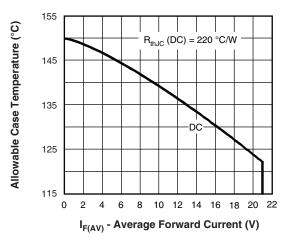


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

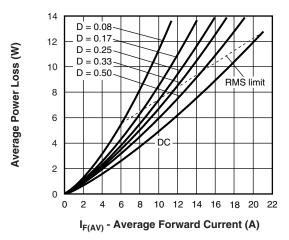


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

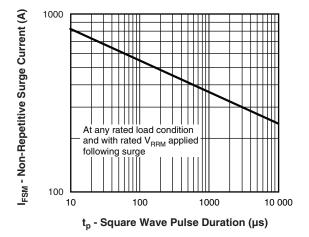


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

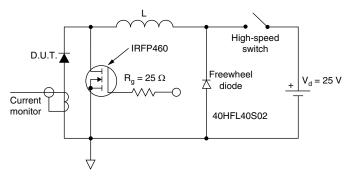


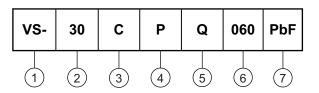
Fig. 8 - Unclamped Inductive Test Circuit



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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (30 = 30 A)

Circuit configuration:

C = Common cathode

Environmental digit

4 - Package:

P = TO-247

5 - Schottky "Q" series

050 = 50 V 060 = 60 V

6 - Voltage code

PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)									
PREFERRED P/N QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPT									
VS-30CPQ050PbF	25	500	Antistatic plastic tube						
VS-30CPQ050-N3	25	500	Antistatic plastic tube						
VS-30CPQ060PbF	25	500	Antistatic plastic tube						
VS-30CPQ060-N3	25	500	Antistatic plastic tube						

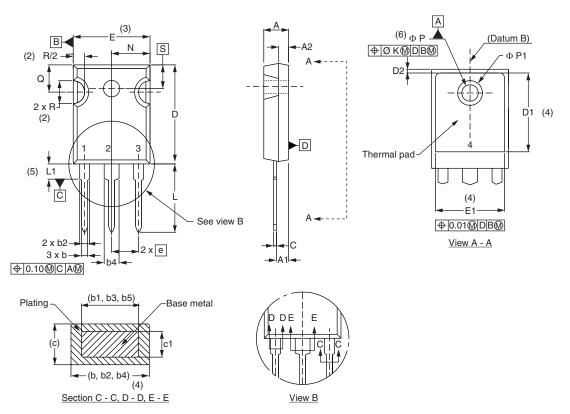
LINKS TO RELATED DOCUMENTS							
Dimensions <u>www.vishay.com/doc?95223</u>							
Dort moulting information	TO-247AC PbF	www.vishay.com/doc?95226					
Part marking information	TO-247AC -N3	www.vishay.com/doc?95007					



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TO-247

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INC	HES	NOTES	NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209			D2	0.51	1.30	0.020	0.051	
A1	2.21	2.59	0.087	0.102			Е	15.29	15.87	0.602	0.625	3
A2	1.50	2.49	0.059	0.098			E1	13.72	-	0.540	-	
b	0.99	1.40	0.039	0.055			е	5.46	BSC	0.215	BSC	
b1	0.99	1.35	0.039	0.053			ØΚ	2.	54	0.0)10	
b2	1.65	2.39	0.065	0.094			L	14.20	16.10	0.559	0.634	
b3	1.65	2.34	0.065	0.092			L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135			Ν	7.62	BSC	0	.3	
b5	2.59	3.38	0.102	0.133			ØΡ	3.56	3.66	0.14	0.144	
С	0.38	0.89	0.015	0.035			Ø P1	-	6.98	-	0.275	
c1	0.38	0.84	0.015	0.033			Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3		R	4.52	5.49	0.178	0.216	
D1	13.08	-	0.515	-	4		S	5.51	BSC	0.217	BSC	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- $^{(7)}\,$ Outline conforms to JEDEC® outline TO-247 with exception of dimension c



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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.

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