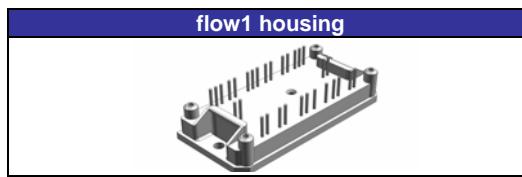
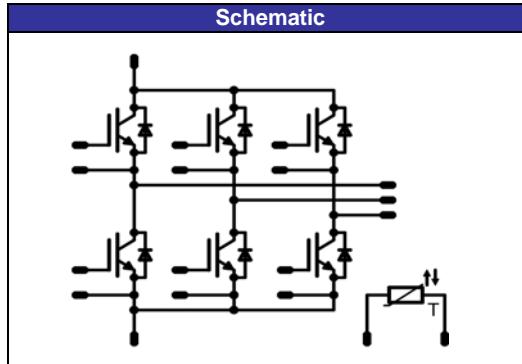


flowPACK 1 3rd gen
600V/50A

Features
<ul style="list-style-type: none"> • Compact flow1 housing • Trench Fieldstop IGBT3 Technology • Compact and Low Inductance Design • AlN substrate for improved performance • Built-in NTC



Target Applications
<ul style="list-style-type: none"> • Motor Drive • Power Generation • UPS



Types
<ul style="list-style-type: none"> • V23990-P823-F

Maximum Ratings

 $T_j=25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
-----------	--------	-----------	-------	------

Inverter Transistor

Collector-emitter break down voltage	V_{CE}		600	V
DC collector current	I_C	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	50	A
Repetitive peak collector current	I_{Cpulse}	t_p limited by $T_{j\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	150	A
Power dissipation per IGBT	P_{tot}	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	139	W
Gate-emitter peak voltage	V_{GE}		± 20	V
Short circuit ratings	t_{sc} V_{CC}	$T_j \leq 150^\circ\text{C}$ $V_{GE}=15\text{V}$	6 360	μs V
Maximum Junction Temperature	$T_{j\max}$		175	$^\circ\text{C}$

Inverter Diode

Peak Repetitive Reverse Voltage	V_{RRM}	$T_j=25^\circ\text{C}$	600	V
DC forward current	I_F	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	50	A
Repetitive peak forward current	I_{FRM}	t_p limited by $T_{j\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	100	A
Power dissipation per Diode	P_{tot}	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	109	W
Maximum Junction Temperature	$T_{j\max}$		175	$^\circ\text{C}$

Maximum Ratings

T_j=25°C, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
-----------	--------	-----------	-------	------

Thermal Properties

Storage temperature	T _{stg}		-40...+125	°C
Operation temperature under switching condition	T _{op}		-40...+150	°C

Insulation Properties

Insulation voltage	V _{is}	t=1min	4000	V _{DC}
Creepage distance			min 12,7	mm
Clearance			min 12,7	mm

Characteristic Values

Parameter	Symbol	Conditions				Value			Unit	
			V_{GE} [V] or V_{GS} [V]	V_r [V] or V_{CE} [V] or V_{DS} [V]	I_c [A] or I_F [A] or I_D [A]	T_j	Min	Typ	Max	

Inverter Transistor

Gate emitter threshold voltage	$V_{GE(th)}$	$V_{CE}=V_{GE}$			0,0008	$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	5	5,8	6,5	V
Collector-emitter saturation voltage	$V_{CE(sat)}$		15		50	$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	1,1	1,56 1,79	2,1	V
Collector-emitter cut-off current incl. Diode	I_{CES}		0	600		$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$			0,35	mA
Gate-emitter leakage current	I_{GES}		20	0		$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$			650	nA
Integrated Gate resistor	R_{gint}							none		Ω
Turn-on delay time	$t_{d(on)}$	$R_{goff}=8 \Omega$ $R_{gon}=8 \Omega$	± 15	300	50	$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		106 98		ns
Rise time	t_r					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		19 16		
Turn-off delay time	$t_{d(off)}$					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		150 173		
Fall time	t_f					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		89 115		
Turn-on energy loss per pulse	E_{on}					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		0,50 0,75		mWs
Turn-off energy loss per pulse	E_{off}					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		1,18 1,63		
Input capacitance	C_{ies}					$T_j=25^\circ\text{C}$		3140		
Output capacitance	C_{oss}	$f=1\text{MHz}$	0	25		$T_j=25^\circ\text{C}$		200		pF
Reverse transfer capacitance	C_{rss}							93		
Gate charge	Q_{Gate}	$V_{CC}=480$	± 15		50	$T_j=25^\circ\text{C}$		310		nC
Thermal resistance chip to heatsink per chip	R_{thJH}	Thermal foil thickness=76um Kunze foil KU-ALF5						0,68		K/W

Inverter Diode

Diode forward voltage	V_F				50	$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	1,2	1,63 1,60	2,1	V
Peak reverse recovery current	I_{RRM}	$R_{gon}=8 \Omega$	± 15	300	50	$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		28 79		A
Reverse recovery time	t_{rr}					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		144 147		ns
Reverse recovered charge	Q_{rr}					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		1,91 4,71		μC
Peak rate of fall of recovery current	$d(i/\text{rec})/\text{max dt}$					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		1357 4135		$\text{A}/\mu\text{s}$
Reverse recovered energy	E_{rec}					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		0,55 1,09		mWs
Thermal resistance chip to heatsink per chip	R_{thJH}	Thermal foil thickness=76um Kunze foil KU-ALF5						0,87		K/W

Thermistor

Rated resistance	R_{25}	Tol. ±5%				$T_j=25^\circ\text{C}$	4,46	4,7	4,94	kΩ
Deviation of R100	D_{RR}	$R_{100}=435\Omega$				$T_C=100^\circ\text{C}$		2,6		%/K
Power dissipation given Epcos-Typ	P					$T_j=25^\circ\text{C}$		210		mW
B-value	$B_{(25/100)}$	Tol. ±3%				$T_j=25^\circ\text{C}$		3530		K

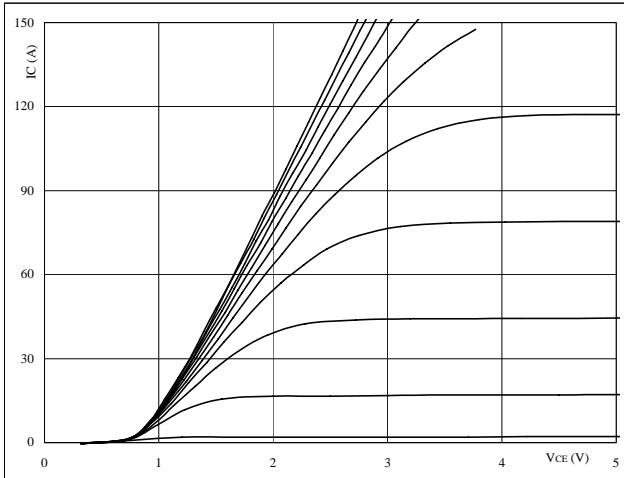
Output Inverter

Figure 1

Output inverter IGBT

Typical output characteristics

$$I_C = f(V_{CE})$$

**At**

$$t_p = 250 \mu\text{s}$$

$$T_j = 25^\circ\text{C}$$

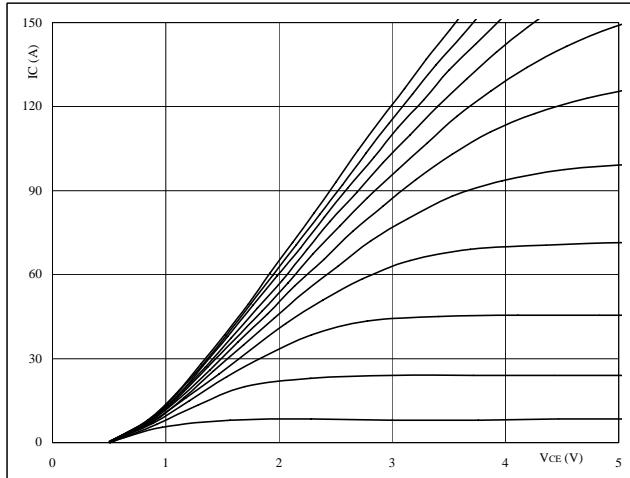
VGE from 7 V to 17 V in steps of 1 V

Figure 2

Output inverter IGBT

Typical output characteristics

$$I_C = f(V_{CE})$$

**At**

$$t_p = 250 \mu\text{s}$$

$$T_j = 150^\circ\text{C}$$

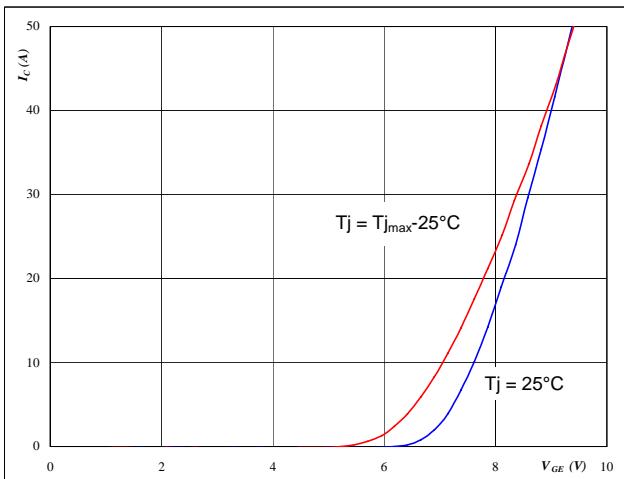
VGE from 7 V to 17 V in steps of 1 V

Figure 3

Output inverter IGBT

Typical transfer characteristics

$$I_C = f(V_{GE})$$

**At**

$$t_p = 250 \mu\text{s}$$

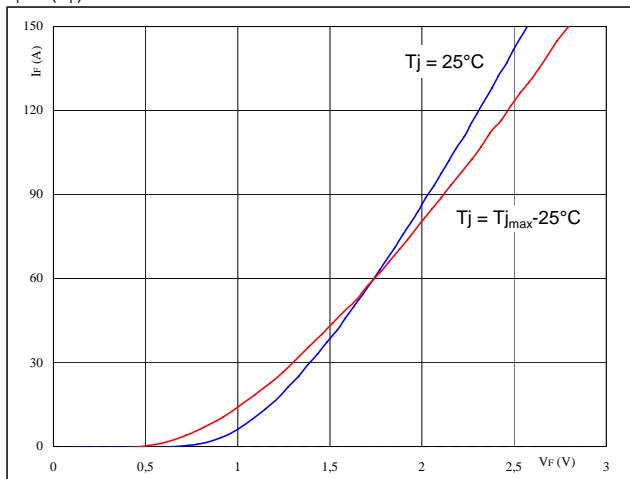
$$V_{CE} = 10 \text{ V}$$

Figure 4

Output inverter FRED

Typical diode forward current as a function of forward voltage

$$I_F = f(V_F)$$

**At**

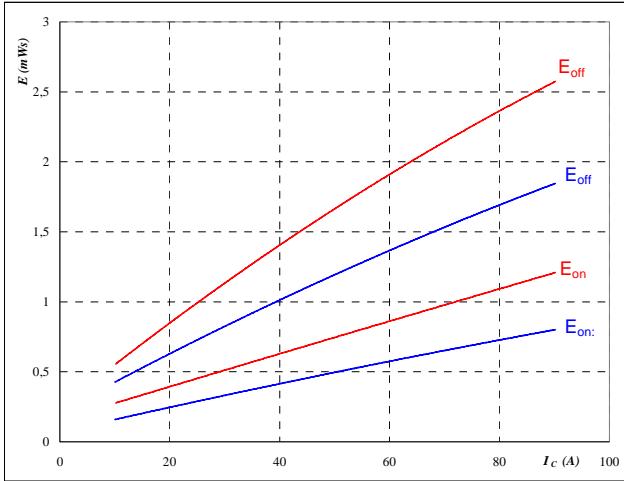
$$t_p = 250 \mu\text{s}$$

Output Inverter

Figure 5

**Typical switching energy losses
as a function of collector current**

$$E = f(I_C)$$



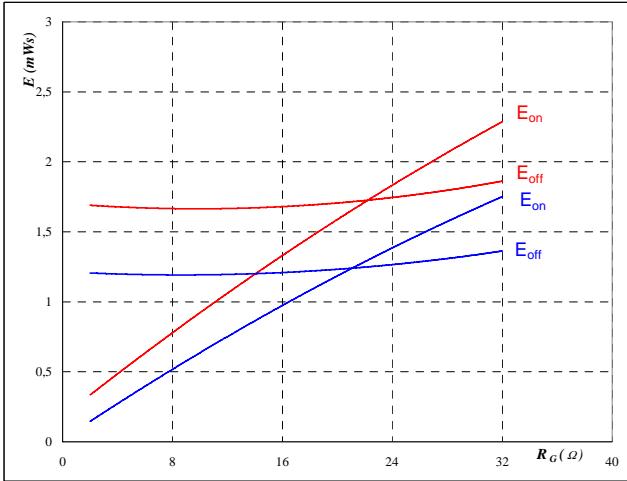
With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 300 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 8 \quad \Omega \\ R_{goff} &= 8 \quad \Omega \end{aligned}$$

Output inverter IGBT
Figure 6

**Typical switching energy losses
as a function of gate resistor**

$$E = f(R_G)$$



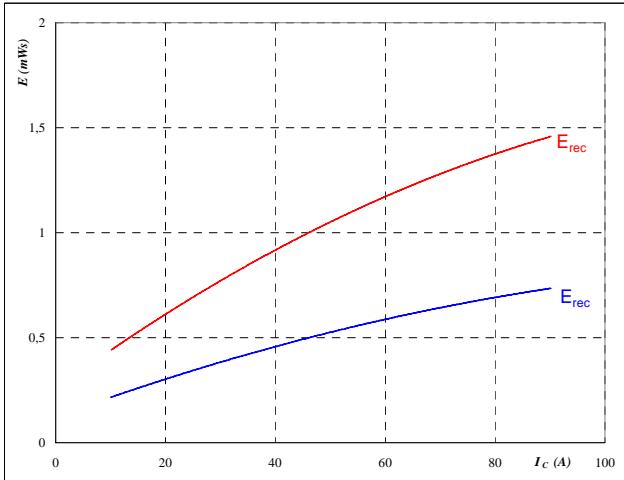
With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 300 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ I_C &= 50 \quad \text{A} \end{aligned}$$

Figure 7

**Typical reverse recovery energy loss
as a function of collector current**

$$E_{rec} = f(I_C)$$



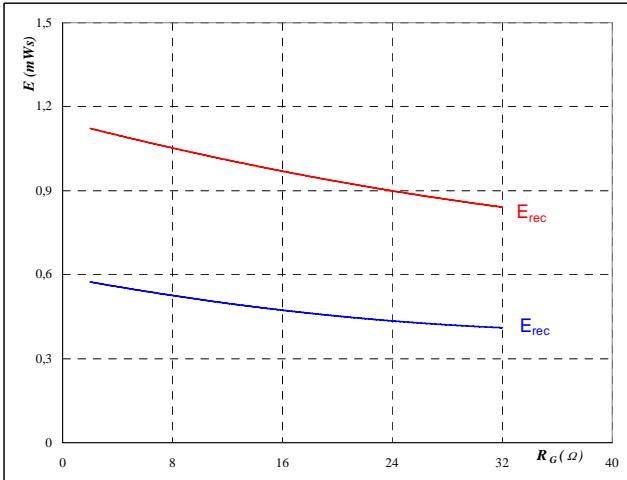
With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 300 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 8 \quad \Omega \end{aligned}$$

Output inverter IGBT
Figure 8

**Typical reverse recovery energy loss
as a function of gate resistor**

$$E_{rec} = f(R_G)$$



With an inductive load at

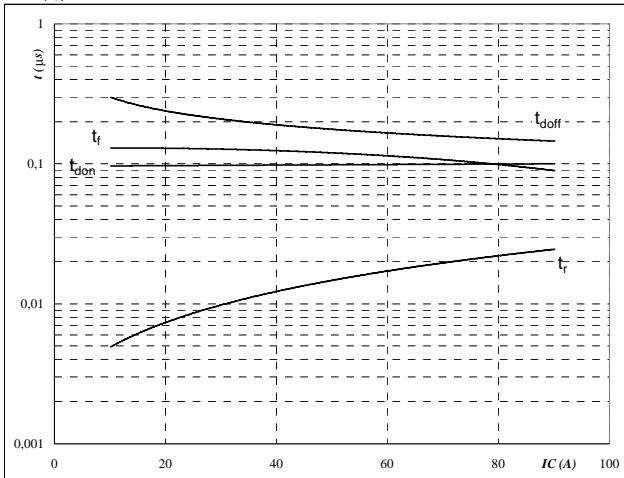
$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 300 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ I_C &= 50 \quad \text{A} \end{aligned}$$

Output Inverter

Figure 9

Typical switching times as a function of collector current

$$t = f(I_C)$$



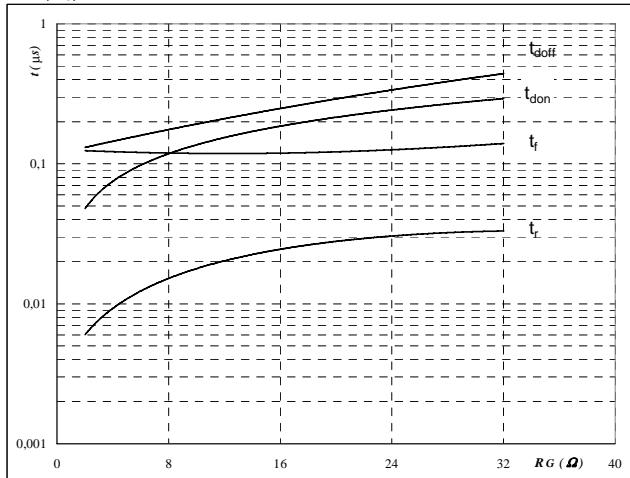
With an inductive load at

$$\begin{aligned} T_j &= 150 \quad ^\circ\text{C} \\ V_{CE} &= 300 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 8 \quad \Omega \\ R_{goff} &= 8 \quad \Omega \end{aligned}$$

Output inverter IGBT
Figure 10

Typical switching times as a function of gate resistor

$$t = f(R_G)$$



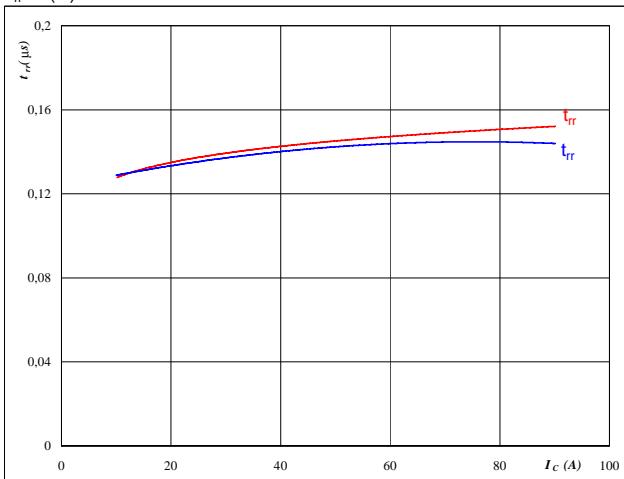
With an inductive load at

$$\begin{aligned} T_j &= 150 \quad ^\circ\text{C} \\ V_{CE} &= 300 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ I_C &= 50 \quad \text{A} \end{aligned}$$

Figure 11
Output inverter FRED

Typical reverse recovery time as a function of collector current

$$t_{rr} = f(I_C)$$



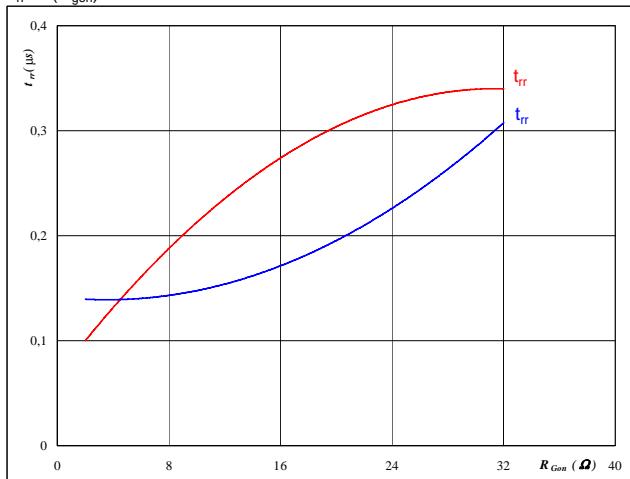
At

$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 300 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 8 \quad \Omega \end{aligned}$$

Figure 12
Output inverter FRED

Typical reverse recovery time as a function of IGBT turn on gate resistor

$$t_{rr} = f(R_{Gon})$$



At

$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_R &= 300 \quad \text{V} \\ I_F &= 50 \quad \text{A} \\ V_{GE} &= \pm 15 \quad \text{V} \end{aligned}$$

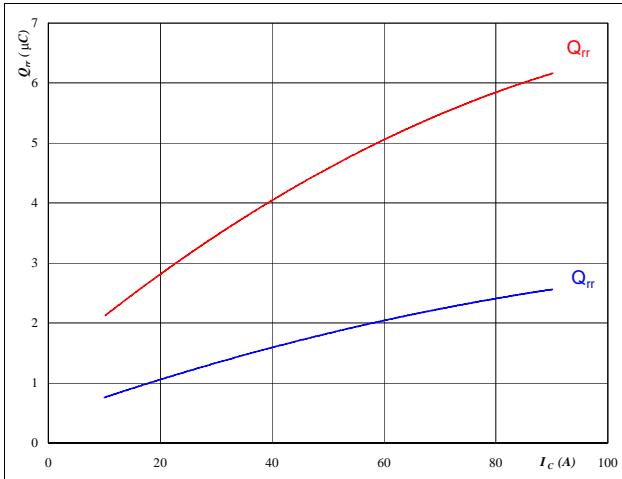
Output Inverter

Figure 13

Output inverter FRED

Typical reverse recovery charge as a function of collector current

$$Q_{rr} = f(I_c)$$


At

$$T_j = 25/150 \quad {}^\circ\text{C}$$

$$V_{CE} = 300 \quad \text{V}$$

$$V_{GE} = \pm 15 \quad \text{V}$$

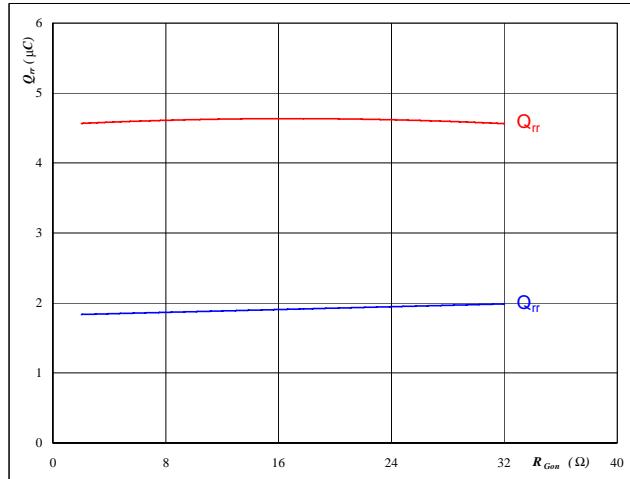
$$R_{gon} = 8 \quad \Omega$$

Figure 14

Output inverter FRED

Typical reverse recovery charge as a function of IGBT turn on gate resistor

$$Q_{rr} = f(R_{gon})$$


At

$$T_j = 25/150 \quad {}^\circ\text{C}$$

$$V_R = 300 \quad \text{V}$$

$$I_F = 50 \quad \text{A}$$

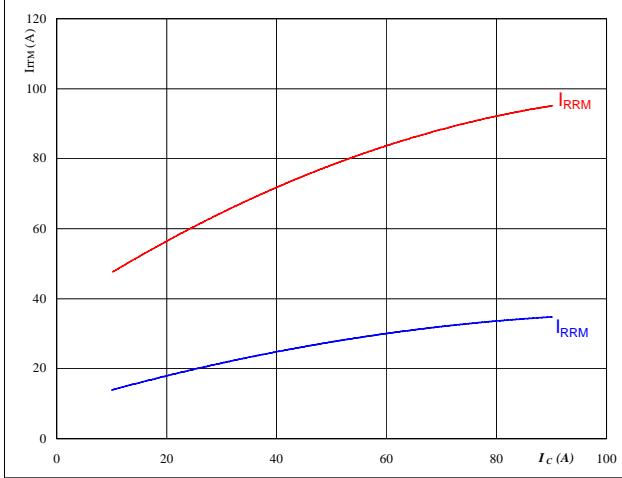
$$V_{GE} = \pm 15 \quad \text{V}$$

Figure 15

Output inverter FRED

Typical reverse recovery current as a function of collector current

$$I_{RRM} = f(I_c)$$


At

$$T_j = 25/150 \quad {}^\circ\text{C}$$

$$V_{CE} = 300 \quad \text{V}$$

$$V_{GE} = \pm 15 \quad \text{V}$$

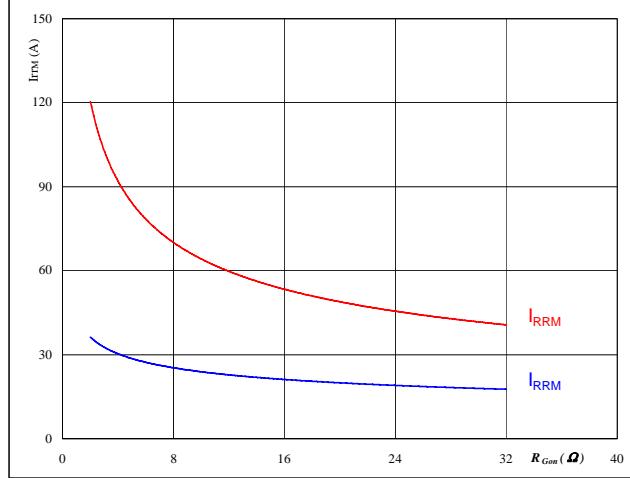
$$R_{gon} = 8 \quad \Omega$$

Figure 16

Output inverter FRED

Typical reverse recovery current as a function of IGBT turn on gate resistor

$$I_{RRM} = f(R_{gon})$$


At

$$T_j = 25/150 \quad {}^\circ\text{C}$$

$$V_R = 300 \quad \text{V}$$

$$I_F = 50 \quad \text{A}$$

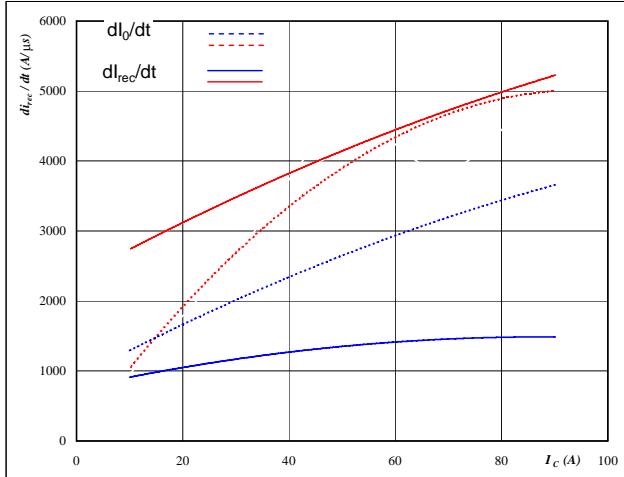
$$V_{GE} = \pm 15 \quad \text{V}$$

Output Inverter

Figure 17

Output inverter FRED

Typical rate of fall of forward and reverse recovery current as a function of collector current
 $dI_0/dt, dI_{rec}/dt = f(I_C)$

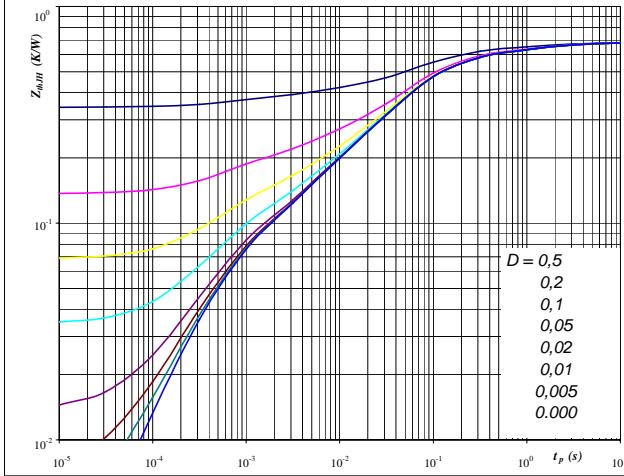
**At**

$T_j = 25/150 \text{ } ^\circ\text{C}$
 $V_{CE} = 300 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{Gon} = 8 \text{ } \Omega$

Figure 19

Output inverter IGBT

IGBT transient thermal impedance as a function of pulse width
 $Z_{thJH} = f(t_p)$

**At**

$D = tp / T$
 $R_{thJH} = 0,68 \text{ K/W}$

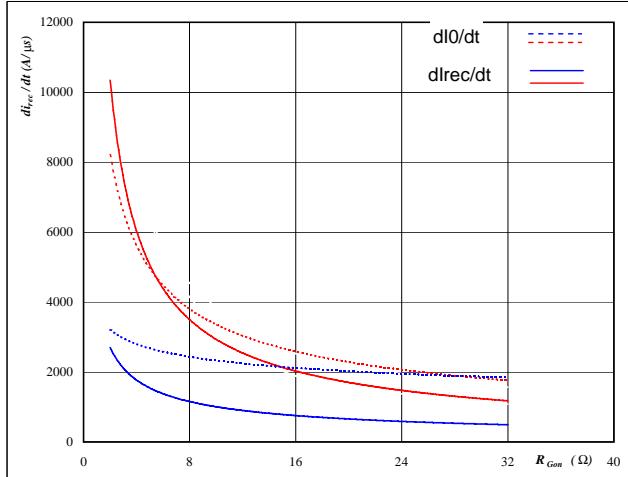
IGBT thermal model values

R (C/W)	Tau (s)
0,02	9,9E+00
0,08	1,2E+00
0,18	1,5E-01
0,26	4,2E-02
0,08	4,6E-03
0,06	5,2E-04

Figure 18

Output inverter FRED

Typical rate of fall of forward and reverse recovery current as a function of IGBT turn on gate resistor
 $dI_0/dt, dI_{rec}/dt = f(R_{gon})$

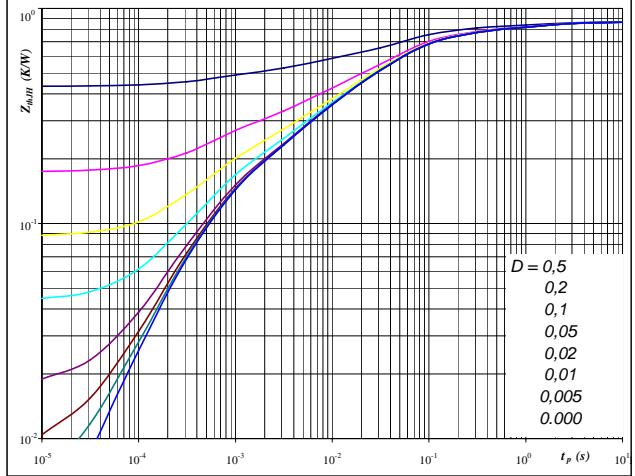
**At**

$T_j = 25/150 \text{ } ^\circ\text{C}$
 $V_R = 300 \text{ V}$
 $I_F = 50 \text{ A}$
 $V_{GE} = \pm 15 \text{ V}$

Figure 20

Output inverter FRED

FRED transient thermal impedance as a function of pulse width
 $Z_{thJH} = f(t_p)$

**At**

$D = tp / T$
 $R_{thJH} = 0,87 \text{ K/W}$

FRED thermal model values

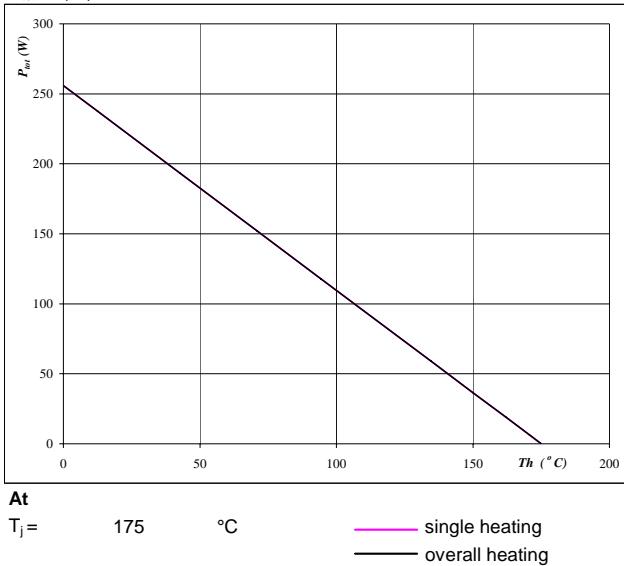
R (C/W)	Tau (s)
0,02	9,5E+00
0,08	1,1E+00
0,15	1,4E-01
0,35	3,2E-02
0,15	4,1E-03
0,11	5,0E-04

Output Inverter

Figure 21

Power dissipation as a function of heatsink temperature

$$P_{\text{tot}} = f(T_h)$$

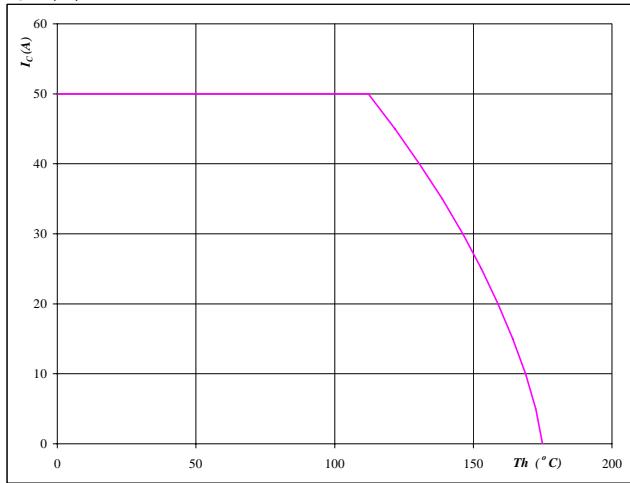
**At**

$$T_j = 175 \quad {}^\circ\text{C}$$

Output inverter IGBT**Figure 22**

Collector current as a function of heatsink temperature

$$I_C = f(T_h)$$

**At**

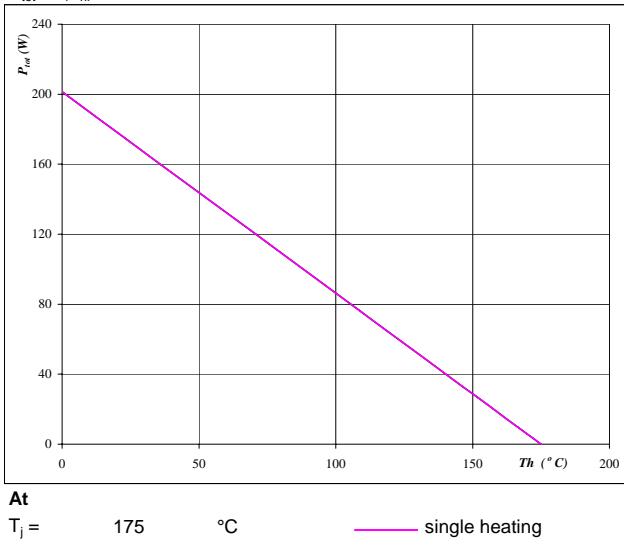
$$T_j = 175 \quad {}^\circ\text{C}$$

$$V_{GE} = 15 \quad \text{V}$$

Figure 23

Power dissipation as a function of heatsink temperature

$$P_{\text{tot}} = f(T_h)$$

Output inverter FRED**At**

$$T_j = 175 \quad {}^\circ\text{C}$$

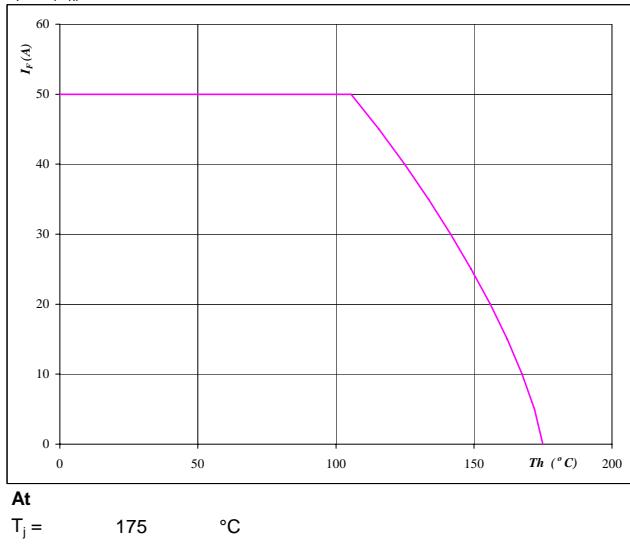
$$\text{single heating}$$

$$\text{overall heating}$$

Figure 24

Forward current as a function of heatsink temperature

$$I_F = f(T_h)$$

Output inverter FRED**At**

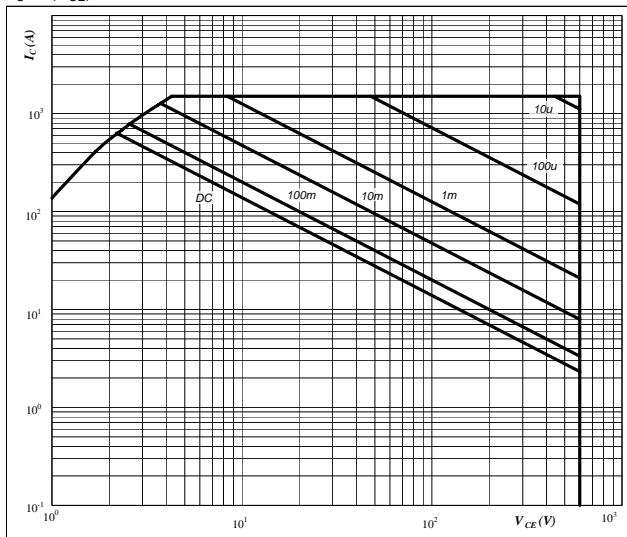
$$T_j = 175 \quad {}^\circ\text{C}$$

Output Inverter

Figure 25

**Safe operating area as a function
of collector-emitter voltage**

$$I_C = f(V_{CE})$$

**At**

D = single pulse

Th = 80 °C

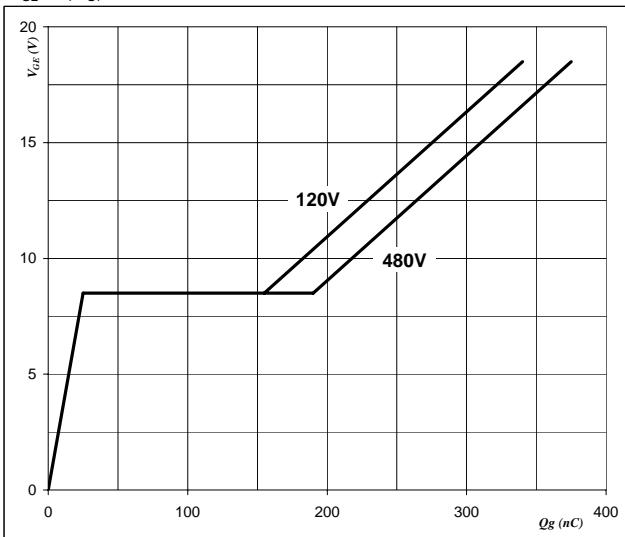
V_{GE} = ±15 V

T_j = T_{jmax} °C

Figure 26

Gate voltage vs Gate charge

$$V_{GE} = f(Qg)$$

**At**

I_C = 50 A

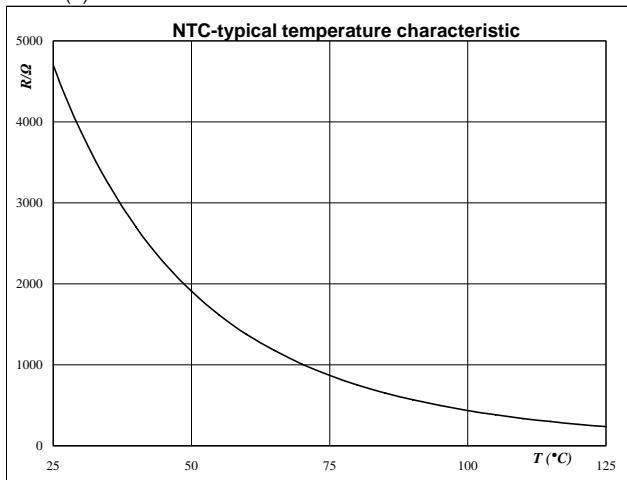
Thermistor

Figure 1

Thermistor

Typical NTC characteristic
as a function of temperature

$$R_T = f(T)$$



Switching Definitions Output Inverter

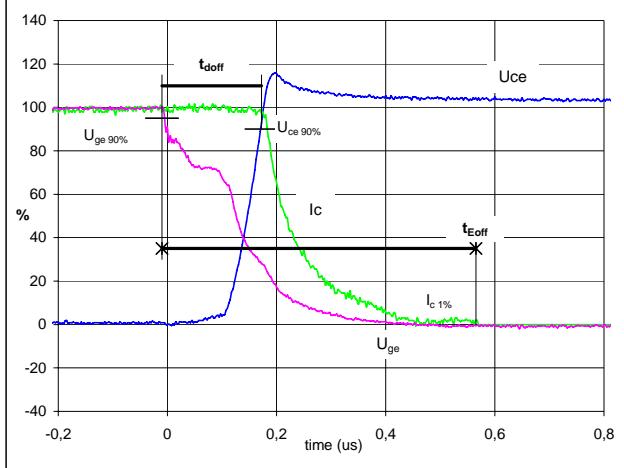
General conditions

T_j	=	150 °C
R_{gon}	=	8 Ω
R_{goff}	=	8 Ω

Figure 1

Output inverter IGBT

Turn-off Switching Waveforms & definition of t_{doff} , t_{Eoff}
 $(t_{Eoff} = \text{integrating time for } E_{off})$

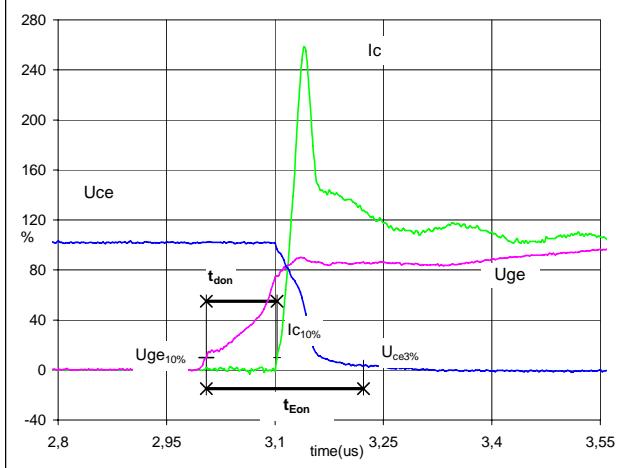


$V_{GE}(0\%) = -15$ V
 $V_{GE}(100\%) = 15$ V
 $V_C(100\%) = 300$ V
 $I_C(100\%) = 50$ A
 $t_{doff} = 0,17$ μs
 $t_{Eoff} = 0,58$ μs

Figure 2

Output inverter IGBT

Turn-on Switching Waveforms & definition of t_{don} , t_{Eon}
 $(t_{Eon} = \text{integrating time for } E_{on})$

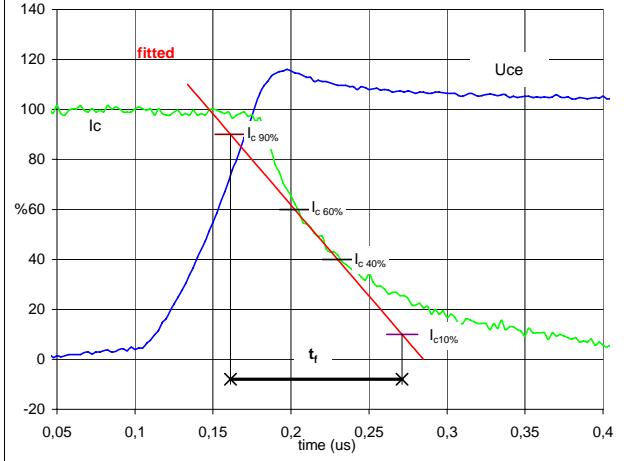


$V_{GE}(0\%) = -15$ V
 $V_{GE}(100\%) = 15$ V
 $V_C(100\%) = 300$ V
 $I_C(100\%) = 50$ A
 $t_{don} = 0,10$ μs
 $t_{Eon} = 0,22$ μs

Figure 3

Output inverter IGBT

Turn-off Switching Waveforms & definition of t_f

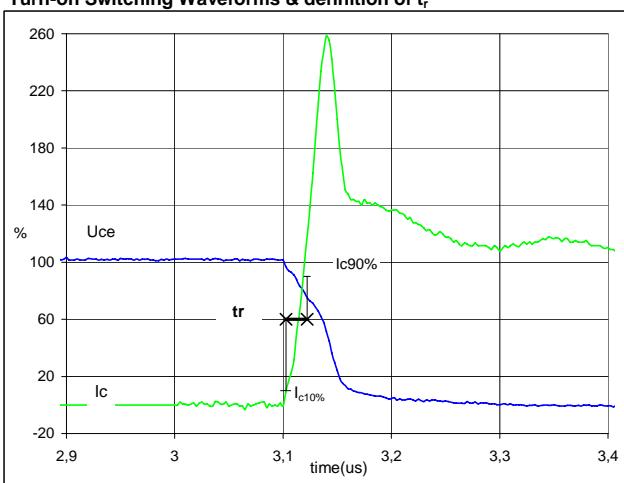


$V_C(100\%) = 300$ V
 $I_C(100\%) = 50$ A
 $t_f = 0,12$ μs

Figure 4

Output inverter IGBT

Turn-on Switching Waveforms & definition of t_r

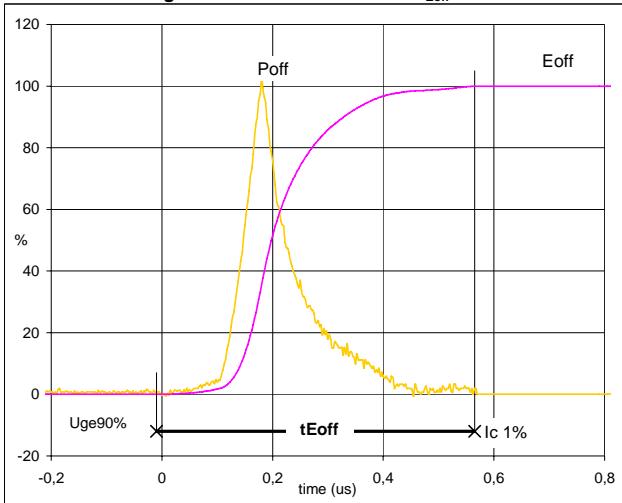


$V_C(100\%) = 300$ V
 $I_C(100\%) = 50$ A
 $t_r = 0,02$ μs

Switching Definitions Output Inverter

Figure 5

Output inverter IGBT

Turn-off Switching Waveforms & definition of t_{Eoff} 

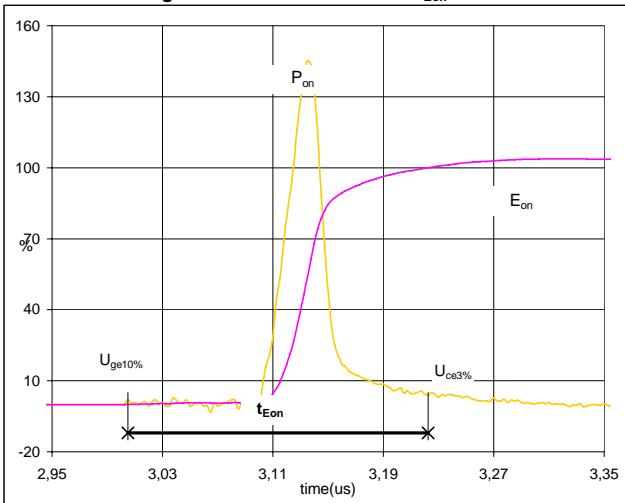
$$P_{off} (100\%) = 15,03 \text{ kW}$$

$$E_{off} (100\%) = 1,63 \text{ mJ}$$

$$t_{Eoff} = 0,58 \mu\text{s}$$

Figure 6

Output inverter IGBT

Turn-on Switching Waveforms & definition of t_{Eon} 

$$P_{on} (100\%) = 15,03 \text{ kW}$$

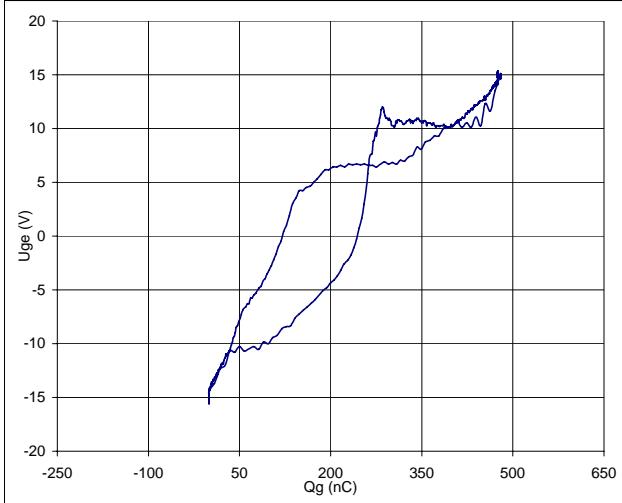
$$E_{on} (100\%) = 0,75 \text{ mJ}$$

$$t_{Eon} = 0,22 \mu\text{s}$$

Figure 7

Output inverter FRED

Gate voltage vs Gate charge (measured)



$$V_{GEoff} = -15 \text{ V}$$

$$V_{GEon} = 15 \text{ V}$$

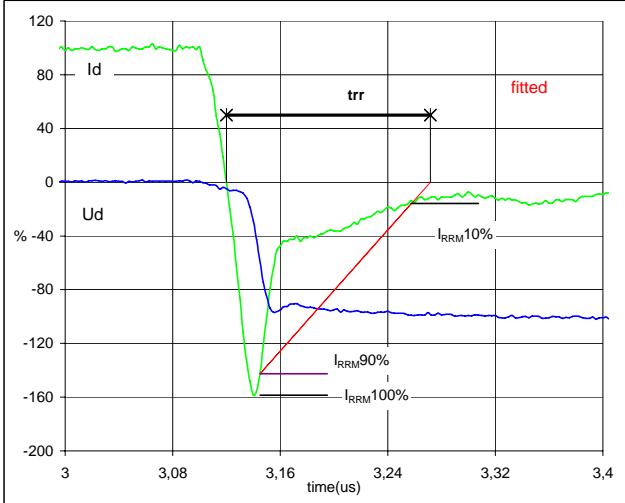
$$V_C (100\%) = 300 \text{ V}$$

$$I_C (100\%) = 50 \text{ A}$$

$$Q_g = 479,76 \text{ nC}$$

Figure 8

Output inverter IGBT

Turn-off Switching Waveforms & definition of t_{trr} 

$$V_d (100\%) = 300 \text{ V}$$

$$I_d (100\%) = 50 \text{ A}$$

$$I_{RRM} (100\%) = -79 \text{ A}$$

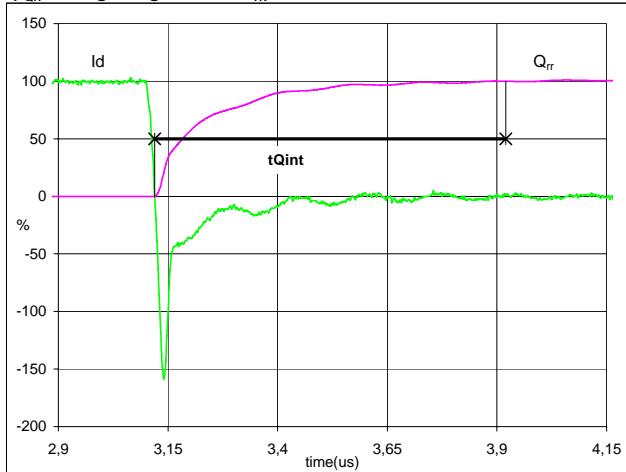
$$t_{trr} = 0,15 \mu\text{s}$$

Switching Definitions Output Inverter

Figure 9

Output inverter FRED

Turn-on Switching Waveforms & definition of t_{Qrr}
 $(t_{Qrr} = \text{integrating time for } Q_{rr})$

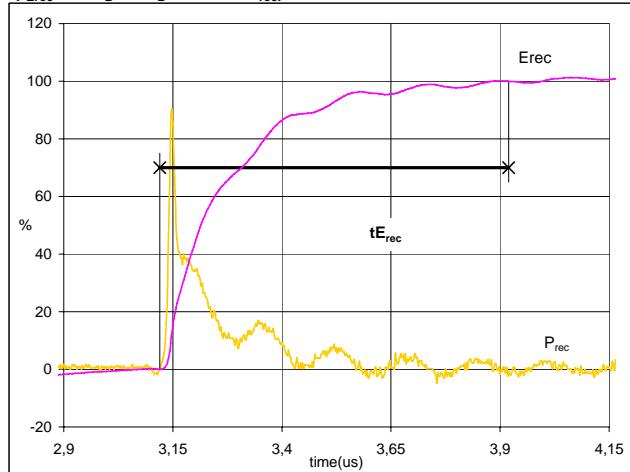


$I_d(100\%) = 50 \text{ A}$
 $Q_{rr}(100\%) = 4,71 \mu\text{C}$
 $t_{Qint} = 0,80 \mu\text{s}$

Figure 10

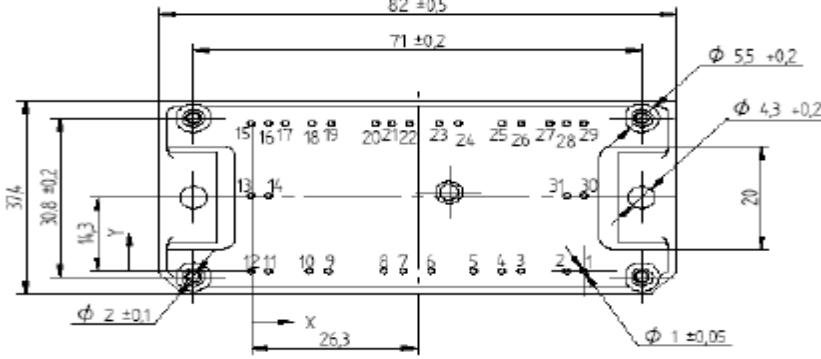
Output inverter FRED

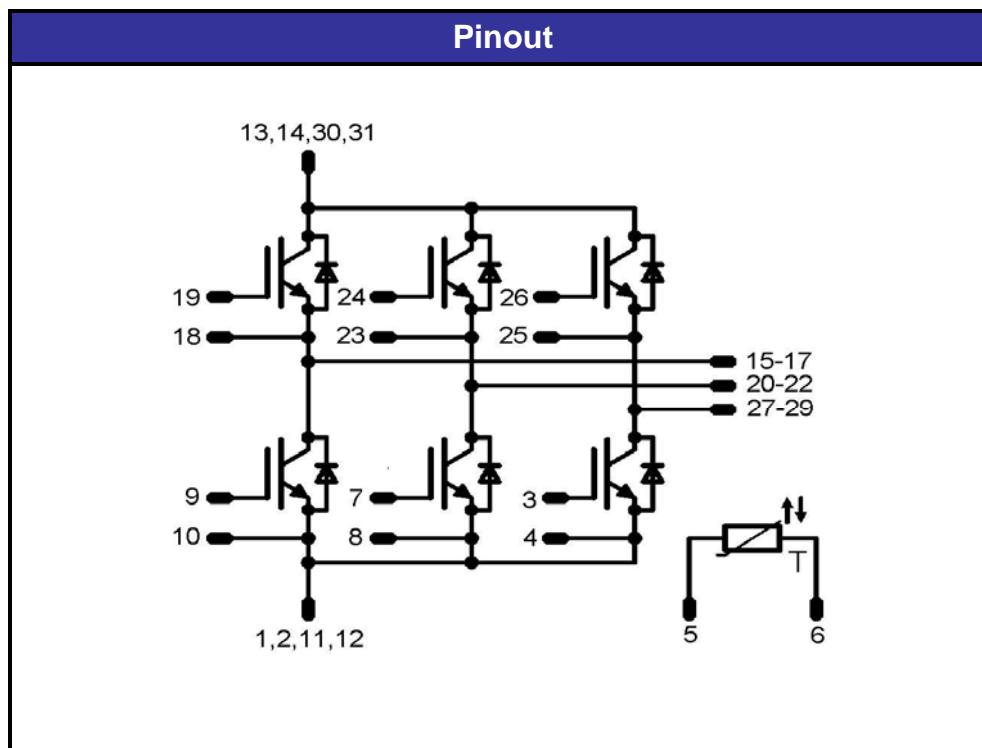
Turn-on Switching Waveforms & definition of t_{Erec}
 $(t_{Erec} = \text{integrating time for } E_{rec})$



$P_{rec}(100\%) = 15,03 \text{ kW}$
 $E_{rec}(100\%) = 1,09 \text{ mJ}$
 $t_{Erec} = 0,80 \mu\text{s}$

Package Outline and Pinout

Outline											
											
Pin Table											
Pin	X	Y	Pin	X	Y	Pin	X	Y	Pin	X	Y
1	52,6	0	9	12,2	0	17	5,4	28,6	25	39,7	28,6
2	49,9	0	10	9,2	0	18	9,6	28,6	26	42,7	28,6
3	42,65	0	11	2,7	0	19	12,6	28,6	27	47,2	28,6
4	39,65	0	12	0	0	20	19,6	28,6	28	49,9	28,6
5	35,15	0	13	0	14,65	21	22,3	28,6	29	52,6	28,6
6	28,4	0	14	2,7	14,65	22	25	28,6	30	52,6	14,65
7	24	0	15	0	28,6	23	29,7	28,6	31	49,9	14,65
8	21	0	16	2,7	28,6	24	32,7	28,6			



PRODUCT STATUS DEFINITIONS

Datasheet Status	Product Status	Definition
Target	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice. The data contained is exclusively intended for technically trained staff.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data may be published at a later date. Vincotech reserves the right to make changes at any time without notice in order to improve design. The data contained is exclusively intended for technically trained staff.
Final	Full Production	This datasheet contains final specifications. Vincotech reserves the right to make changes at any time without notice in order to improve design. The data contained is exclusively intended for technically trained staff.

DISCLAIMER

The information given in this datasheet describes the type of component and does not represent assured characteristics. For tested values please contact Vincotech. Vincotech reserves the right to make changes without further notice to any products herein to improve reliability, function or design. Vincotech does not assume any liability arising out of the application or use of any product or circuit described herein; neither does it convey any license under its patent rights, nor the rights of others.

LIFE SUPPORT POLICY

Vincotech products are not authorised for use as critical components in life support devices or systems without the express written approval of Vincotech.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in labelling can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.