TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SX04BFE

Digital Clock-Buffer with High-pass-filter, linear-amplifier and Digital-output-buffer

Features

• High speed operation : f_{IN}= 0.032 to 80 MHz

at square-wave,

input rise and fall time = 0 to 10 ns/V

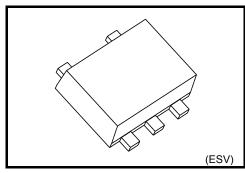
: Slew rate = 0.53 V/ns (min)

at V_{CC} = 1.65 to 1.95 V, C_L =25 pF

• Operating voltage range : V_{CC} = 1.65 to 3.6 V

Output drive capability :Fan-out (Load capacitance) = 25 pF

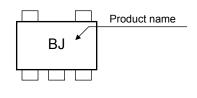
• 3.6-V tolerant inputs.

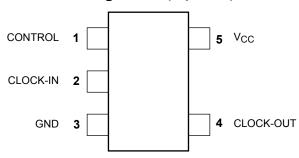


Weight: 0.003 g (Typ.)

Marking

Pin Assignment (top view)





Absolute Maximum Ratings (Ta = 25°C)

| Characteristic | Symbol | Rating | Unit |
|------------------------------|------------------|-------------------------------|------|
| Supply voltage | V _{CC} | −0.5 to 4.6 | V |
| Input voltage | V _{IN} | −0.5 to 4.6 | V |
| Output voltage | V _{OUT} | -0.5 to V _{CC} + 0.5 | V |
| Input diode current | lık | -20 | mA |
| Output diode current | lok | ±20 (Note 1) | mA |
| Output current | Гоит | ±50 | mA |
| V _{CC} /GND current | Icc | ±50 | mA |
| Power dissipation | PD | 150 | mW |
| Storage temperature | T _{stg} | −65 to 150 | °C |

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: V_{OUT} < GND, V_{OUT} > V_{CC}

Start of commercial production 2011-05

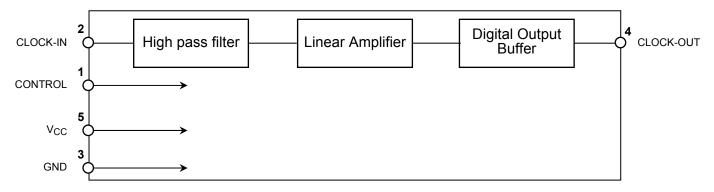


Truth Table

| INF | PUT | OUTPUT | Function | |
|---------|----------|-----------|----------|--|
| CONTROL | CLOCK-IN | CLOCK-OUT | | |
| L | L X | | Disable | |
| Н | 7_ | Н | Enable | |
| Н | \ | L | Enable | |

X: Don't care

System Diagram



Operating Ranges

| Characteristic | Symbol | Rating | Unit | |
|---------------------------------------|-------------------|----------------------|------|--|
| Supply voltage | V _{CC} | 1.65 to 3.6 | V | |
| CONTROL Input voltage | V _{IN} | 0 to 3.6 | V | |
| CLOCK-IN Input voltage (peak to peak) | Via | 0.7 to 3.6 (Note 2) | V | |
| CLOCK-IN Input voltage (peak to peak) | V _{ICpp} | 0 to 3.6 (Note 3) | | |
| Output voltage | Vout | 0 to V _{CC} | V | |
| Operating temperature | T _{opr} | −40 to 85 | °C | |
| Clock input frequency (Square-wave) | f _{IN} | 0.032 to 80 (Note 4) | MHz | |
| Clock input frequency (Sine-wave) | f _{IN} | 12 to 80 | MHz | |

Note 2: CONTROL = H level state

Note 3: CONTROL = L level state

Note 4: Input rise and fall time = 0 to 10 ns/V

Electrical Characteristics

DC Characteristics

| Characteristic Symbol T | | C: make al | _ | Tank Complition | | Ta = 25°C | | | Ta = -40 to 85°C | | Unit |
|--------------------------------|--------|---------------------------------|---------------------|--|------------------------|-----------------------|------|------------------------|--------------------------|------------------------|------|
| | | est Condition | V _{CC} (V) | Min | Тур. | Max | Min | Max | Unit | | |
| High | V | | | 1.65 to 1.95 | V _{CC} × 0.75 | _ | _ | V _{CC} × 0.75 | _ | | |
| CONTROL | Level | V _{IH} | _ | | 2.3 to 3.6 | V _{CC} × 0.7 | _ | _ | V _{CC} × 0.7 | _ | V |
| Input voltage | Low | V _{IL} | | | 1.65 to 1.95 | ı | ı | V _{CC} × 0.25 | | V _{CC} × 0.25 | V |
| | Level | VIL | | _ | 2.3 to 3.6 | ı | ı | V _{CC} × 0.3 | | V _{CC} × 0.3 | |
| | | | | I _{OH} = -100 μA | 1.65 | 1.55 | 1.65 | _ | 1.55 | ı | . V |
| | | | | | 2.3 | 2.2 | 2.3 | _ | 2.2 | | |
| | High | | | | 3.0 | 2.9 | 3.0 | _ | 2.9 | | |
| | Level | | | $I_{OH} = -4 \text{ mA}$ | 1.65 | 1.29 | 1.52 | _ | 1.29 | | |
| | | | | $I_{OH} = -8 \text{ mA}$ | 2.3 | 1.9 | 2.15 | _ | 1.9 | | |
| Output voltage | | | | $I_{OH} = -24 \text{ mA}$ | 3.0 | 2.3 | 2.68 | _ | 2.3 | | |
| Output voltage | | | _ | $I_{OL} = 100 \ \mu A$ | 1.65 | | 0 | 0.1 | | 0.1 | |
| Low Level | | | | | 2.3 | | 0 | 0.1 | | 0.1 | |
| | - | | | | 3.0 | _ | 0 | 0.1 | _ | 0.1 | |
| | Level | | | $I_{OL} = 4 \text{ mA}$ $I_{OL} = 8 \text{ mA}$ | 1.65 | _ | 0.08 | 0.24 | _ | 0.24 | |
| | | | | | 2.3 | _ | 0.1 | 0.3 | _ | 0.3 | |
| | | | | I _{OL} = 24 mA | 3.0 | _ | 0.22 | 0.55 | _ | 0.55 | |
| Input leakage c | urrent | I _{IN} | V _{IN(CC} | ontrol) = 0 to 3.6V | 0 to 3.6 | _ | _ | ±1 | _ | ±10 | μΑ |
| Quiescent supply current Icc | | ONTROL) = VIL OCK-IN) = OPEN | 3.6 | _ | _ | ±1 | _ | ±10 | μА | | |

3 2014-03-01

AC Characteristics (unless otherwise specified, Input: square-wave, $t_r = t_f = 3ns$)

| Characteristic | Symbol | Test Condition | | Ta = 25°C | | | Ta = −40 to 85°C | | Unit |
|-------------------------------|---------------------|---|---------------------|-----------|------|-----|---------------------|-----|--------|
| | | | V _{CC} (V) | Min | Тур. | Max | Min | Max | |
| | | $C_L = 5 \text{ pF},$ $R_L = 1 \text{ M}\Omega$ | 1.65 to 1.95 | _ | 5.2 | _ | _ | 7.4 | |
| | | | 2.3 to 2.7 | _ | 3.0 | _ | _ | 4.8 | |
| Propagation delay time | t _{pLH} | | 3.0 to 3.6 | _ | 2.3 | _ | _ | 4.1 | ns |
| Tropagation delay time | t _{pHL} | | 1.65 to 1.95 | _ | 5.4 | _ | _ | 8.7 | |
| | | $C_L = 25 \text{ pF},$ $R_L = 1 \text{ M}\Omega$ | 2.3 to 2.7 | _ | 3.3 | _ | _ | 5.3 | |
| | | | 3.0 to 3.6 | _ | 2.6 | _ | _ | 4.6 | |
| Slew rate | SR | $C_L = 5 \text{ pF},$ $R_L = 1 \text{ M}\Omega$ | 1.65 to 1.95 | _ | _ | _ | 1.02 | _ | - V/ns |
| | | | 2.3 to 2.7 | _ | _ | _ | 1.42 | _ | |
| | | | 3.0 to 3.6 | _ | _ | _ | 1.85 | _ | |
| | | $C_L = 25 \text{ pF},$ $R_L = 1 \text{ M}\Omega$ | 1.65 to 1.95 | _ | _ | _ | 0.53 | _ | |
| | | | 2.3 to 2.7 | _ | _ | _ | 0.74 | _ | |
| | | | 3.0 to 3.6 | _ | _ | _ | 0.96 | _ | |
| Input capacitance (CONTROL) | C _{IN-CNT} | _ | 3.3 | _ | 2.1 | _ | _ | _ | pF |
| Input capacitance (CLOCK-IN) | C _{IN-CKI} | _ | 3.3 | 1 | 3.3 | | | _ | pF |
| Power dissipation capacitance | C _{PD} | (Note 5) | 1.8 | 1 | 24 | | _ | _ | nE |
| | | | 3.3 | - | 29 | _ | _ | _ | pF |

Note 5: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

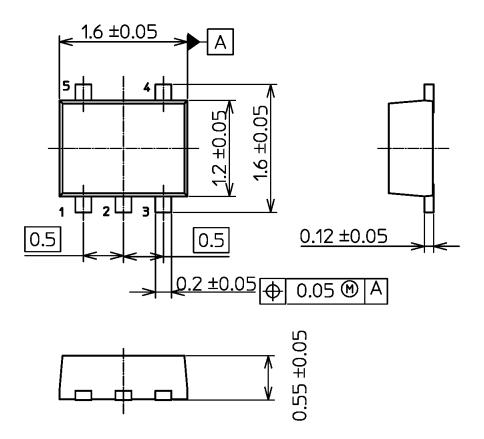
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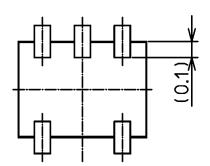
Average operating current can be obtained by the equation:

 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

Package Dimensions

Unit: mm





BOTTOM VIEW

5

Weight: 0.003 g (Typ.)

2014-03-01

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