



25N10

Power MOSFET

N-CHANNEL ENHANCEMENT MODE POWER MOSFET

DESCRIPTION

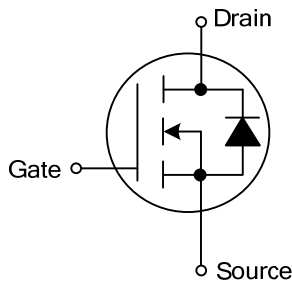
The UTC **25N10** is an N-channel enhancement mode power MOSFET and it uses UTC's perfect technology to provide designers with fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

It is generally suitable for all commercial-industrial applications and DC/DC converters requiring low voltage.

FEATURES

- * Single Drive Requirement
- * Low Gate Charge
- * RoHS Compliant

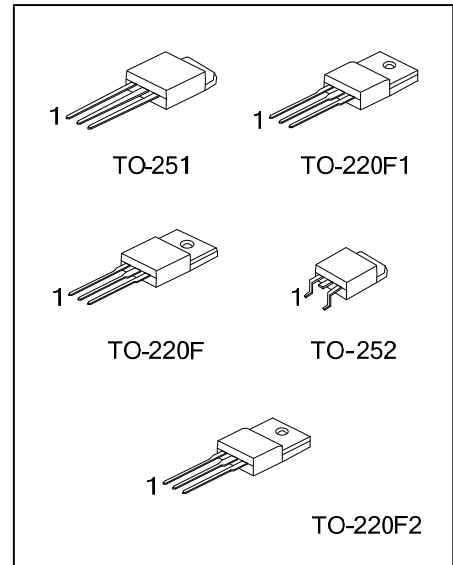
SYMBOL



ORDERING INFORMATION

| Ordering Number | | Package | Pin Assignment | | | Packing |
|-------------------|--------------|----------|----------------|---|---|-----------|
| Lead Free Plating | Halogen Free | | 1 | 2 | 3 | |
| 25N10L-TF1-T | 25N10G-TF1-T | TO-220F1 | G | D | S | Tube |
| 25N10L-TF2-T | 25N10G-TF2-T | TO-220F2 | G | D | S | Tube |
| 25N10L-TF3-T | 25N10G-TF3-T | TO-220F | G | D | S | Tube |
| 25N10L-TM3-T | 25N10G-TM3-T | TO-251 | G | D | S | Tube |
| 25N10L-TN3-R | 25N10G-TN3-R | TO-252 | G | D | S | Tape Reel |

| | |
|---|---|
| <p>25N10L-TF1-T</p> <p>(1)Packing Type</p> <p>(2)Package Type</p> <p>(3)Lead Free</p> | <p>(1) T: Tube, R: Tape Reel</p> <p>(2) TF1: TO-220F1, TF2: TO-220F2, TF3: TO-220F</p> <p>TM3: TO-251, TN3: TO-252</p> <p>(3) L: Lead Free, G: Halogen Free</p> |
|---|---|



MARKING INFORMATION

| PACKAGE | MARKING |
|--|---|
| TO-220F1 TO-220F2 TO-220 TO-251 TO-252 | <div><div>UTC 25N10</div><div>Lot Code</div><div>1</div><div>L: Lead Free G: Halogen Free Data Code</div></div> |

■ ABSOLUTE MAXIMUM RATINGS

| PARAMETER | | SYMBOL | RATINGS | UNIT |
|--|--------------------|-----------|-----------------|-------------|
| Drain Source Voltage | | V_{DSS} | 100 | V |
| Gate Source Voltage | | V_{GSS} | ± 20 | V |
| Continuous Drain Current ($V_{GS}=10V$) | $T_C=25^{\circ}C$ | I_D | 23 | A |
| | $T_C=100^{\circ}C$ | I_D | 14.6 | A |
| Pulsed Drain Current (Note 2) | | I_{DM} | 80 | A |
| Total Power Dissipation ($T_C=25^{\circ}C$) | TO-220F/TO-220F1 | P_D | 50 | W |
| | TO-220F2 | | 52 | |
| | TO-251/TO-252 | | 41 | |
| Operating Junction Temperature | | T_J | $-55 \sim +150$ | $^{\circ}C$ |
| Storage Temperature | | T_{STG} | $-55 \sim +150$ | $^{\circ}C$ |

Note: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Pulse width limited by max. junction temperature

■ THERMAL DATA

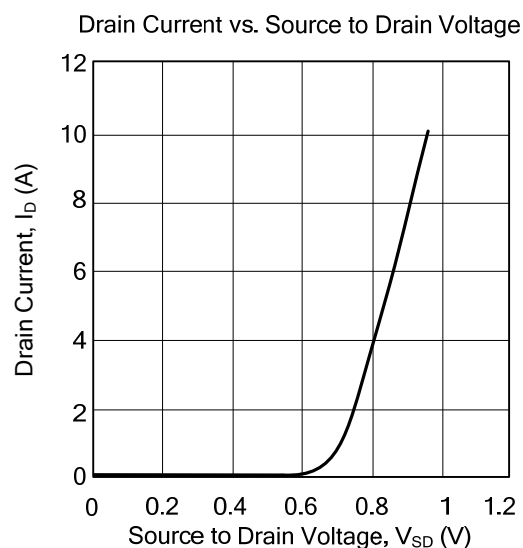
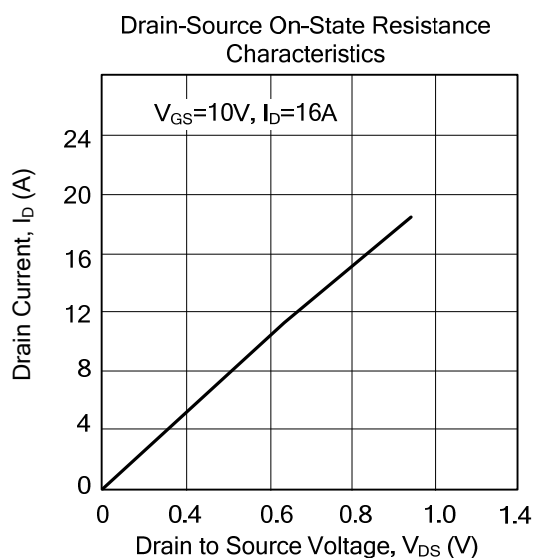
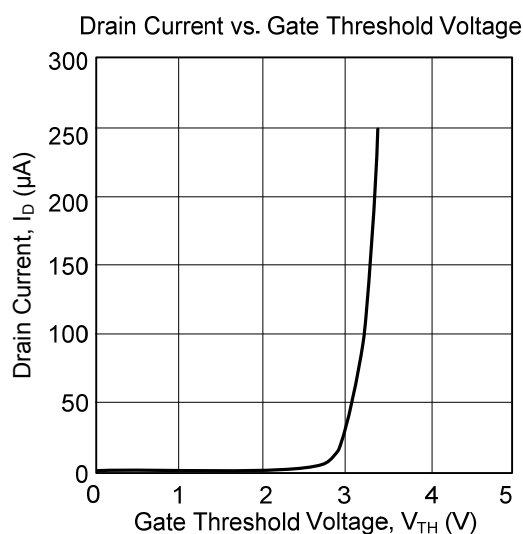
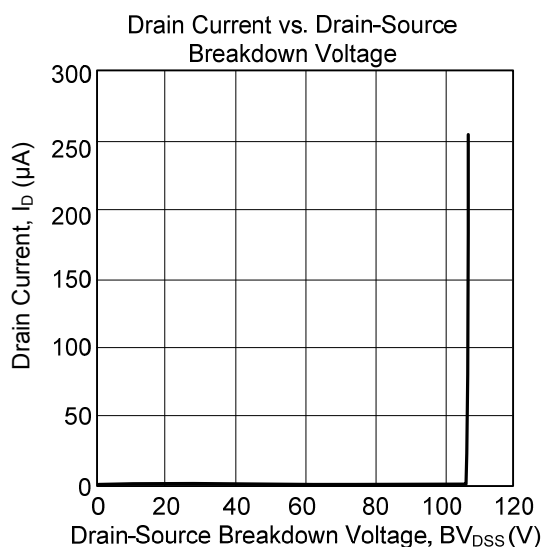
| PARAMETER | | SYMBOL | RATINGS | UNIT |
|---------------------|------------------|---------------|---------|---------------|
| Junction to Ambient | TO-220F/TO-220F1 | θ_{JA} | 62.5 | $^{\circ}C/W$ |
| | TO-220F2 | | | |
| | TO-251/TO-252 | | 100 | |
| Junction to Case | TO-220F/TO-220F1 | θ_{JC} | 2.5 | $^{\circ}C/W$ |
| | TO-220F2 | | 2.4 | |
| | TO-251/TO-252 | | 3 | |

■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

| PARAMETER | SYMBOL | TEST CONDITIONS | MIN | TYP | MAX | UNIT |
|---|-------------------------------------|--|-----|------|------|------|
| OFF CHARACTERISTICS | | | | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | V _{GS} =0V, I _D =1mA | 100 | | | V |
| Breakdown Voltage Temperature Coefficient | ΔBV _{DSS} /ΔT _J | Reference to 25°C , I _D =1mA | | 0.14 | | V/°C |
| Drain-Source Leakage Current | I _{DSS} | V _{DS} =100V, V _{GS} =0V, T _J =25°C | | | 25 | μA |
| | | V _{DS} =80V, V _{GS} =0V,T _J =150°C | | | 100 | μA |
| Gate-Source Leakage Current | I _{GSS} | V _{GS} =±20V | | | ±100 | nA |
| ON CHARACTERISTICS | | | | | | |
| Gate Threshold Voltage | V _{GS(TH)} | V _{DS} =V _{GS} , I _D =250μA | 2 | | 4 | V |
| Static Drain-Source On-Resistance (Note) | R _{DS(ON)} | V _{GS} =10V, I _D =16A | | | 80 | mΩ |
| Forward Transconductance | g _{FS} | V _{DS} =10V, I _D =16A | | 14 | | S |
| DYNAMIC PARAMETERS | | | | | | |
| Input Capacitance | C _{ISS} | V _{DS} =25V, V _{GS} =0V, f=1.0MHz | | 1060 | 1700 | pF |
| Output Capacitance | C _{OSS} | | | 270 | | pF |
| Reverse Transfer Capacitance | C _{RSS} | | | 8 | | pF |
| Gate Resistance | R _G | f=1.0MHz | | 1.5 | 2.3 | Ω |
| SWITCHING PARAMETERS | | | | | | |
| Total Gate Charge (Note) | Q _G | V _{GS} =10V, V _{DS} =80V, I _D =16A | | 19 | 30 | nC |
| Gate Source Charge | Q _{GS} | | | 5 | | nC |
| Gate Drain Charge | Q _{GD} | | | 6 | | nC |
| Turn-ON Delay Time ¹ | t _{D(ON)} | V _{DD} =50V, I _D =16A, R _G =3.3Ω, V _{GS} =10V, R _D =3.125Ω | | 10 | | ns |
| Turn-ON Rise Time | t _R | | | 28 | | ns |
| Turn-OFF Delay Time | t _{D(OFF)} | | | 17 | | ns |
| Turn-OFF Fall-Time | t _F | | | 2 | | ns |
| SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS | | | | | | |
| Drain-Source Diode Forward Voltage (Note) | V _{SD} | I _S =16A, V _{GS} =0V | | | 1.3 | V |
| Reverse Recovery Time | t _{RR} | I _S =16A,V _{GS} =0V, | | 90 | | ns |
| Reverse Recovery Charge | Q _{RR} | dI/dt=100A/μs | | 380 | | nC |

Note: Pulse Test : Pulse width ≤ 300μs, Duty cycle ≤ 2%.

■ TYPICAL CHARACTERISTICS



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