RClamp1851ZA Ultra Small RClamp® 1-Line, 18V ESD Protection

PROTECTION PRODUCTS - RailClamp®

Description

RClamp® TVS diodes are designed to protect sensitive electronics from damage or latch-up due to ESD. They are designed to replace multilayer varistors (MLVs) in portable applications such as cell phones, notebook computers, and other portable electronics. This device offers desirable characteristics for board level protection including fast response time, low operating and clamping voltage, and no device degradation.

RClamp®1851ZA is specifically designed for protection of Near Field Communications (NFC) interfaces. It features extremely good ESD protection characteristics including a low typical dynamic resistance of 0.16 Ohms, low peak ESD clamping voltage, and high ESD withstand voltage (+/-17kV contact per IEC 61000-4-2). Low typical capacitance (0.35pF at VR=0V) means that harmonic distortion the the RF signal is minimized. This device is bidirectional and has a working voltage of 18V for use on NFC resonator circuits without signal clipping.

RClamp1851ZA is in a 2-pin SLP0603P2X3F package measuring 0.6 x 0.3 mm with a nominal height of only 0.25mm. Leads are finished with NiAu. The small package gives the designer the flexibility to protect single lines in applications where arrays are not practical. The combination of small size and high ESD surge capability makes them ideal for use in portable applications such as cellular phones, digital cameras, and tablet PC's.

Features

- ◆ High ESD withstand Voltage: +/-17kV (Contact) and +/- 20kV (Air) per IEC 61000-4-2
- Ultra-small package
- Protects one high speed data line
- Low ESD clamping voltage
- Working voltage: 18V
- Low capacitance: 0.35pF typical
- ◆ Low leakage current
- Extremely low dynamic resistance: 0.16 Ohms (Typ)
- Solid-state silicon-avalanche technology

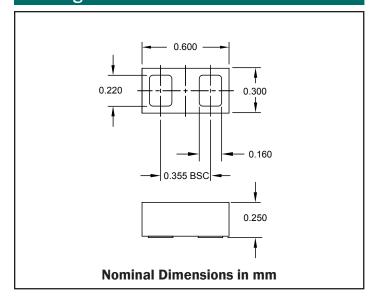
Mechanical Characteristics

- ◆ SLP0603P2X3F package
- Pb-Free, Halogen Free, RoHS/WEEE Compliant
- ♦ Nominal Dimensions: 0.6 x 0.3 x 0.25 mm
- ◆ Lead Finish: NiAu
- Marking: Marking code
- Packaging: Tape and Reel

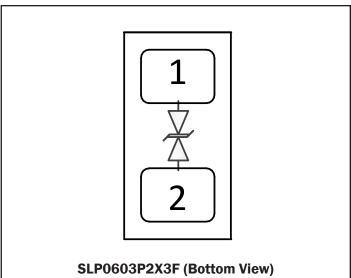
Applications

- Near Field Communication (NFC) lines
- RF signal lines
- Cellular Handsets
- Tablets
- FM Antenna

Package Dimensions



Schematic & Pin Configuration





Absolute Maximum Ratings

| Rating | Symbol | Value | Units |
|--|------------------|-------------|-------|
| Peak Pulse Current (tp = 8/20μs) | I _{PP} | 3 | А |
| ESD per IEC 61000-4-2 (Air) ⁽¹⁾ ESD per IEC 61000-4-2 (Contact) ⁽¹⁾ | V _{ESD} | ±20 ±17 | kV |
| Operating Temperature | T _J | -40 to +85 | °C |
| Storage Temperature | T _{STG} | -55 to +150 | °C |

Electrical Characteristics (T=25°C unless otherwise specified)

| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Units |
|------------------------------------|------------------|---|------|------|------|-------|
| Reverse Stand-Off Voltage | V _{RWM} | $T = -40 \text{ to } +85^{\circ}\text{C}$ | | | 18 | V |
| Breakdown Voltage | V _{BR} | I _{BR} = 10μA | 18.5 | 22.5 | 26.5 | V |
| Reverse Leakage Current | I _R | V _{RWM} = 18V | | <1 | 50 | nA |
| ESD Clamping Voltage ² | V _c | $I_{pp} = 4A$ tp = 0.2/100ns | | 5.5 | | V |
| ESD Clamping Voltage ² | V _c | $I_{pp} = 16A$ tp = 0.2/100ns | | 7.5 | | V |
| Dynamic Resistance ^{2, 3} | R _{DYN} | tp = 0.2/100ns | | 0.16 | | Ohms |
| Junction Capacitance | C _J | VR = OV; f = 1MHz | | 0.35 | 0.45 | pF |

Notes

¹⁾Measured with a 40dB attenuator, 50 Ohm scope input impedance, 2GHz bandwidth. ESD gun return path connected to ESD ground plane.

²⁾Transmission Line Pulse Test (TLP) Settings: tp = 100ns, tr = 0.2ns, I_{TLP} and V_{TLP} averaging window: t1 = 70ns to t2 = 90ns.

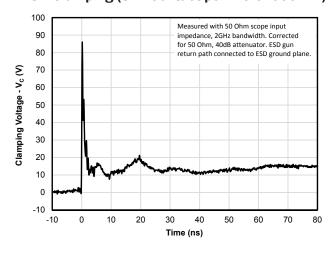
³⁾Dynamic resistance calculated from $\rm I_{\rm TLP}$ = 4A to $\rm I_{\rm TLP}$ = 16A

⁴⁾Device is electrically symmetrical

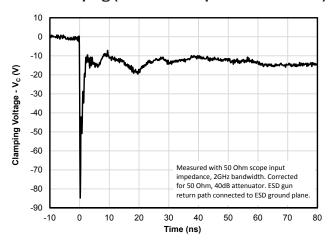


Typical Characteristics

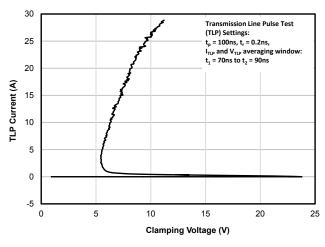
ESD Clamping (8kV Contact per IEC 61000-4-2)



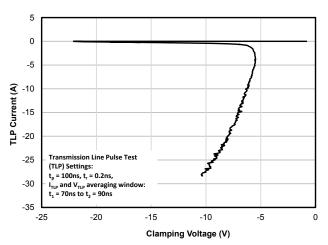
ESD Clamping (-8kV Contact per IEC 61000-4-2)



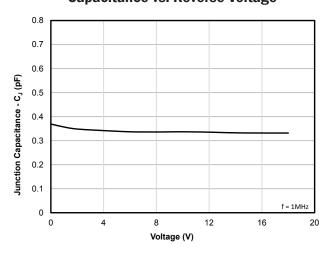
TLP Characteristic (Positive Pulse)



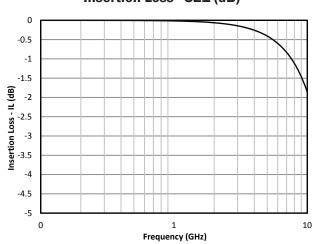
TLP Characteristic (Negative Pulse)



Capacitance vs. Reverse Voltage



Insertion Loss - S21 (dB)





Applications Information

ESD Protection of NFC Interfaces

The Near Field Communication (NFC) antenna is usually connected to the NFC controller IC via contact points on the phone. These contact points are user accessable and therefore may be subjected to ESD strikes. External protection (TVS) devices should be placed between the antenna and the NFC chip interface. The working voltage of the TVS should be high enough as not to clip the NFC signal. Additionally, the capacitance of the device

should be minimized in order to avoid harmonic disctortion of the RF signal. RClamp1851ZA meets these requirements and also features extremely low dynamic resistance resulting in low ESD clamping voltage. The low dynamic resistance also helps insure protection for Schottky diodes that may be used in the NFC circuit. RClamp1851ZA is designed to work on NFC circuits with AC signals as high as 18V. An example protection ciruit is shown below in Figure 1.

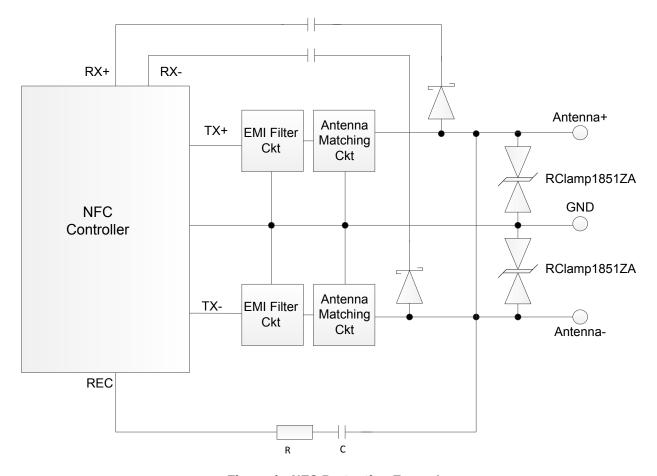


Figure 1 - NFC Protection Example

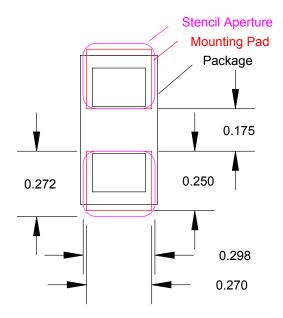


Applications Information

Assembly Guidelines

The small size of this device means that some care must be taken during the mounting process to insure reliable solder joint. The table below provides Semtech's recommended assembly guidelines for mounting this device. The figure at the right details Semtech's recommended aperture based on the below recommendations. Note that these are only recommendations and should serve only as a starting point for design since there are many factors that affect the assembly process. The exact manufacturing parameters will require some experimentation to get the desired solder application.

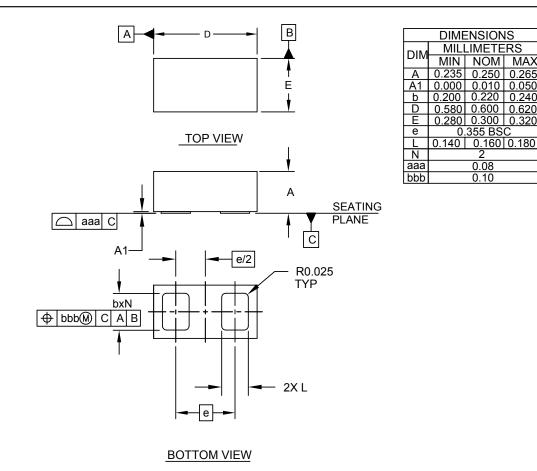
| Assembly Parameter | Recommendation |
|--------------------------|----------------------------------|
| Solder Stencil Design | Laser cut, Electro-polished |
| Aperture shape | Rectangular with rounded corners |
| Solder Stencil Thickness | 0.100 mm (0.004") |
| Solder Paste Type | Type 4 size sphere or smaller |
| Solder Reflow Profile | Per JEDEC J-STD-020 |
| PCB Solder Pad Design | Non-Solder mask defined |
| PCB Pad Finish | OSP OR NiAu |



Recommended Mounting Pattern



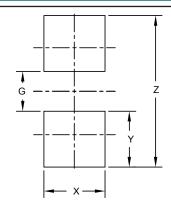
Outline Drawing - SLP0603P2X3F



NOTES:

1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).

Land Pattern - SLP0603P2X3F



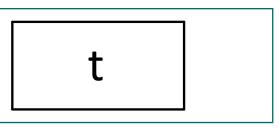
| DIMENSIONS | | |
|------------|-------------|--|
| DIM | MILLIMETERS | |
| G | 0.177 | |
| Х | 0.272 | |
| Υ | 0.247 | |
| Z | 0.671 | |

NOTES:

CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES). THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY.
CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR
COMPANY'S MANUFACTURING GUIDELINES ARE MET.



Marking



Notes: Device is Electrically Symmetrical

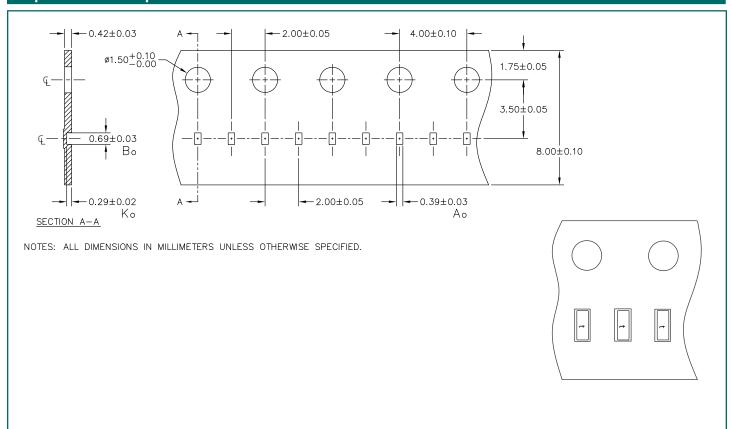
Ordering Information

| Part Number | Qty per Reel | Reel Size |
|-----------------|--------------|-----------|
| RClamp1851ZATFT | 15000 | 7" |

Notes:

1) RailClamp and RClamp are trademarks of Semtech Corporation.

Tape and Reel Specification



Contact Information

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