

TC74LCXZA240FT, TC74LCXZA240FK

Low Voltage Octal Bus Buffer with 5 V Tolerant Inputs and Outputs

The TC74LCXZA240 is a high-performance CMOS octal bus buffer. Designed for use in 2.5-V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation. The device is designed for low-voltage (2.5V) VCC applications, but it could be used to interface to 5-V supply environment for both inputs and outputs.

When Power supply voltage is turned on, turned off or VCC is between 0 to 1.5V, output will be at high impedance.

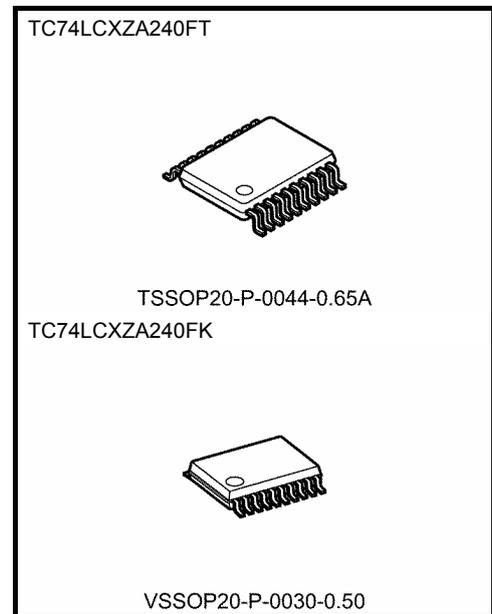
For operation at (2.5 V) VCC, hot board insertion is applicable.

The TC74LCXZA240 is an inverting 3-state buffer having two active-low output enables. This device is designed to be used with 3-state memory address drivers, etc.

All inputs are equipped with protection circuits against static discharge.

Features

- Low-voltage operation: VCC = 2.3 to 2.7 V
- High-speed operation: t_{pd} = 7.5 ns (max) (VCC = 2.3 to 2.7 V)
- Output current: I_{OH} = -12 mA (min) / I_{OL} = 18 mA (min)
(VCC = 2.3V)
- Available in TSSOP and VSSOP (US)
- Power-down protection provided on all inputs and outputs
- Pin and function compatible with the 74 series
(74AC/VHC/HC/F/ALS/LS etc.) 240 type

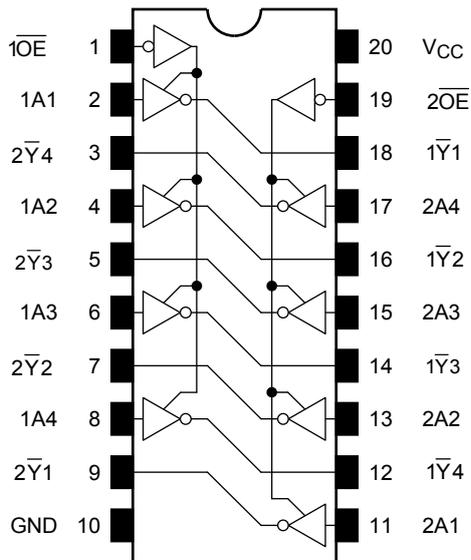


Weight

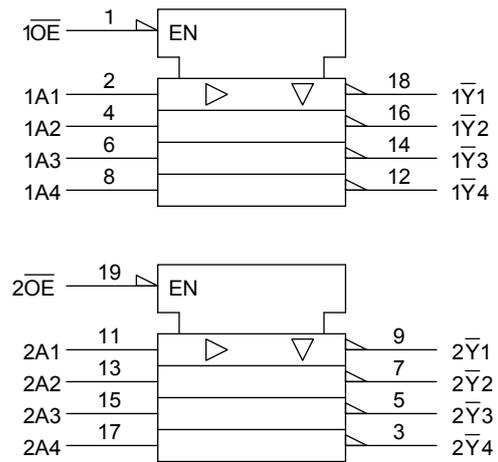
TSSOP20-P-0044-0.65A	: 0.08 g (typ.)
VSSOP20-P-0030-0.50	: 0.03 g (typ.)

Start of commercial production
2011-03

Pin Assignment (top view)



IEC Logic Symbol



Truth Table

Inputs		Outputs
\overline{OE}	A_n	
L	L	H
L	H	L
H	X	Z

X: Don't care

Z: High impedance

Absolute Maximum Ratings (Note1)

Characteristics	Symbol	Rating	Unit
Power supply voltage	V_{CC}	-0.5 to 7.0	V
DC input voltage	V_{IN}	-0.5 to 7.0	V
DC output voltage	V_{OUT}	-0.5 to 7.0 (Note 2)	V
		-0.5 to $V_{CC} + 0.5$ (Note 3)	
Input diode current	I_{IK}	-50	mA
Output diode current	I_{OK}	± 50 (Note 4)	mA
DC output current	I_{OUT}	± 50	mA
Power dissipation	P_D	180	mW
DC V_{CC} /ground current	I_{CC}/I_{GND}	± 100	mA
Storage temperature	T_{stg}	-65 to 150	$^{\circ}C$

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: Output in off-state

Note 3: High or low state. I_{OUT} absolute maximum rating must be observed.

Note 4: $V_{OUT} < GND$, $V_{OUT} > V_{CC}$

Operating Ranges (Note1)

Characteristics	Symbol	Rating	Unit
Power supply voltage	V_{CC}	2.3 to 2.7	V
Input voltage	V_{IN}	0 to 5.5	V
Output voltage	V_{OUT}	0 to 5.5 (Note 2)	V
		0 to V_{CC} (Note 3)	
Output current	I_{OH}/I_{OL}	-18/24 (Note 4)	mA
Operating temperature	T_{opr}	-40 to 85	$^{\circ}C$
Input rise and fall time	dt/dv	0 to 10 (Note 5)	ns/V
Power-up ramp rate	dt/dV_{CC}	150 (min)	$\mu s/V$

Note 1: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

Note 2: Output in off-state

Note 3: High or low state.

Note 4: $V_{CC} = 2.3$ to 2.7 V

Note 5: $V_{IN} = 0.7$ to 1.7 V, $V_{CC} = 2.5$ V

Electrical Characteristics

DC Characteristics (Ta = -40 to 85°C)

Characteristics		Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Input voltage	H-level	V _{IH}	—	2.3 to 2.7	1.7	—	V
	L-level	V _{IL}	—	2.3 to 2.7	—	0.7	
Output voltage	H-level	V _{OH}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -100 μA	2.3 to 2.7	V _{CC} - 0.2	V
				I _{OH} = -8 mA	2.3	1.8	
				I _{OH} = -12 mA	2.3	1.7	
	L-level	V _{OL}	V _{IN} = V _{IH} or V _{IL}	I _{OL} = 100 μA	2.3 to 2.7	—	0.2
				I _{OL} = 18 mA	2.3	—	0.55
Input leakage current		I _{IN}	V _{IN} = 0 to 5.5 V	2.3 to 2.7	—	±5.0	μA
3-state output off-state current		I _{OZ}	V _{IN} = V _{IH} or V _{IL} V _{OUT} = 0 to 5.5 V	2.3 to 2.7	—	±5.0	μA
			Output enable=don't care V _{OUT} = 0.5 to 5.5 V	0 to 1.2	—	±5.0	
Power off leakage current		I _{OFF}	V _{IN} /V _{OUT} = 5.5 V	0	—	10.0	μA
Quiescent supply current		I _{CC}	V _{IN} = V _{CC} or GND	2.3 to 2.7	—	40	μA
			V _{IN} /V _{OUT} = 2.7 to 5.5 V	2.3 to 2.7	—	±40	

AC Characteristics (Ta = -40 to 85°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time	t _{pLH} t _{pHL}	Figure 1, Figure 2	2.5 ± 0.2	1.5	7.5	ns
Output enable time	t _{pZL} t _{pZH}	Figure 1, Figure 3	2.5 ± 0.2	1.5	9.1	ns
Output disable time	t _{pLZ} t _{pHZ}	Figure 1, Figure 3	2.5 ± 0.2	1.5	8.3	ns
Output to output skew	t _{osLH} t _{osHL}	(Note1)	2.5 ± 0.2	—	1.0	ns

Note1: Parameter guaranteed by design.

$$(t_{osLH} = |t_{pLHm} - t_{pLHn}|, t_{osHL} = |t_{pHLm} - t_{pHLn}|)$$

Dynamic Switching Characteristics (Ta = 25°C, input: t_r = t_f = 2.5 ns, C_L = 30 pF, R_L = 500 Ω)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Typ.	Unit
Quiet output maximum dynamic V _{OL}	V _{OLP}	V _{IH} = 2.5 V, V _{IL} = 0 V	2.5	0.6	V
Quiet output minimum dynamic V _{OL}	V _{OLV}	V _{IH} = 2.5 V, V _{IL} = 0 V	2.5	0.6	V

Capacitive Characteristics (Ta = 25°C)

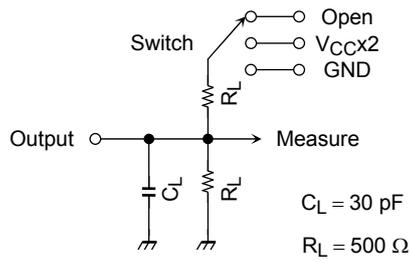
Characteristics	Symbol	Test Condition	V _{CC} (V)	Typ.	Unit	
Input capacitance	C _{IN}	—	2.5	5	pF	
Output capacitance	C _{OUT}	—	2.5	7	pF	
Power dissipation capacitance	C _{PD}	f _{IN} = 10 MHz	(Note)	2.5	18	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption.

Average operating current can be obtained by the equation:

$$I_{CC(opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/8 \text{ (per bit)}$$

AC Test Circuit



Parameter	Switch
t_{pLH} , t_{pHL}	Open
t_{pLZ} , t_{pZL}	$V_{CC} \times 2$
t_{pHZ} , t_{pZH}	GND

Figure 1

AC Waveform

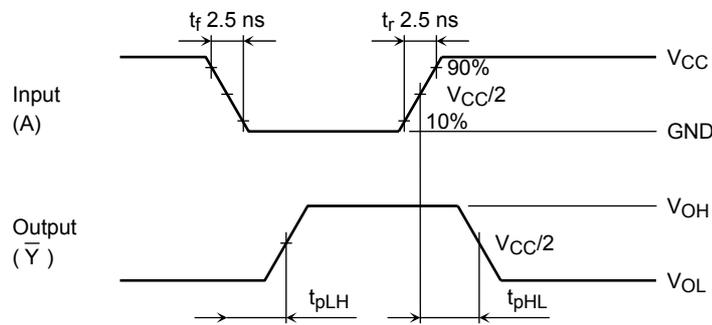


Figure 2 t_{pLH} , t_{pHL}

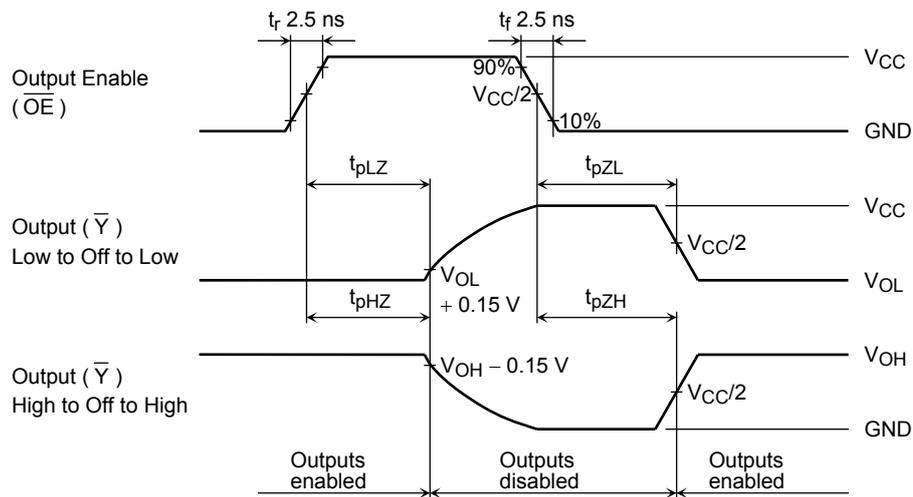
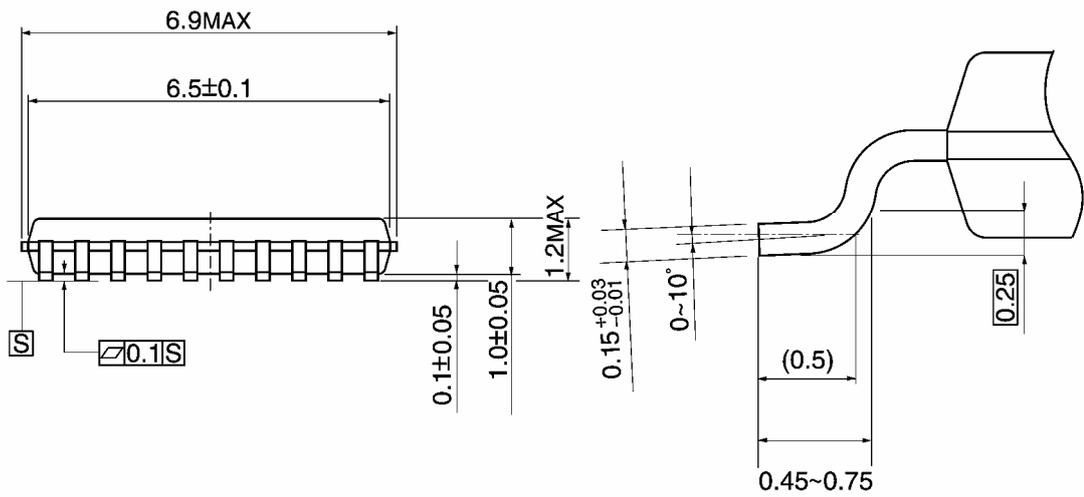
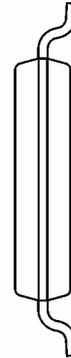
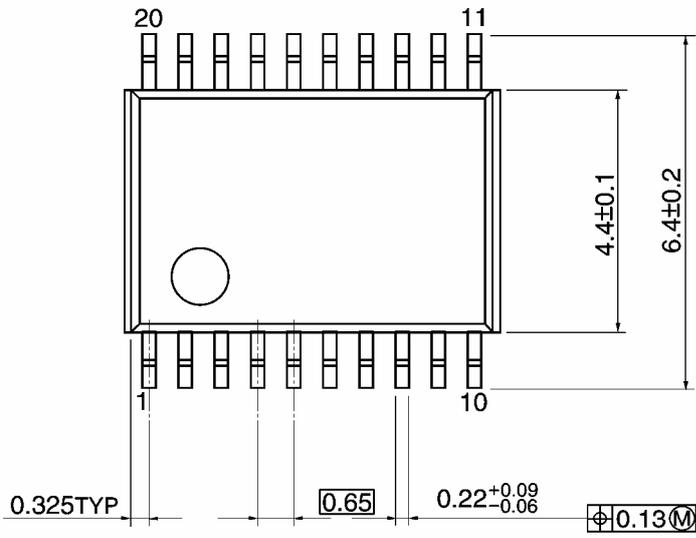


Figure 3 t_{pLZ} , t_{pHZ} , t_{pZL} , t_{pZH}

Package Dimensions

TSSOP20-P-0044-0.65A

Unit: mm

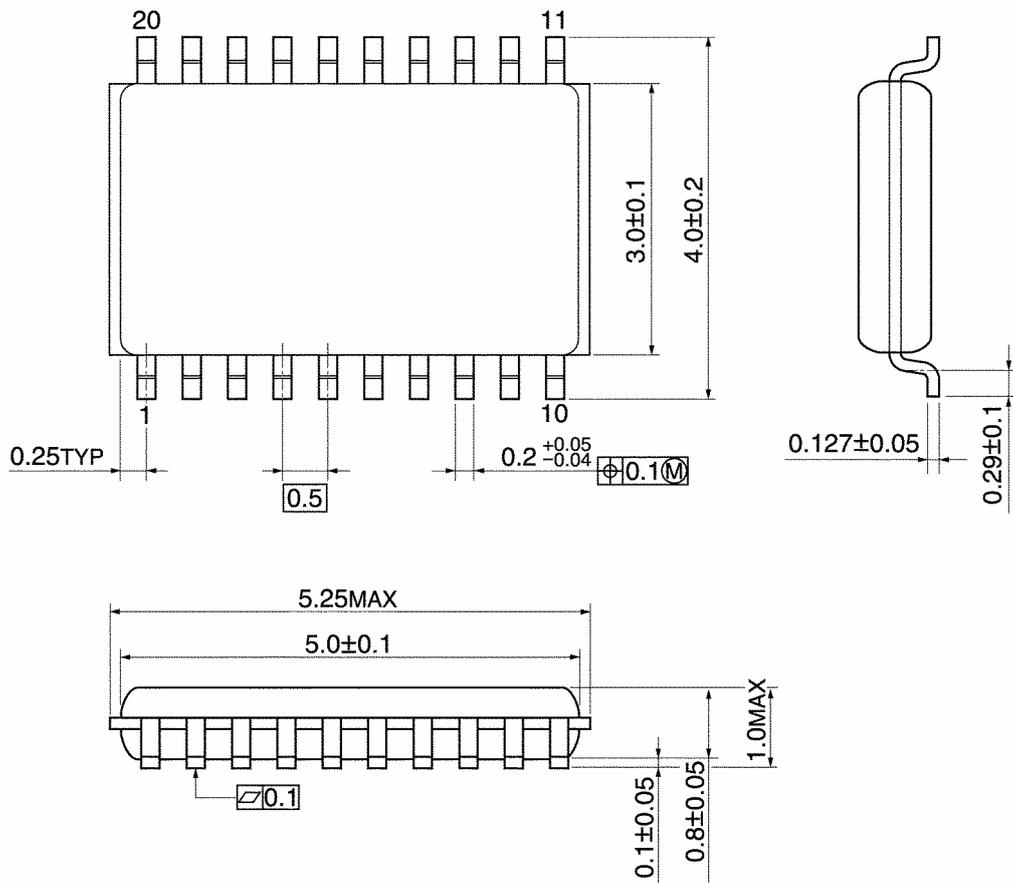


Weight: 0.08 g (typ.)

Package Dimensions

VSSOP20-P-0030-0.50

Unit: mm



Weight: 0.03 g (typ.)

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