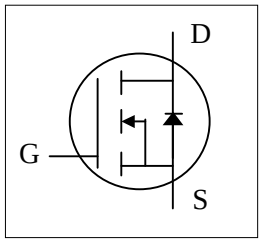
AP16N50P-HF

Halogen-Free Product

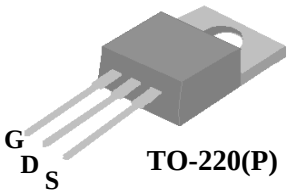
Advanced Power Electronics Corp.

N-CHANNEL ENHANCEMENT MODE POWER MOSFET

- **Low On-resistance**
- **Simple Drive Requirement**
- **Fast Switching Characteristic**
- **RoHS Compliant & Halogen-Free**



BV_{DSS}	500V
$R_{DS(ON)}$	0.4Ω
I_D	16A



TO-220(P)

Description

AP16N50 series are from Advanced Power innovated design and silicon process technology to achieve the lowest possible on-resistance and fast switching performance. It provides the designer with an extreme efficient device for use in a wide range of power applications.

The TO-220 package is widely preferred for all commercial-industrial through hole applications. The low thermal resistance and low package cost contribute to the worldwide popular package.

Absolute Maximum Ratings@T_j=25°C(unless otherwise specified)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	500	V
V_{GS}	Gate-Source Voltage	±30	V
I_D@T_C=25℃	Drain Current, V_{GS} @ 10V	16	A
I_D@T_C=100℃	Drain Current, V_{GS} @ 10V	11	A
I_{DM}	Pulsed Drain Current[1]	60	A
P_D@T_C=25℃	Total Power Dissipation	173.6	W
E_{AS}	Single Pulse Avalanche Energy[3]	75	mJ
T_{STG}	Storage Temperature Range	-55 to 150	℃
T_J	Operating Junction Temperature Range	-55 to 150	℃

Thermal Data

Symbol	Parameter	Value	Units
Rthj-c	Maximum Thermal Resistance, Junction-case	0.72	℃/W
Rthj-a	Maximum Thermal Resistance, Junction-ambient	62	℃/W

Electrical Characteristics@T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V, I_D=250uA	500	-	-	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance[2]	V_{GS}=10V, I_D=6.5A	-	-	0.4	Ω
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_D=250uA	2	-	4	V
g_{fs}	Forward Transconductance	V_{DS}=10V, I_D=8A	-	8	-	S
I_{DSS}	Drain-Source Leakage Current	V_{DS}=500V, V_{GS}=0V	-	-	100	uA
I_{GSS}	Gate-Source Leakage	V_{GS}=±30V, V_{DS}=0V	-	-	±100	nA
Q_g	Total Gate Charge	I_D=16A	-	33	53	nC
Q_{gs}	Gate-Source Charge	V_{DS}=400V	-	11	-	nC
Q_{gd}	Gate-Drain ("Miller") Charge	V_{GS}=10V	-	9	-	nC
$t_{d(on)}$	Turn-on Delay Time	V_{DD}=200V	-	55	-	ns
t_r	Rise Time	I_D=8A	-	50	-	ns
$t_{d(off)}$	Turn-off Delay Time	R_G=50Ω	-	141	-	ns
t_f	Fall Time	V_{GS}=10V	-	40	-	ns
C_{iss}	Input Capacitance	V_{GS}=0V	-	1950	3120	pF
C_{oss}	Output Capacitance	V_{DS}=15V	-	630	-	pF
C_{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	20	-	pF

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_{SD}	Forward On Voltage[2]	I_S=16A, V_{GS}=0V	-	-	1.3	V
t_{rr}	Reverse Recovery Time	I_S=16A, V_{GS}=0V	-	495	-	ns
Q_{rr}	Reverse Recovery Charge	dI/dt=100A/μs	-	10	-	uC

Notes:

1.Pulse width limited by Max junction temperature.

2.Pulse test

3.Starting Tj=25°C , V_{DD}=50V, V_{GS}=10V, L=3mH, R_G=25Ω

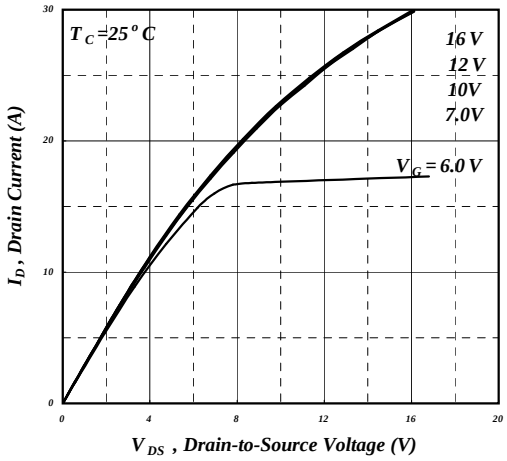


Fig 1. Typical Output Characteristics

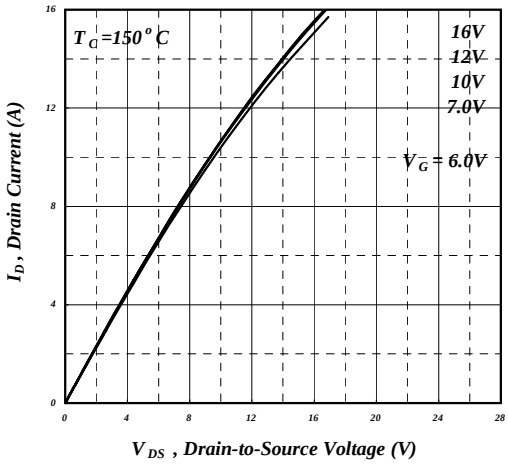


Fig 2. Typical Output Characteristics

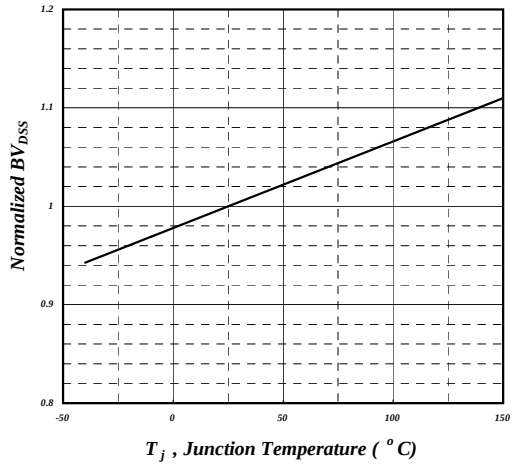


Fig 3. Normalized BV_{DSS} v.s. Junction Temperature

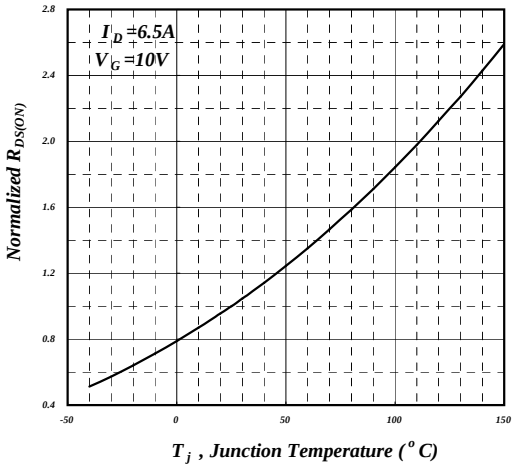


Fig 4. Normalized On-Resistance v.s. Junction Temperature

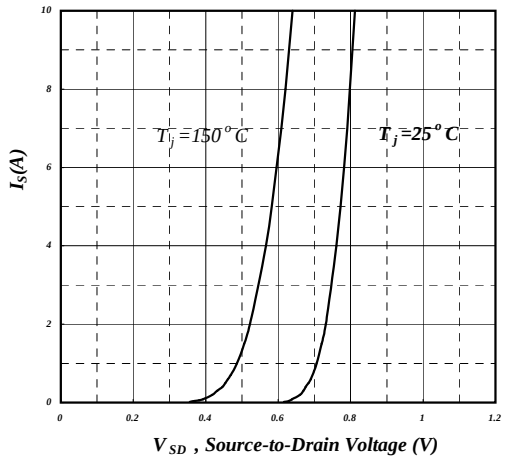


Fig 5. Forward Characteristic of Reverse Diode

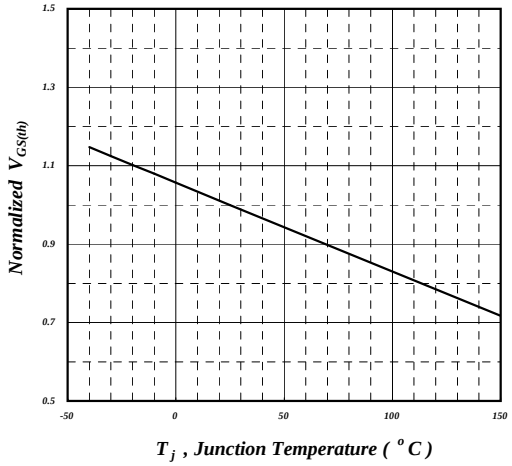


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

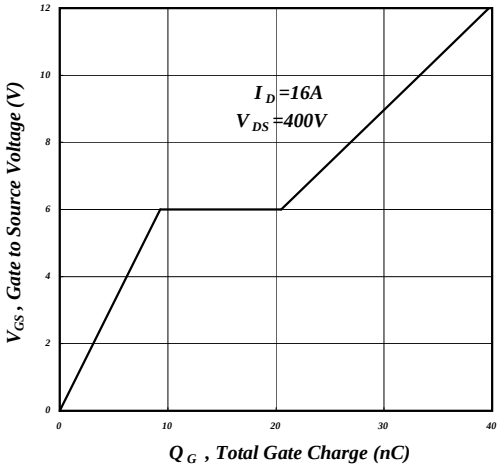


Fig 7. Gate Charge Characteristics

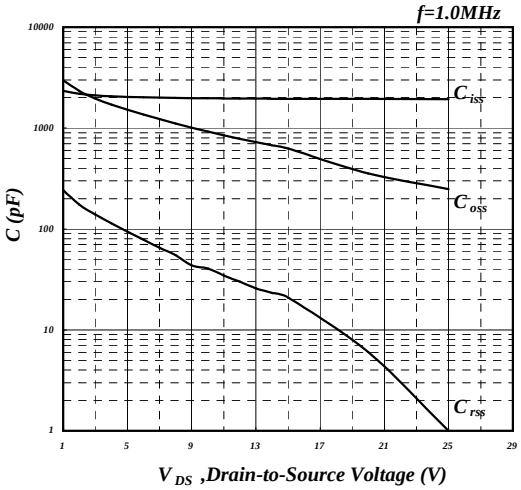


Fig 8. Typical Capacitance Characteristics

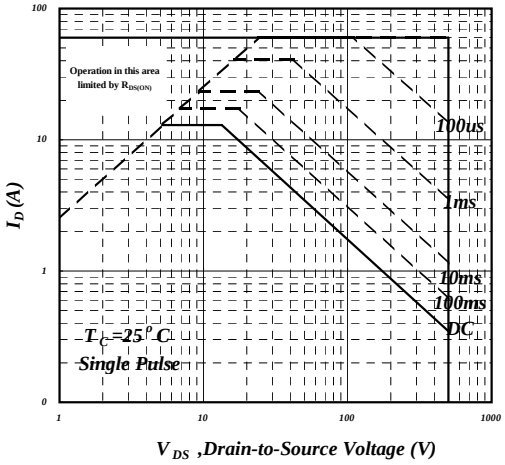


Fig 9. Maximum Safe Operating Area

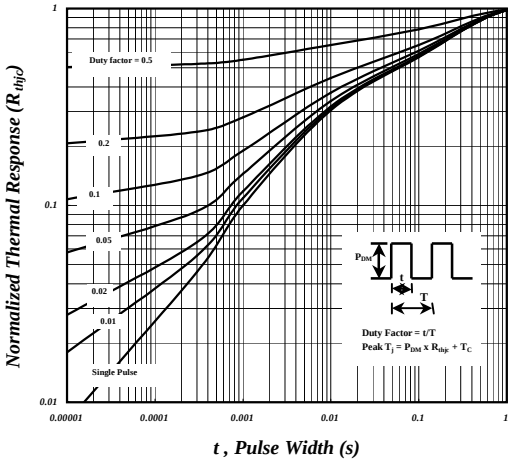


Fig 10. Effective Transient Thermal Impedance

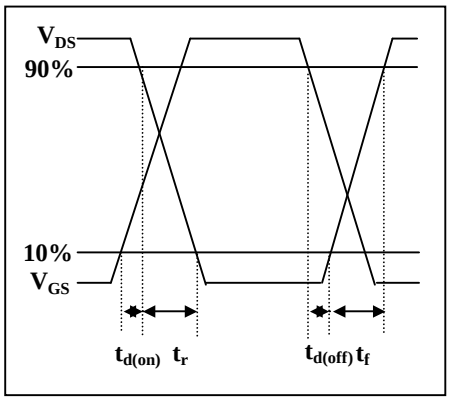


Fig 11. Switching Time Waveform

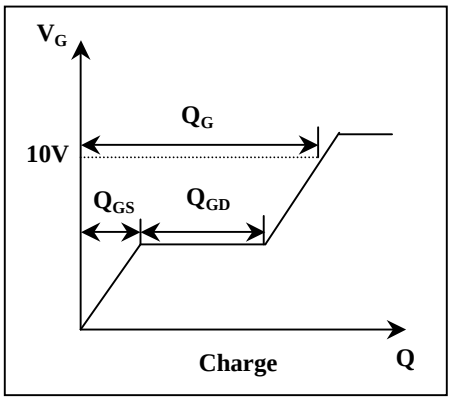


Fig 12. Gate Charge Waveform

MARKING INFORMATION