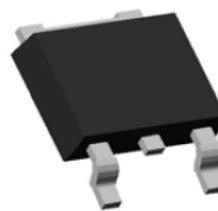
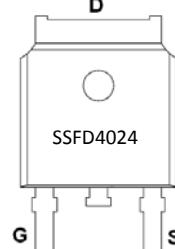


Main Product Characteristics:

V_{DSS}	40V
$R_{DS(on)}$	30 mohm
I_D	12A



TO-252 top view



Marking and pin Assignment

Description:

It utilizes the latest trench processing techniques to achieve extremely low on resistance, fast switching speed and high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in Automotive applications and a wide variety of other applications

Absolute max Rating:

	Parameter	Max.	Units
ID @ TC = 25°C	Continuous Drain Current, VGS @ 10V	12	A
ID @ TC = 100°C	Continuous Drain Current, VGS @ 10V	12	
IDM	Pulsed Drain Current①	30	
PD @TC = 25°C	Power Dissipation	20	W
VGS	Gate-to-Source Voltage	± 20	V
EAS	Single Pulse Avalanche Energy②	22	mJ
IAR	Avalanche Current @ L=0.3mH	10	A
TJ TSTG	Operating Junction and Storage Temperature Range	-55 to + 175	°C

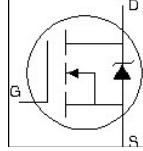
Thermal Resistance

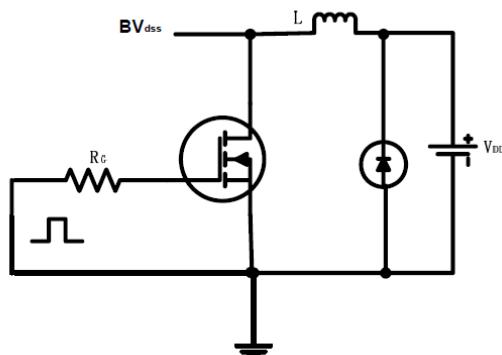
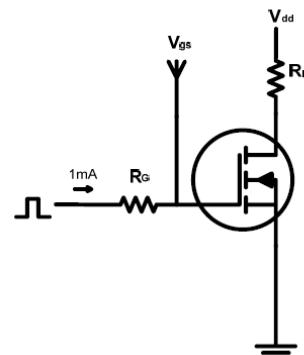
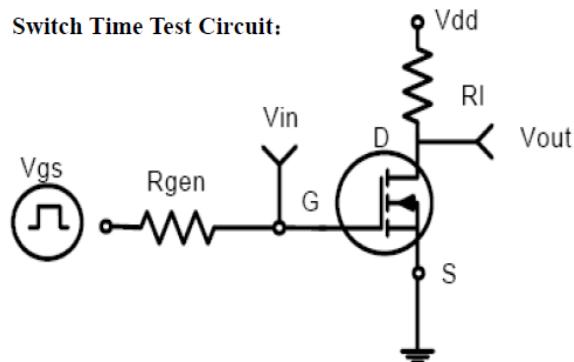
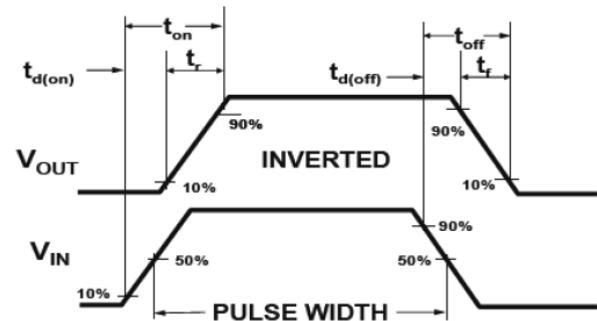
Symbol	Characterizes	Value	Unit
$R_{\theta JC}$	Junction-to-case	7.5	°C/W
$R_{\theta JA}$	Junction-to-ambient	30	°C/W
	Junction-to-Ambient (PCB mounted, steady-state)	60	°C/W

Electrical Characterizes @ $T_A=25^\circ C$ unless otherwise specified

	Parameter	Min.	Typ.	Max	Units	Conditions
BVDSS	Drain-to-Source breakdown voltage	40	—	—	V	$VGS = 0V, ID = 250\mu A$
RDS(on)	Static Drain-to-Source on-resistance	—	24	30	$m\Omega$	$VGS = 10V, ID = 12A$ ③
VGS(th)	Gate threshold voltage	1	—	3	V	$VDS = VGS, ID = 250\mu A$
IDSS	Drain-to-Source leakage current	—	—	1	μA	$VDS = 40V, VGS = 0V$
		—	—	150		$VDS = 40V, VGS = 0V, TJ = 125^\circ C$
IGSS	Gate-to-Source forward leakage	—	—	100	nA	$VGS = 20V$
	Gate-to-Source reverse leakage	—	—	-100		$VGS = -20V$
Qg	Total gate charge	—	9.5	—	nC	$ID = 12A VDS = 20V VGS = 10V$ ③
Qgs	Gate-to-Source charge	—	4.5	—		
Qgd	Gate-to-Drain("Miller") charge	—	1.5	—		
td(on)	Turn-on delay time	—	3.5	—		
tr	Rise time	—	6	—	ns	$VDD = 20V ID = 12A RG = 1.7 \Omega VGS = 10V$ ③
td(off)	Turn-Off delay time	—	13.5	—		
tf	Fall time	—	3.5	—		
Ciss	Input capacitance	—	410	—	pF	$VGS = 0V VDS = 20V f = 1.0MHz$
Coss	Output capacitance	—	95	—		
Crss	Reverse transfer capacitance	—	35	—		

Source-Drain Ratings and Characteristics

	Parameter	Min.	Typ.	Max	Units	Conditions
IS	Continuous Source Current (Body Diode)	—	—	12	A	MOSFET symbol showing the integral reverse p-n junction diode.
		—	—	12		
VSD	Diode Forward Voltage	—	0.75	1.0	V	$TJ = 25^\circ C, IF = 1A, VDD = 20V di/dt = 100A/\mu s$ ③
	Reverse Recovery Time	—	23	—		
Qrr	Reverse Recovery Charge	—	18.5	—	nC	$TJ = 25^\circ C, IF = 12A, VDD = 20V di/dt = 100A/\mu s$ ③
	Forward Turn-on Time	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

EAS test circuits:

Gate charge test circuit:

Switch Time Test Circuit:

Switch Waveforms:


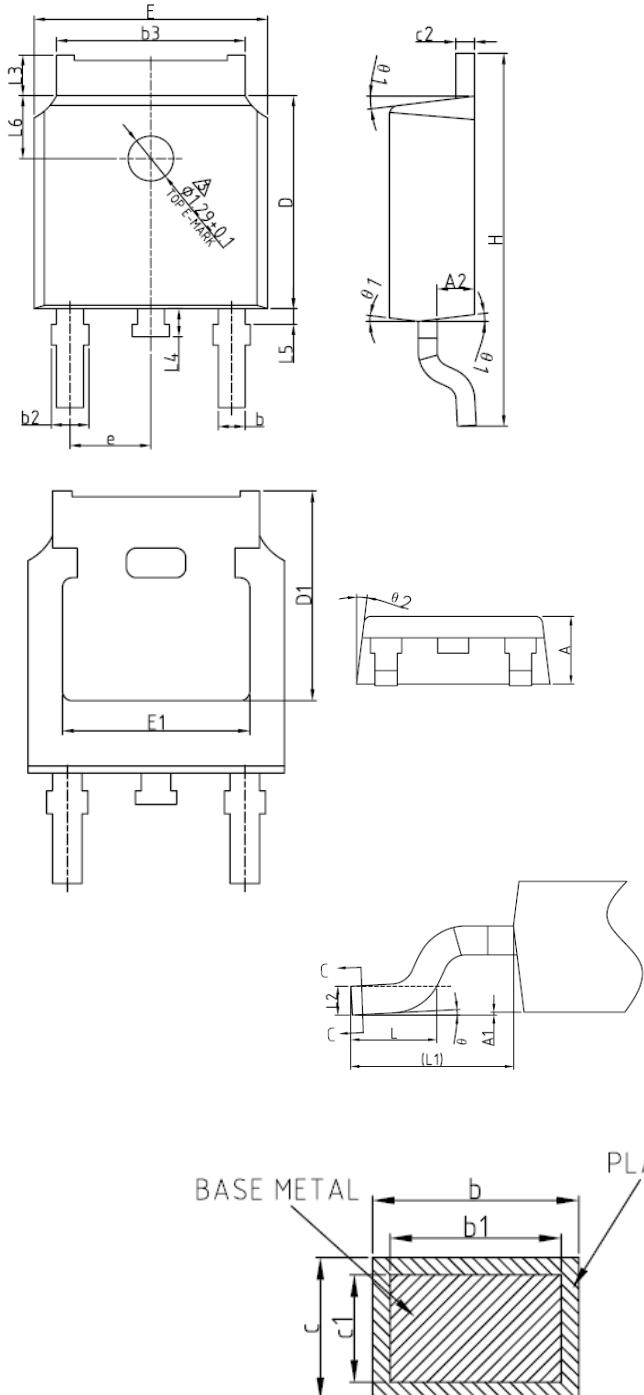
Notes:

- ①Repetitive rating; pulse width limited by max. junction temperature.
- ②Limited by TJmax, starting TJ = 25°C, L = 0.3mH RG =50Ω, IAS = 82A, VGS =10V. Part not recommended for use above this value.
- ③Pulse width < 1.0ms; duty cycle<2%.

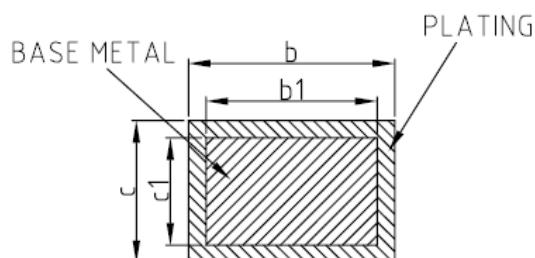
Mechanical Data:

TO-252E-2-M PACKAGE INFORMATION

Dimensions in Millimeters



SYMBOL	MIN	NOM	MAX
A	2.20	2.30	2.38
A1	0	—	0.10
A2	0.90	1.01	1.10
b	0.72	—	0.85
b1	0.71	0.76	0.81
b2	0.72	—	0.90
b3	5.13	5.33	5.46
c	0.47	—	0.60
c1	0.46	0.51	0.56
c2	0.47	—	0.60
D	6.00	6.10	6.20
D1	5.25	—	—
E	6.50	6.60	6.70
E1	4.70	—	—
e	2.186	2.286	2.386
H	9.80	10.10	10.40
L	1.40	1.50	1.70
L1	2.90REF		
L2	0.51BSC		
L3	0.90	—	1.25
L4	0.60	0.80	1.00
L5	0.15	—	0.75
L6	1.80REF		
theta	0°	—	8°
theta 1	5°	7°	9°
theta 2	5°	7°	9°


NOTES:

1. Dimensions are inclusive of plating
2. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 6 mils.
3. Dimension L is measured in gauge plane.
4. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.