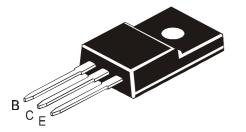


NPN SILICON PLANAR POWER TRANSISTORS

CFD1264, CFD1264A

TO-220FP Fully Isolated Plastic Package



Complementary CFB940, CFB940A

ABSOLUTE MAXIMUM RATINGS(T_a=25°C)

DESCRIPTION	SYMBOL	CFD1264	CFD1264A	UNIT
Collector Base Voltage	V _{CBO}	200	200	V
Collector Emitter Voltage	V _{CEO}	150	180	V
Emitter Base Voltage	V _{EBO}	6	V	
RMS Isolation Voltage (for 1sec, R.H.	** V _{ISOL} (a)	350	V _{RMS}	
<30%, T _a = 25°C)	(b)	150	V _{RMS}	
Collector Current Peak	I _{CP}	3		A
Collector Current	I _C	2		A
Collector Power Dissipation @T _c =25°C	Pc	30		W
@T _a =25°C		2	2	W
Junction Temperature	Tj	15	i0	°C
Storage Temperature Range	T _{stg}	- 55 to	+150	°C

** RMS Isolation Voltage: (a) 3500 V_{RMS} with Package in Clip Mounting Position (b) 1500 V_{RMS} with Package in Screw Mounting Position (for 1sec, R.H.<30%, T_a=25°C; Pulse Test: Pulse Width Output: Stress S

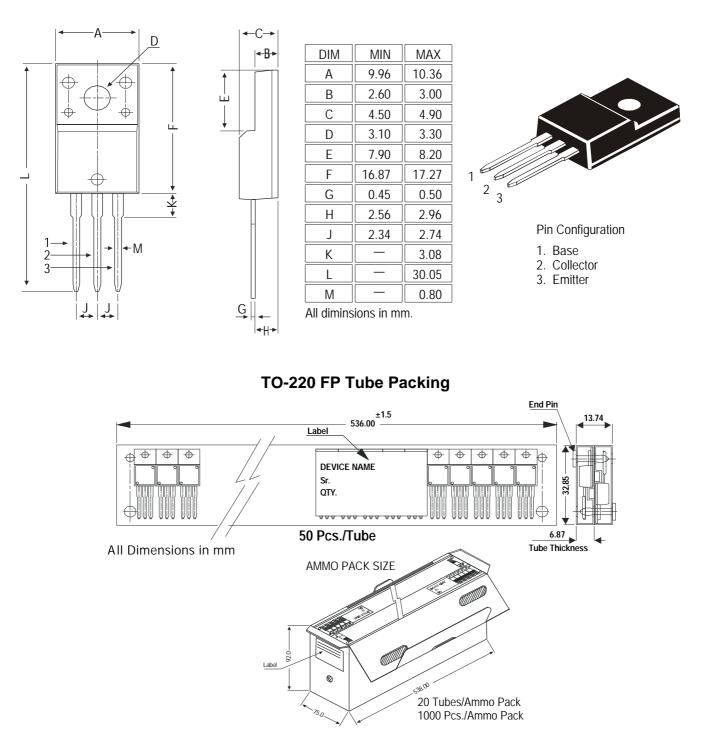
ELECTRICAL CHARACTERISTICS (T_c=25°C unless otherwise specified)

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Collector Cut Off Current	Сво	V _{CB} =200V, I _E =0			50	μA
Emitter Cut Off Current	ЕВО	$V_{EB} = 4V, I_{C} = 0$			50	μA
Collector Base Voltage	V _{CBO}	I _C =500μΑ, I _E =0	200			V
Collector Emitter Voltage	V _{CEO}	I _C =5mA, I _B =0				
		CFD1264	150			V
		CFD1264A	180			V
Emitter Base Voltage	V _{EBO}	I _E =500μA, I _C =0	6			V
DC Current Gain	h _{FE} *	$V_{CE} = 10V, I_{C} = 150mA$	60		240	
	h _{FE}	V _{CE} =10V, I _C =400mA	50			
Base Emitter On Voltage	V _{BE}	V _{CE} =10V, I _C =400mA			1	V
Collector Emitter Saturation Voltage	V _{CE(sat)}	I _C =500mA, I _B =50mA			1	V

h_{FE}* Classification

P:100-240

TO-220FP Fully Isolated Plastic Package



TO-220FP Fully Isolated Plastic Package

Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
TO-220FP	200 pcs/polybag 50 pcs/tube	396 gm/200 pcs 135 gm/50 pcs	3" x 7.5" x 7.5" 3.5" x 3.7" x 21.5"		17" x 15" x 13.5" 19" x 19" x 19"	16K 10K	36 kgs 28 kgs

CFD1264, CFD1264A

TO-220FP Fully Isolated Plastic Package

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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