

1S20 THRU 1S100



1.0 AMP SCHOTTKY BARRIER RECTIFIERS



FEATURES

- * Low forward voltage drop
- * High current capability
- * High reliability
- * High surge current capability
- * Epitaxial construction

MECHANICAL DATA

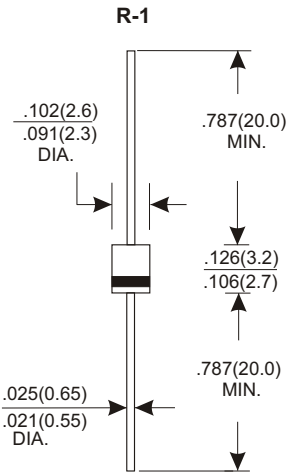
- * Case: Molded plastic
- * Epoxy: UL 94V-0 rate flame retardant
- * Lead: Axial leads, solderable per MIL-STD-202, method 208 guaranteed
- * Polarity: Color band denotes cathode end
- * Mounting position: Any

VOLTAGE RANGE

20 to 100 Volts

CURRENT

1.0 Ampere



Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating 25°C ambient temperature unless otherwise specified.
Single phase half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

TYPE NUMBER	1S20	1S30	1S40	1S50	1S60	1S80	1S100	UNITS	
Maximum Recurrent Peak Reverse Voltage	20	30	40	50	60	80	100	V	
Maximum RMS Voltage	14	21	28	35	42	56	70	V	
Maximum DC Blocking Voltage	20	30	40	50	60	80	100	V	
Maximum Average Forward Rectified Current									
.375"(9.5mm) Lead Length at T _L =100°C								1.0	A
Peak Forward Surge Current, 8.3 ms single half sine-wave superimposed on rated load (JEDEC method)								30	A
Maximum Instantaneous Forward Voltage at 1.0A	0.55		0.70		0.85			V	
Maximum DC Reverse Current T _a =25°C								0.5	mA
at Rated DC Blocking Voltage T _a =100°C								10	mA
Typical Junction Capacitance (Note1)	110			80				pF	
Typical Thermal Resistance R _{θJL} (Note 2)								10	°C/W
Operating Temperature Range T _J								-65 — +150	°C
Storage Temperature Range T _{STG}								-65 — +150	°C

NOTES:

1. Measured at 1MHz and applied reverse voltage of 4.0V D.C.
2. Thermal Resistance Junction to Lead Vertical PC Board Mounting 0.375"(9.5mm) Lead Length.

RATING AND CHARACTERISTIC CURVES (1S20 THRU 1S100)

FIG.1-TYPICAL FORWARD CURRENT DERATING CURVE

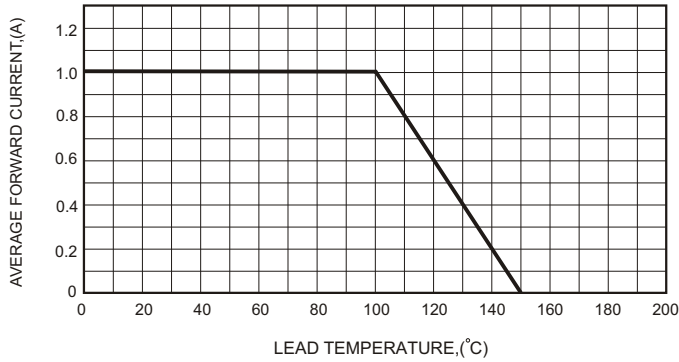


FIG.2-TYPICAL FORWARD CHARACTERISTICS

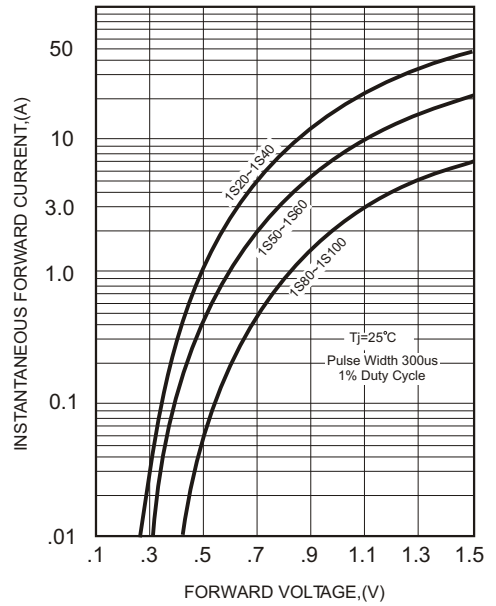


FIG.3-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

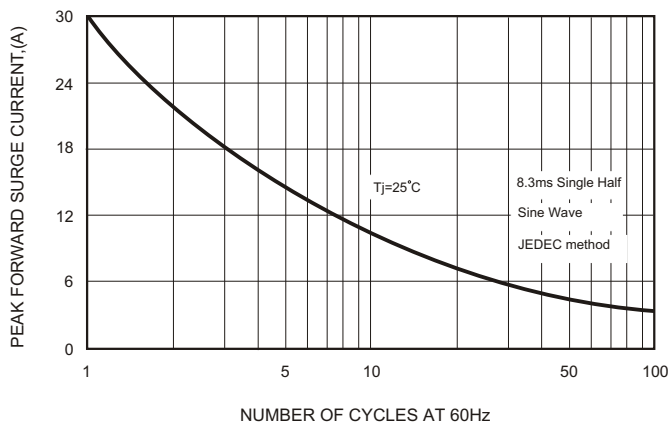


FIG.5 - TYPICAL REVERSE CHARACTERISTICS

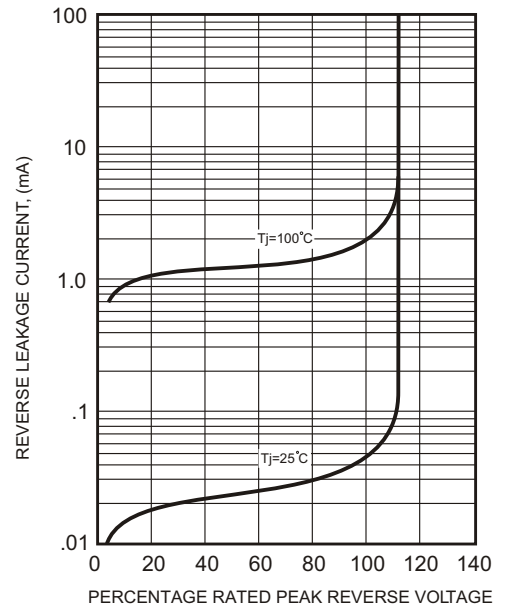


FIG.4-TYPICAL JUNCTION CAPACITANCE

