AN7002K, AN7002S

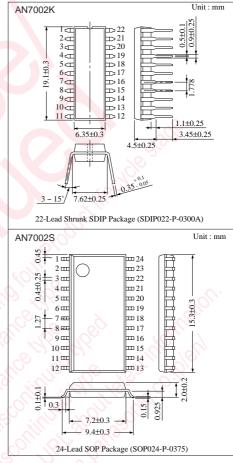
Single Chip ICs for AM Radio

■ Overview

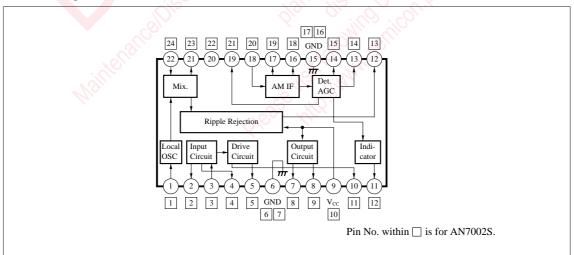
The AN7002K and the AN7002S are the single chip ICs incorporating AM tuner block to power amp. block for 3V radio and are suitable for low-end AM radio.

■ Features

- Single chip ICs incorporating AM tuner block to power amp. block
- Low power consumption : 17mA (at no signal)
- Built-in tuning indicator circuit
- Adjustment-free IF



■ Block Diagram



■ Absolute Maximum Ratings (Ta=25°C)

| Parameter | | Symbol | Rating | Unit |
|-------------------------------|----------------|--------------|--------------------|------|
| C | AN7002K | W | 6 | 37 |
| Supply Voltage | AN7002S | $V_{\rm CC}$ | 4.5 | V |
| Supply Current | Supply Current | | 300 | mA |
| D D: : :: | AN7002K | P_{D} | 1980 | mW |
| Power Dissipation | AN7002S | | 520 | |
| Operating Ambient Temperature | | $T_{ m opr}$ | -20 ~ + 75 | °C |
| C4 T | AN7002K | | -55 ~ + 150 | °C |
| Storage Temperature | AN7002S | T_{stg} | −55 ~ + 125 | °C |

■ Recommended Operating Range (Ta=25°C)

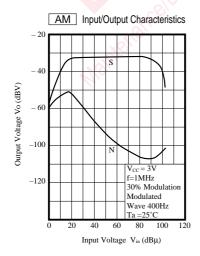
| Parameter | | Symbol | Range | |
|-----------------------------------|---------|--------|-------------|--|
| Operating Supply Voltage Range | AN7002K | V | 1.8V ~ 6V | |
| | AN7002S | Vcc | 1.8V ~ 4.5V | |

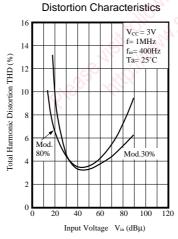
■ Electrical Characteristics

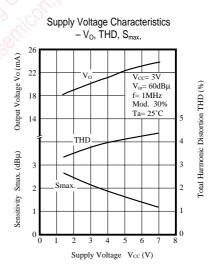
 $(V_{CC} = 3V, [AM Section] R_L = 5k\Omega, f = 1MHz, Mod. = 30\%, f_M = 400Hz [Power Section] R_L = 8\Omega, f = 1kHz, Ta = 25°C)$

| Parameter | Symbol | Condition | min. | typ. | max. | Unit |
|---|---------------|--|------|------|------|-------|
| Total Circuit Current | I_{tot} | AM $V_{in} = -20 dB \mu$, Power $V_{in} = 0 mV$ | 9 | 17 | 25 | mA |
| AM Section (Power Section R _g =10k | Ω) | 1000 | | | | |
| Detection Output Voltage | V_{0} | $V_{in} = 60 dB \mu$ | 16 | 24 | 32 | mVrms |
| Sensitivity | Smax. | $V_0 = 5 \text{mVrms}$ | _4 | 4.5 | 13 | dΒμ |
| Indicator Sensitivity | $V_{\rm IND}$ | Pin11 = less than 1VV | 7 | 17 | 27 | dΒμ |
| Power Section (AM Section V _{in} = -2 | 0dBμ) | 162 161 171 | 37 | | | |
| Voltage Gain | G_{V} | $V_0 = 0.28 \text{mVrms}$ | 41 | 44 | 47 | dB |
| Output Noise Voltage | V_{NO} | $R_g = 10k\Omega$, DIN/AUDIO | 16 | 0.3 | 0.6 | mVrms |
| Total Harmonic Distortion THD V _O = 0.28mVrm | | $V_0 = 0.28 \text{mVrms}$ | | 2.5 | 5 | % |
| Maximum Output Po | | THD = 10% | 100 | 120 | | mW |

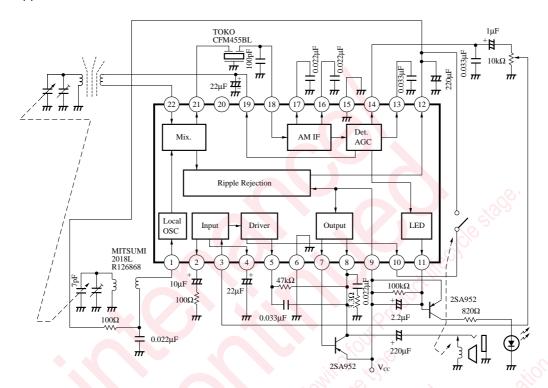
■ Characteristics Curve







■ Application Circuit (AN7002K)



■ Pin Descriptions

Pin No. within ☐ is for AN7002S

| Fill Descriptions | | | | | Pin No. Within 18 for AN 70028 |
|-------------------|----------------|---------------------|---------------|---|---|
| | No. AN7002S | Pin Name | Typ. Waveform | Description | Equivalent Circuit |
| 1 | 1 | Local OSC | SOUTH POPUL | AMOSC pin Oscillation circuit is made up with differential amp. | Vcc 1 |
| 9 | 10 | $V_{\rm cc}$ | DC 3V | Supply pin | |
| 3 | 3 | AF Input | | Differential amp. base input | 3 3 ≥ 10kΩ |
| 4 | 4 | Ripple Rejection | _ | Ripple rejection pin | To power amp. To $20k\Omega$ $20k\Omega$ $20k\Omega$ |

| Pin Descriptions (Cont.) | | | | | |
|--------------------------|---------|-----------------------|-----------------|--|---|
| AN7002K | AN7002S | Pin Name | Typ. Waveform | Description | Equivalent Circuit |
| 5 | 5 | Phase Compensation | | Phase compensation pin Connect phase compensation C, R between power output pin and this pin. | \$1.8kΩ 3.3kΩ 5 5 |
| 6 | 6 7 | GND | DC 0V | GND pin | |
| 7 | 8 | PNP Power Base | | PNP power transistor base connection pin | 910 |
| 8 | 9 | Power Output | \sim | Power output pin | 89 |
| 2 | 2 | Negative Feedback | | Power amp. negative feedback pin | 170Ω G 70Ω G 70Ω T 70Ω T 7 |
| 10 | 11 | Current Switching | | Current switching pin Connect this pin with V_{CC} and increase supply current quantity. | 10 11 11 11 11 11 11 11 11 11 11 11 11 1 |
| 11 | 12 | Indicator | Sortifue dinchi | Indicator pin LED drive becomes open collector output used AGC voltage. Drive capacitance is 2mA. So LED can't be driven directly. | |
| 12 | 13 | Ripple Rejection | _ | Ripple rejection pin Make up of π type filter at C, R and increase ripple rejection efficiency. | Other block power supply line 1200 Ω 200 μ F |
| 13 | 14 | Detection | | Detection pin Input 455kHz IF signal to two Tr in reverse phase, peak detect all-rectified waveform through constant-current supply and C, R charge and discharge. | 13 14 0.033 π μF |
| 14 | 15 | Detection Output | \sim | Detection output pin This pin is PNP emitter ground amp. output. Output impedance is 2.2kΩ. | 14/15 ξ 2.2kΩ |

■ Pin Descriptions (Cont.)

| Pin : | No. | D: 3- | | | |
|-------|----------|------------|-----------------------|---|--|
| | AN7002S | Pin Name | Typ. Waveform | Description | Equivalent Circuit |
| 15 | 16 17 | GND | DC 0V | GND pin | |
| 16 | 18 | IF By-pass | 455kHz IF Signal | IF by-pass capacitor connection pin | 17 19 16 18 |
| 17 | 19 | IF By-pass | 455kHz IF Signal | IF by-pass capacitor connection pin | 2.7kΩ \$ \$\frac{\text{G}}{\text{S}} \frac{\text{G}}{\text{S}} \frac{\text{I2}}{\text{I3}} |
| 18 | 20 | IF Input | 455kHz IF Signal | IF input pin Differential amp. base input | 20 🖨 |
| 19 | 21 | AGC | | AGC pin Connect capacitor | 22 |
| 22 | 24 | RF Input | AM Signal | RF input pin Differential amp. base input | (19) \$ property 19 property |
| 21 | 23 | MIX Output | 455kHz IF Signal | MIX output pin Connect ceramic filter | 12 13 |
| 20 | 22 | NC | ill l e di | NC Pin | DON'T SOLL |
| | | | | | |

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