TOSHIBA Field Effect Transistor Silicon N Channel MOS Type $(\pi - MOSVII)$

TK9A45D

Switching Regulator Applications

• Low drain-source ON-resistance: RDS (ON) = 0.63 Ω (typ.)

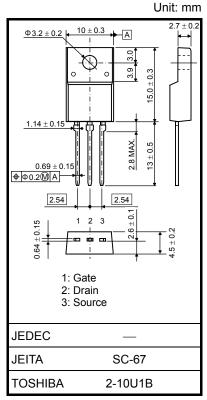
• High forward transfer admittance: $|Y_{fs}| = 4.8 \text{ S}$ (typ.)

• Low leakage current: $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 450 \text{ V)}$

• Enhancement-mode: $V_{th} = 2.0 \text{ to } 4.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characte	ristics	Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	450	V
Gate-source voltage		V_{GSS}	±30	V
Drain current	DC (Note 1)	I _D	9	Α
	Pulse (Note 1)	I _{DP}	36	A
Drain power dissipati	on (Tc = 25°C)	P _D	40	W
Single pulse avalanch	ne energy (Note 2)	E _{AS}	170	mJ
Avalanche current		I _{AR}	9	Α
Repetitive avalanche	energy (Note 3)	E _{AR}	4.0	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature	range	T _{stg}	-55 to 150	°C



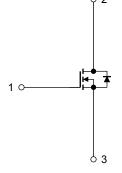
Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	3.125	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W

Internal Connection



Note 1: Please use devices on conditions that the channel temperature is below 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 3.5 mH, R_G = 25 Ω , I_{AR} = 9 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic sensitive device. Please handle with caution.



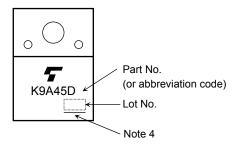
Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μА
Drain cut-off current		I _{DSS}	V _{DS} = 450 V, V _{GS} = 0 V	_	_	10	μА
Drain-source breakdown voltage		V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	450	_	_	V
Gate threshold vo	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	V
Drain-source ON	resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 4.5 A	_	0.63	0.77	Ω
Forward transfer admittance		Y _{fs}	V _{DS} = 10 V, I _D = 4.5 A	1.0	4.8	_	S
Input capacitance		C _{iss}		_	800	_	pF
Reverse transfer capacitance		C _{rss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	_	4	_	
Output capacitance		C _{oss}			100	_	
Switching time	Rise time	t _r	$\begin{array}{c c} 10 \text{ V} & \text{ID} = 4.5 \text{ A} & \text{Vout} \\ \hline V_{GS} & \text{O V} & \text{S} & \text{RL} = 44 \Omega \end{array}$	_	20	_	
	Turn-on time	t _{on}			40	_	ns
	Fall time	t _f			12		
	Turn-off time	t _{off}	Duty ≤ 1%, t _W = 10 μs	_	60	_	
Total gate charge		Qg		_	16	_	
Gate-source charge		Q _{gs}	$V_{DD} \approx 360 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 9\text{A}$	_	10	_	nC
Gate-drain charge		Q _{gd}		_	6	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

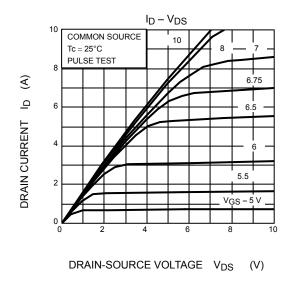
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current	(Note 1)	I _{DR}	_			9	Α
Pulse drain reverse current	(Note 1)	I _{DRP}	_	_	_	36	Α
Forward voltage (diode)		V_{DSF}	I _{DR} = 9 A, V _{GS} = 0 V			-1.7	V
Reverse recovery time		t _{rr}	I _{DR} = 9 A, V _{GS} = 0 V,	_	1200	_	ns
Reverse recovery charge		Q _{rr}	dl _{DR} /dt = 100 A/μs	_	10	_	μС

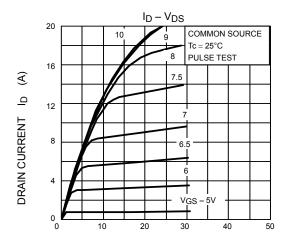
Marking



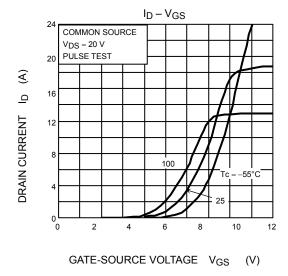
Note 4 : A line under a Lot No. identifies the indication of product Labels $\hbox{[[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]}$

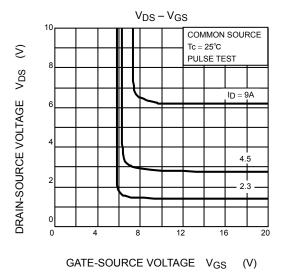
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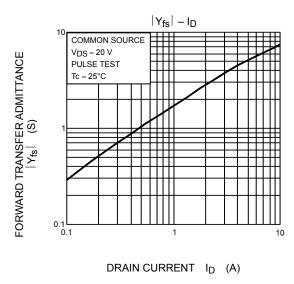


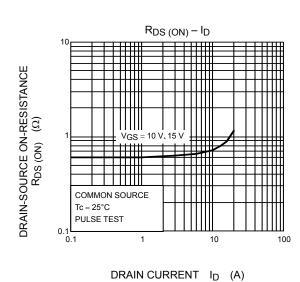






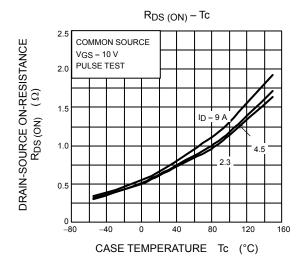


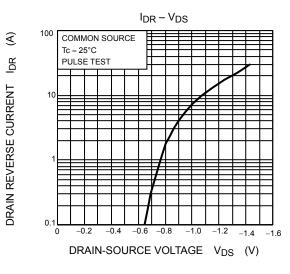


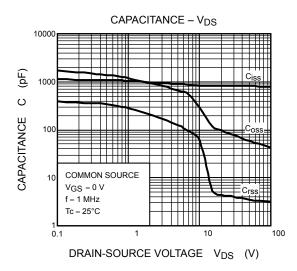


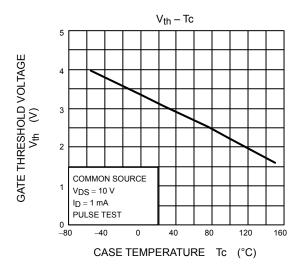
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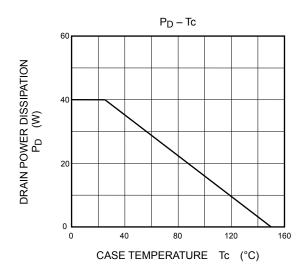
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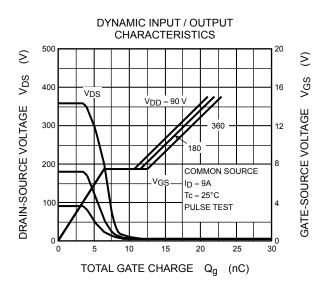


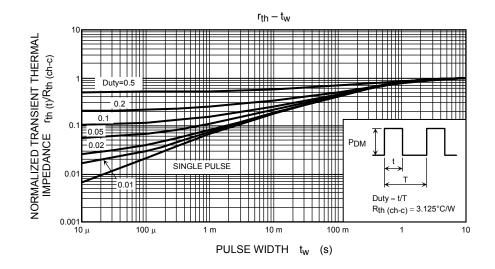


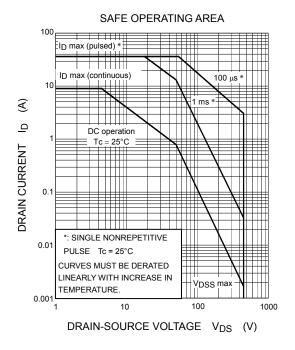


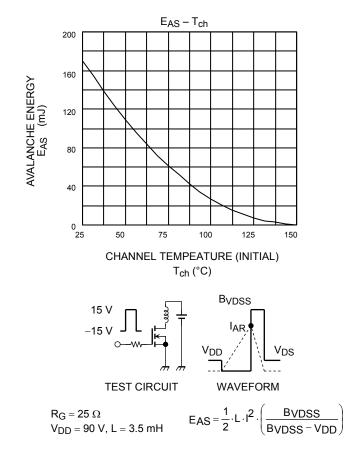












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