



# Description

The Advanced Ultra Low Power (AUP) CMOS logic family is designed for low power and extended battery life in portable applications.

The 74AUP2G07 is composed of two buffers with open drain outputs designed for operation over a power supply range of 0.8V to 3.6V. The device is fully specified for partial power down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output preventing damaging current backflow when the device is powered down. The gates perform the positive Boolean function:

 $\mathsf{Y} = \mathsf{A}$ 

### Features

- Advanced Ultra Low Power (AUP) CMOS
- Supply Voltage Range from 0.8V to 3.6V
- -4mA Output Drive at 3.0V
- Low Static power consumption
- I<sub>CC</sub> < 0.9μA
- Low Dynamic Power Consumption
- C<sub>PD</sub> = 0.6pF Typical at 3.6V
- Schmitt Trigger Action at All Inputs Make the Circuit Tolerant for Slower Input Rise and Fall Time. The hysteresis is typically 250mV at V<sub>CC</sub> = 3.0V
- IOFF Supports Partial-Power-Down Mode Operation
- ESD Protection per JESD 22
  - Exceeds 200-V Machine Model (A115)
  - Exceeds 2000-V Human Body Model (A114)
  - Exceeds 1000-V Charged Device Model (C101)
- Latch-Up Exceeds 100mA per JESD 78, Class I
- Leadless packages per JESD30E
  - DFN1410 denoted as X2-DFN1410-6
  - DFN1010 denoted as X2-DFN1010-6
  - DFN0910 denoted as X2-DFN0910-6
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
  - 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
  - See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  - 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

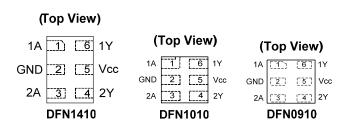
Click here for ordering information, located at the end of datasheet

Notes:

# 74AUP2G07

DUAL BUFFERS WITH OPEN DRAIN OUTPUTS

## **Pin Assignments**



# Applications

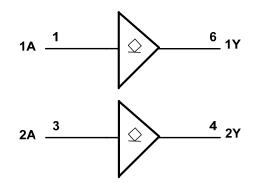
- Suited for battery and low power needs
- Wide array of products such as:
  - PCs, networking, notebooks, netbooks, PDAs
  - Tablet Computers, E-readers
  - Computer peripherals, hard drives, CD/DVD ROM
  - TV, DVD, DVR, set top box
  - Cell Phones, Personal Navigation / GPS
  - MP3 players ,Cameras, Video Recorders



# **Pin Descriptions**

Pin Name	Pin NO	Function
1A	1	Data Input
GND	2	Ground
2A	3	Data Input
2Y	4	Data Output
V <sub>CC</sub>	5	Supply Voltage
1Y	6	Data Output

# Logic Diagram



# **Function Table**

Inputs	Output					
nA	nY					
Н	Z					
L	L					



# Absolute Maximum Ratings (Note 4) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Parameter	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
ESD MM	Machine Model ESD Protection	200	V
V <sub>CC</sub>	Supply Voltage Range	-0.5 to +4.6	V
VI	Input Voltage Range	-0.5 to +4.6	V
Vo	Voltage applied to Output in High or Low State	-0.5 to V <sub>CC</sub> +0.5	V
l <sub>IK</sub>	Input Clamp Current VI < 0	50	mA
I <sub>OK</sub>	Output Clamp Current (V <sub>O</sub> < 0)	-50	mA
lo	Continuous Output Current ( $V_0 = 0$ to $V_{CC}$ )	±20	mA
Icc	Continuous Current Through V <sub>CC</sub>	50	mA
IGND	Continuous Current Through GND	-50	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T <sub>STG</sub>	Storage Temperature	-65 to +150	°C

Note: 4. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

# Recommended Operating Conditions (Note 5) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Pa	arameter	Min	Max	Unit	
Vcc	Operating Voltage		0.8	3.6	V	
VI	Input Voltage		0	3.6	V	
Vo	Output Voltage		0	V <sub>CC</sub>	V	
		$V_{\rm CC} = 0.8V$		20	μA	
		V <sub>CC</sub> = 1.1V		1.1		
		$V_{CC} = 1.4V$		1.7	mA	
I <sub>OL</sub>	Low-Level Output Current	V <sub>CC</sub> = 1.65V		1.9		
		V <sub>CC</sub> = 2.3V		3.1		
		V <sub>CC</sub> = 3.0V		4		
Δt/ΔV	Input Transition Rise or Fall Rate	V <sub>CC</sub> = 0.8V to 3.6V		200	ns/V	
T <sub>A</sub>	Operating Free-Air Temperature		-40	+125	°C	

Note: 5. Unused inputs should be held at  $V_{CC}$  or Ground.



# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

	Deremeter	Test	Conditions		T <sub>A</sub> = -	+25°C	T <sub>A</sub> = -40°C	to +85°C	Unit
Symbol	Parameter	lest	Conditions	V <sub>cc</sub>	Min	Мах	Min	Мах	Unit
				0.8V to 1.65V	0.80 X V <sub>CC</sub>		0.80 X V <sub>CC</sub>		
VIH	High-Level Input			1.65V to 1.95V	$0.65 \times V_{CC}$		$0.65 \text{ X V}_{CC}$		V
۷IH	Voltage			2.3V to 2.7V	1.6		1.6		v
				3.0V to 3.6V	2.0		2.0		
				0.8V to 1.65V		$0.30 \ X \ V_{CC}$		$0.30 \ X \ V_{CC}$	
VIL	Low-Level Input			1.65V to 1.95V		$0.35 \ X \ V_{CC}$		$0.35 \ X \ V_{CC}$	V
- 12	voltage			2.3V to 2.7V		0.7		0.7	
				3.0V to 3.6V		0.9		0.9	
		I <sub>OL</sub> = 20μΑ		0.8V to 3.6		0.1		0.1	
		$I_{OL} = 1.1 \text{mA}$		1.1V		0.3 X V <sub>CC</sub>		0.3 X V <sub>CC</sub>	
		I <sub>OL</sub> = 1.7mA		1.4V		0.31		0.37	
Vol	Low-Level Output	I <sub>OL</sub> = 1.9mA		1.65V		0.31		0.35	V
102	Voltage	I <sub>OL</sub> = 2.3mA		2.3V		0.31		0.33	
		I <sub>OL</sub> = 3.1mA				0.44		0.45	
		I <sub>OL</sub> = 2.7mA		3		0.31		0.33	
		$I_{OL}$ = 4mA				0.44		0.45	
h	Input Current	A or B Input	$V_1$ = GND to 3.6V	0V to 3.6V		± 0.1		± 0.5	μA
I <sub>OZ</sub>	Z State Leakage Current	V <sub>O</sub> = 3.6 V, V	/ <sub>i</sub> = 3.6 V	3.6V		± 0.1		± 0.5	μA
IOFF	Power Down Leakage Current	$V_1$ or $V_0$ = 0V to 3.6V		0V		± 0.2		± 0.6	μA
$\Delta I_{OFF}$	Delta Power Down Leakage Current	$V_1$ or $V_0$ = 0V to 3.6V		0V to 0.2V		± 0.2		± 0.6	μA
Icc	Supply Current	$V_{I} = GND$ or	$V_{CC}, I_{O} = 0$	0.8V to 3.6		0.5		0.9	μA
$\Delta I_{CC}$	Additional Supply Current	One input at inputs at V <sub>C0</sub>	V <sub>CC</sub> -0.6V Other	3.3V		40		50	μA
Dumph al	Domonia						T <sub>A</sub> = -40°C	to +125°C	Unit
Symbol	Paramet	er	lest Col	Test Conditions		V <sub>cc</sub>		Max	Uni
					0.8V to	0 1.65V	0.80 X V <sub>CC</sub>		
VIH	High-Level Input Volt	200			1.65V t	o 1.95V	0.70 X V <sub>CC</sub>		v
VIН		aye			2.3V t	o 2.7V	1.6		v
					3.0V t	o 3.6V	2.0		
					0.8V to	0 1.65V		$0.25 \ X \ V_{CC}$	
VIL	Low-Level InputVolta	ae			1.65V te			$0.30 \ X \ V_{CC}$	V
- 12		0 -			2.3V t			0.7	
						o 3.6V		0.9	
			I <sub>OL</sub> = 20μA			o 3.6V		0.11	
			I <sub>OL</sub> = 1.1mA			1V		0.33 X V <sub>CC</sub>	
			I <sub>OL</sub> = 1.7mA			4V		0.41	
Vol	Low-Level OutputVol	lane	I <sub>OL</sub> = 1.9mA		1.6	5V		0.39	V
VOL		ugo	I <sub>OL</sub> = 2.3mA		2:	3V		0.36	•
			I <sub>OL</sub> = 3.1mA		۷.,	50		0.50	
			$I_{OL} = 2.7 \text{mA}$		2	V		0.36	
			I <sub>OL</sub> = 4mA		3	V		0.50	
l <sub>l</sub>	Input Current	A or B Input, $V_1 = 0$		GND to 3.6V	0V to	9.6V		± 0.75	μA
I <sub>OZ</sub>	Z State Leakage Curr			6V	3.0	6V		± 0.75	μA
IOFF	Power Down Leakage				0	V		± 0.75	μA
ΔI <sub>OFF</sub>	Delta Power Down Le Current		$V_{\rm I}$ or $V_{\rm O}$ = 0V to 3.		0V to	0.2V		± 2.5	μA
Icc	Supply Current		$V_{I} = GND \text{ or } V_{CC},$	I <sub>O</sub> = 0	0.8V t	o 3.6V		1.4	μA
50	11 2 11 200			Other inputs at	+				



# **Switching Characteristics**

### C<sub>L</sub> = 5pF see Figure 1

Parameter	From Input	TO OUTPUT	V	T <sub>A</sub> = +25°C			T <sub>A</sub> = -40°C to +85°C		T <sub>A</sub> = -40°C to +125°C		Unit
			V <sub>cc</sub>	Min	Тур	Max	Min	Мах	Min	Max	Unit
			0.8V		12.8						
		v	1.2V ± 0.1V	2.6	5.8	11.3	2.3	12.5	2.3	15.9	
4	٨		1.5V ± 0.1V	1.8	3.6	6.4	1.6	7.4	1.6	8.2	
t <sub>pd</sub>	A	ř	1.8V ± 0.15V	1.5	2.9	5	1.4	5.9	1.4	6.5	ns
			2.5V ± 0.2V	1.2	2.4	3.9	1.1	4.5	1.1	5	
			3.3V ± 0.3V	0.9	3	3.5	0.8	3.9	0.8	4.3	

### C<sub>L</sub> = 10pF see Figure 1

Parameter	From Input	TO OUTPUT	N <sub>e</sub> e	-	T <sub>A</sub> = +25°C			T <sub>A</sub> = -40°C to +85°C		T <sub>A</sub> = -40°C to +125°C	
rarameter			Vcc	Min	Тур	Max	Min	Max	Min	Max	Unit
			0.8V		14.5						
		Y	1.2V ± 0.1V	3.1	7	13.4	2.9	15.1	2.9	19.2	
	۸		1.5V ± 0.1V	2.3	4.8	7.5	2.1	8.7	2.1	10.5	
t <sub>pd</sub>	A		1.8V ± 0.15V	2	3.8	4.8	1.8	7	1.8	7.7	ns
			2.5V ± 0.2V	1.6	3.1	4.6	1.5	5.4	1.5	6	
			3.3V ± 0.3V	1.2	4.3	4.9	1.1	5.4	1.1	5.9	

### $C_L$ = 15pF see Figure 1

Parameter	From	то	Vcc	T <sub>A</sub> = +25°C			T <sub>A</sub> = -40°C to +85°C		T <sub>A</sub> = -40°C to +125°C		Unit
Inpu	Input	OUTPUT	VCC	Min	Тур	Max	Min	Max	Min	Max	Unit
			0.8V		16.2						
		Y	1.2V ± 0.1V	3.5	8.2	14.3	3.3	17.4	3.3	22.5	- ns
	А		1.5V ± 0.1V	2.6	6.2	8.6	2.4	10.5	2.4	13.7	
t <sub>pd</sub>	A		1.8V ± 0.15V	2.3	5	6.7	2.1	8	2.1	9.8	
			2.5V ± 0.2V	2.1	3.9	5.1	1.8	6.1	1.8	6.8	
			3.3V ± 0.3V	1.6	5.6	6.4	1.4	7.1	1.4	7.8	

### C<sub>L</sub> = 30pF see Figure 1

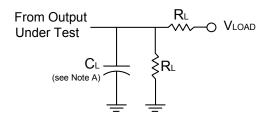
Parameter	From Input	TO OUTPUT	N <sub>a</sub> a	-	T <sub>A</sub> = +25°C		T <sub>A</sub> = -40°C to +85°C		T <sub>A</sub> = -40°C to +125°C		Unit
r al allietei			Vcc	Min	TYP	Min	Min	Мах	Min	Max	Onit
			0.8V		19.8						
		Y	1.2V ± 0.1V	4.8	9.8	18.4	4.4	18.4	4.4	25.8	ns
	А		1.5V ± 0.1V	3.6	8.2	13.9	3.2	13.9	3.2	18	
t <sub>pd</sub>	A		1.8V ± 0.15V	3.2	7.8	12.2	2.9	12.2	2.9	15.2	
			2.5V ± 0.2V	2.4	7.5	9.9	2.6	9.9	2.6	11.4	
			3.3V ± 0.3V	1.8	9.2	10.6	2.1	11.6	2.1	12.8	



# **Operating Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

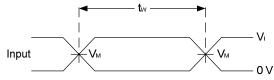
	Parameter	Test Conditions	V <sub>cc</sub>	Тур	Unit
			0.8V	0.3	
			1.2V ± 0.1V	0.4	
0	Device Dissignation Connectories	f = 1MHz	1.5V ± 0.1V	0.5	~ <b>C</b>
C <sub>pd</sub>	Power Dissipation Capacitance	No Load	1.8V ± 0.15V	0.5	pF
			2.5V ± 0.2V	0.5	
			3.3V ± 0.3V	0.6	
CI	Input Capacitance	V <sub>I</sub> = V <sub>CC</sub> or GND	0V or 3.3V	2.0	pF
Co	Output Capacitance	$V_0 = V_{CC}$ or GND	0V	2.0	pF

# **Parameter Measurement Information**

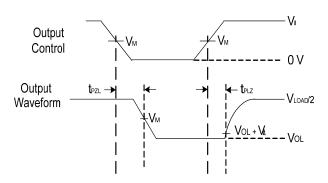


TEST	Condition
t <sub>PLZ</sub> (see Notes D and E)	Vload
t <sub>PZL</sub> (see Notes D and F)	Vload

M	Inp	outs	V	N/	<u> </u>		V۸	
Vcc	VI	t <sub>r</sub> /t <sub>f</sub>	V <sub>M</sub>	VLOAD	C∟	R∟	VΔ	
0.8V	Vcc	≤3ns	V <sub>CC</sub> /2	2 X V <sub>CC</sub>	5, 10, 15, 30pF	5 kΩ	0.1V	
1.2V±0.1V	V <sub>CC</sub>	≤3ns	V <sub>CC</sub> /2	2 X V <sub>CC</sub>	5, 10, 15, 30pF	5 kΩ	0.1V	
1.5V±0.1V	V <sub>CC</sub>	≤3ns	V <sub>CC</sub> /2	2 X V <sub>CC</sub>	5, 10, 15, 30pF	5 kΩ	0.15V	
1.8V±0.15V	V <sub>CC</sub>	≤3ns	V <sub>CC</sub> /2	2 X V <sub>CC</sub>	5, 10, 15, 30pF	5 kΩ	0.15V	
2.5V±0.2V	V <sub>CC</sub>	≤3ns	V <sub>CC</sub> /2	2 X V <sub>CC</sub>	5, 10, 15, 30pF	5 kΩ	0.15V	
3.3V±0.3V	V <sub>CC</sub>	≤3ns	V <sub>CC</sub> /2	$2 X V_{CC}$	5, 10, 15, 30pF	5 kΩ	0.3V	



Voltage Waveform Pulse Duration



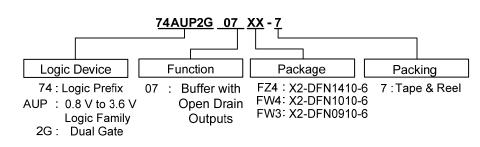
Voltage Waveform Propagation Delay Times

#### Figure 1 Load Circuit andVoltage Waveforms

- Notes: A. Includes test lead and test apparatus capacitance.
  - B. All pulses are supplied at pulse repetition rate  $\leq$  10MHz.
  - C. The inputs are measured one at a time with one transition per measurement.
  - D. For the open drain device  $t_{\mathsf{PLZ}}$  and  $t_{\mathsf{PZL}}$  are the same as  $t_{\mathsf{PD}}.$
  - E.  $t_{PZL}$  is measured at V<sub>M</sub>.
  - D.  $t_{PLZ}$  is measured at V\_OL +V\_{\Delta}.



# **Ordering Information**



Part Number F	Package Code Packaging	7" Tape and Reel (Note 6)		
Fart Number		Fackaging	Quantity	Part Number Suffix
74AUP2G07FZ4-7	FZ4	X2-DFN1410-6	5000/Tape & Reel	-7
74AUP2G07FW4-7	FW4	X2-DFN1010-6	5000/Tape & Reel	-7
74AUP2G07FW3-7	FW3	X2-DFN0910-6	5000/Tape & Reel	-7

Note: 6. The taping orientation is located on our website at http://www.diodes.com/datasheets/ap02007.pdf

# **Marking Information**

### (1) X2-DFN1410-6, X2-DFN1010-6, X2-DFN0910-6



 XX : Identification Code
Y : Year : 0~9
W: Week : A~Z : 1~26 week; a~z : 27~52 week; z represents 52 and 53 week

X : A~Z : Internal code

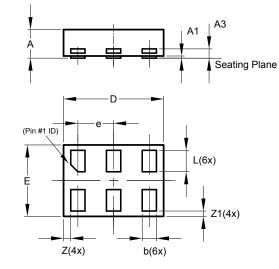
Part Number	Package	Identification Code
74AUP2G07FZ4	X2-DFN1410-6	RP
74AUP2G07FW4	X2-DFN1010-6	SP
74AUP2G07FW3	X2-DFN0910-6	MP



# Package Outline Dimensions (All dimensions in mm.)

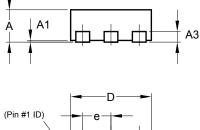
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

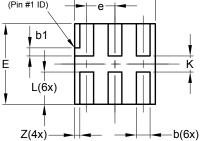
### (1) Package Type X2-DFN1410-6



X2-DFN1410-6			
Dim	Min	Max	Тур
Α		0.40	0.39
A1	0.00	0.05	0.02
A3			0.13
b	0.15	0.25	0.20
D	1.35	1.45	1.40
Е	0.95	1.05	1.00
е			0.50
L	0.25	0.35	0.30
Z			0.10
Z1	0.045	0.105	0.075
All	All Dimensions in mm		

### (2) Package Type: X2-DFN1010-6





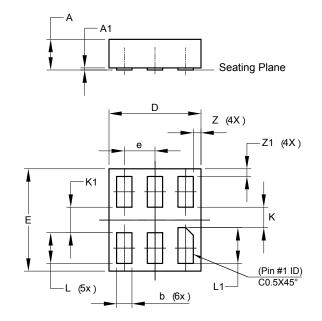
X2-DFN1010-6			
Dim	Min	Max	Тур
Α	-	0.40	0.39
A1	0.00	0.05	0.02
A3			0.13
b	0.14	0.20	0.17
b1	0.05	0.15	0.10
D	0.95	1.05	1.00
Е	0.95	1.05	1.00
е			0.35
L	0.35	0.45	0.40
К	0.15		
Z	_	-	0.065
All Dimensions in mm			



# Package Outline Dimensions (cont.) (All dimensions in mm.)

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

### (3) Package Type: X2-DFN0910-6



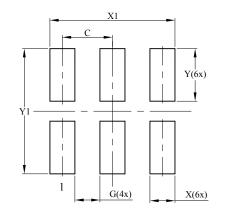
X2-DFN0910-6			
Dim	Min	Max	Тур
Α	-	0.35	0.30
A1	0	0.03	0.02
b	0.10	0.20	0.15
D	0.85	0.95	0.90
Е	0.95	1.05	1.00
е	-	-	0.30
к	0.20	-	-
K1	0.25	-	-
L	0.25	0.35	0.30
L1	0.30	0.40	0.35
Ζ	-	-	0.075
Z1	-	-	0.075
All [	All Dimensions in mm		



# **Suggested Pad Layout**

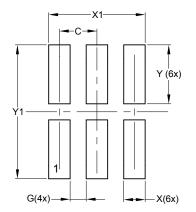
Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

### (1) Package Type X2-DFN1410-6



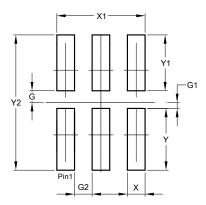
Dimensions	Value (in mm)
С	0.500
G	0.250
X	0.250
X1	1.250
Y	0.525
Y1	1.250

### (2) Package Type: X2-DFN1010-6



Dimensions	Value (in mm)
С	0.350
G	0.150
Х	0.200
X1	0.900
Y	0.550
Y1	1.250

### (3) Package Type: X2-DFN0910-6



Dimensions	Value (in mm)
G	0.100
G1	0.050
G2	0.150
Х	0.150
X1	0.750
Y	0.525
Y1	0.475
Y2	1.150



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