

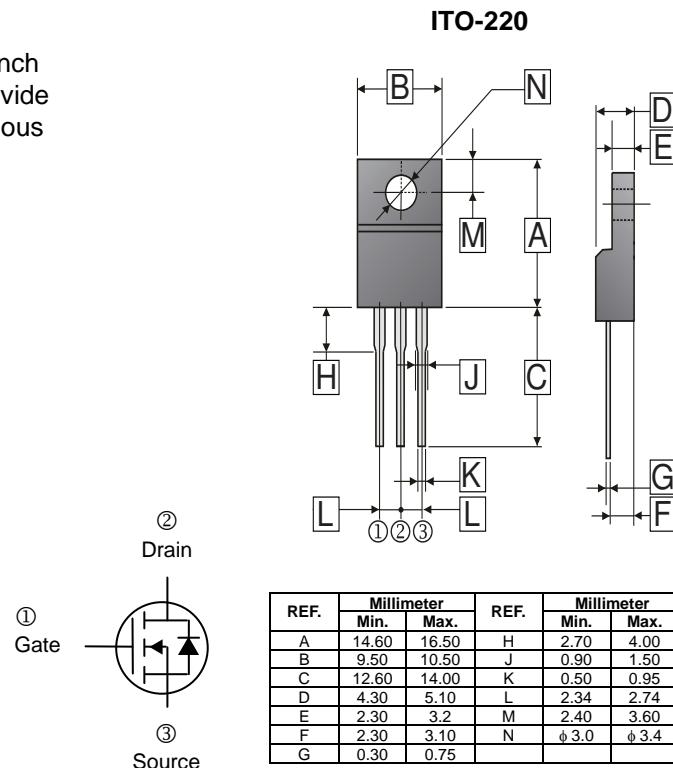
RoHS Compliant Product  
A suffix of "-C" specifies halogen free

## DESCRIPTION

The SSRF07N65SL is the highest performance trench N-ch MOSFETs with extreme high cell density, which provide excellent  $R_{DS(on)}$  and gate charge for most of the synchronous buck converter applications.

## FEATURES

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available



## ABSOLUTE MAXIMUM RATINGS ( $T_A=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	650	V
Gate-Source Voltage	$V_{GS}$	$\pm 30$	V
Continuous Drain Current $T_C=25^\circ\text{C}$	$I_D$	7	A
		4	A
Pulsed Drain Current	$I_{DM}$	28	A
Total Power Dissipation $T_C=25^\circ\text{C}$	$P_D$	46	W
		0.37	
Single Pulse Avalanche Energy <sup>1</sup>	$E_{AS}$	435	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55~150	°C
Thermal Resistance Rating			
Maximum Thermal Resistance Junction-Ambient	$R_{\theta JA}$	120	°C / W
Maximum Thermal Resistance Junction-Case	$R_{\theta JC}$	2.7	°C / W

Notes:

1.  $L=30\text{mH}$ ,  $I_{AS}=5\text{A}$ ,  $V_{DD}=100\text{V}$ ,  $R_G=25\Omega$ , Starting  $T_J = 25^\circ\text{C}$

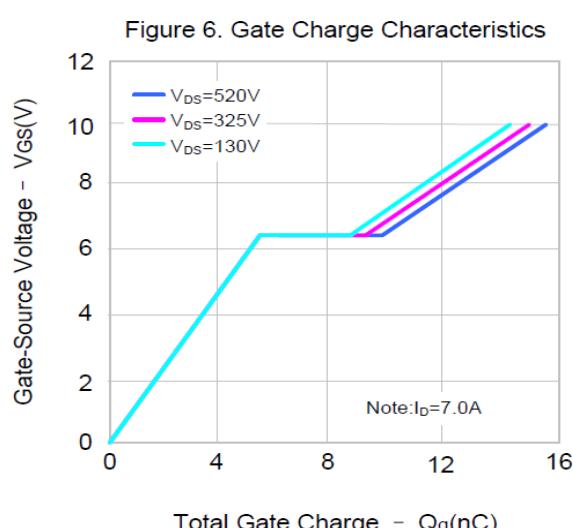
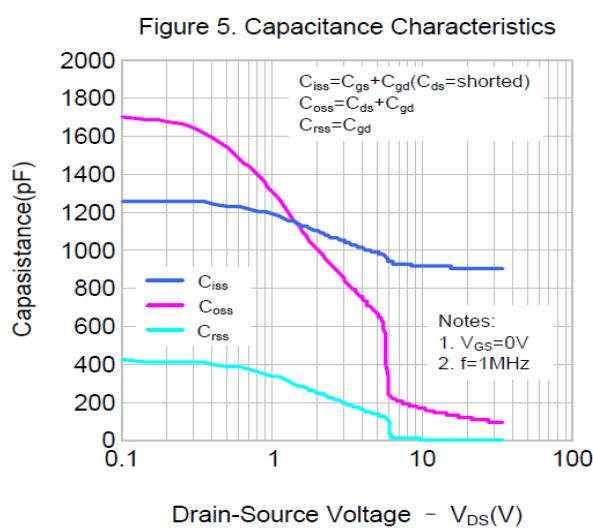
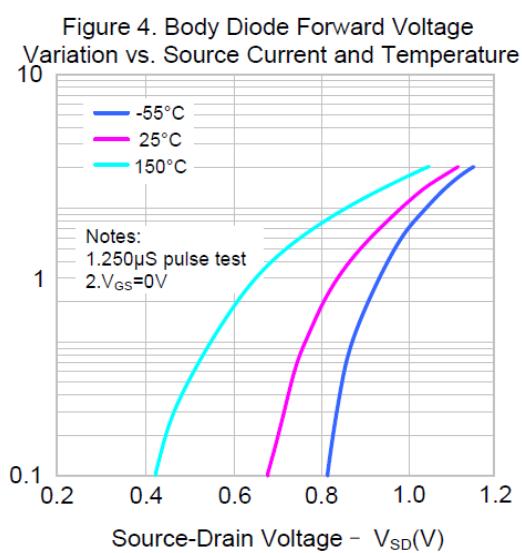
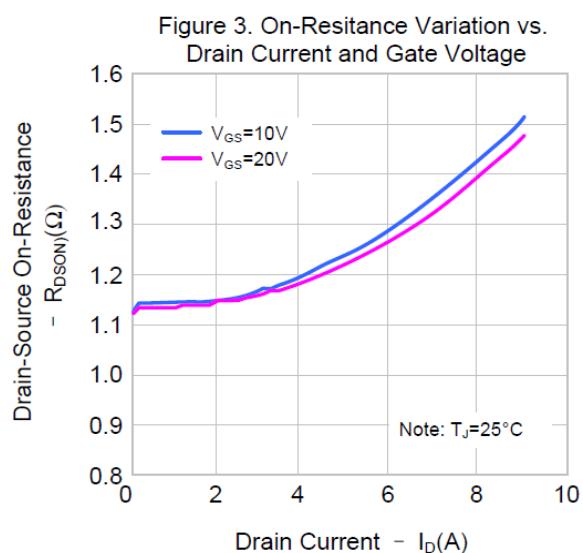
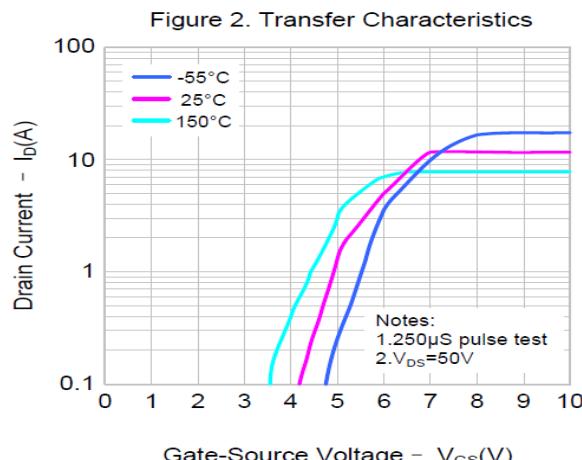
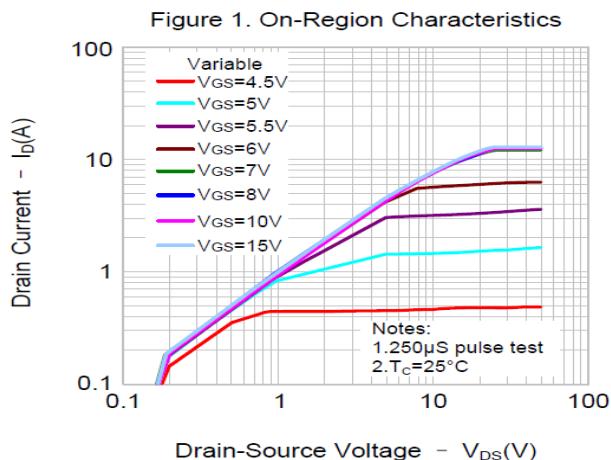
**ELECTRICAL CHARACTERISTICS** ( $T_J = 25^\circ\text{C}$  unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	650	-	-	V	$V_{GS}=0$ , $I_D=250\mu\text{A}$
Gate-Threshold Voltage	$V_{GS(\text{th})}$	2	-	4	V	$V_{DS}=V_{GS}$ , $I_D=250\mu\text{A}$
Gate-Source Leakage Current	$I_{GSS}$	-	-	$\pm 100$	nA	$V_{GS} = \pm 30\text{V}$
Drain-Source Leakage Current	$I_{DSS}$	-	-	1	$\mu\text{A}$	$V_{DS}=650\text{V}$ , $V_{GS}=0$
Static Drain-Source On-Resistance	$R_{DS(\text{ON})}$	-	1.1	1.4	$\Omega$	$V_{GS}=10\text{V}$ , $I_D=3.5\text{A}$
Total Gate Charge <sup>1,2</sup>	$Q_g$	-	15.5	-	nC	$I_D=7\text{A}$ $V_{DS}=520\text{V}$ $V_{GS}=10\text{V}$
Gate-Source Charge <sup>1,2</sup>	$Q_{gs}$	-	5.4	-		
Gate-Drain Charge <sup>1,2</sup>	$Q_{gd}$	-	4.5	-		
Turn-on Delay Time <sup>1,2</sup>	$T_{d(\text{on})}$	-	29	-	nS	$V_{DD}=325\text{V}$ $I_D=7\text{A}$ $R_G=25\Omega$
Rise Time <sup>1,2</sup>	$T_r$	-	48	-		
Turn-off Delay Time <sup>1,2</sup>	$T_{d(\text{off})}$	-	39	-		
Fall Time <sup>1,2</sup>	$T_f$	-	33	-		
Input Capacitance	$C_{iss}$	-	903.3	-	pF	$V_{GS}=0$ $V_{DS}=25\text{V}$ $f=1.0\text{MHz}$
Output Capacitance	$C_{oss}$	-	97.7	-		
Reverse Transfer Capacitance	$C_{rss}$	-	3.1	-		
<b>Source-Drain Diode</b>						
Diode Forward Voltage	$V_{SD}$	-	-	1.4	V	$I_S=7\text{A}$ , $V_{GS}=0$
Continuous Source Current	$I_S$	-	-	7	A	Integral Reverse P-N Junction Diode in the MOSFET
Pulsed Source Current	$I_{SM}$	-	-	28	A	
Reverse Recovery Time <sup>1</sup>	$T_{rr}$	-	532.77	-	ns	$I_S=7\text{A}$ , $V_{GS}=0$ , $dI_F/dt=100\text{A}/\mu\text{s}$
Reverse Recovery Charge <sup>1</sup>	$Q_{rr}$	-	3.57	-	$\mu\text{C}$	

Notes:

1. Pulse Test: Pulse width  $\leq 300\mu\text{s}$ , Duty cycle  $\leq 2\%$
2. Essentially independent of operating temperature.

## CHARACTERISTIC CURVES



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Figure 7. Breakdown Voltage Variation vs. Temperature

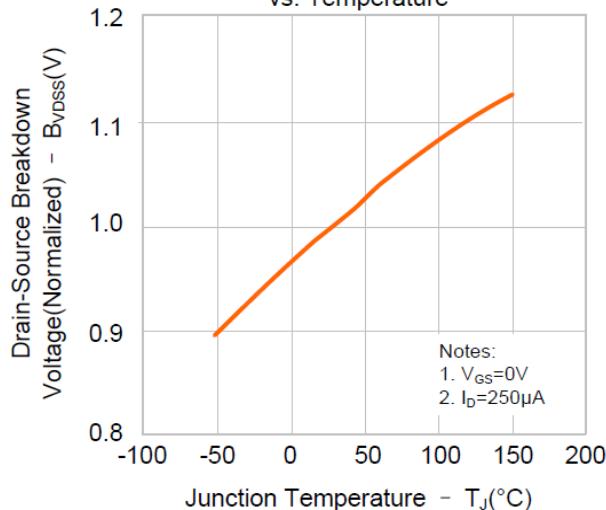


Figure 8. On-resistance Variation vs. Temperature

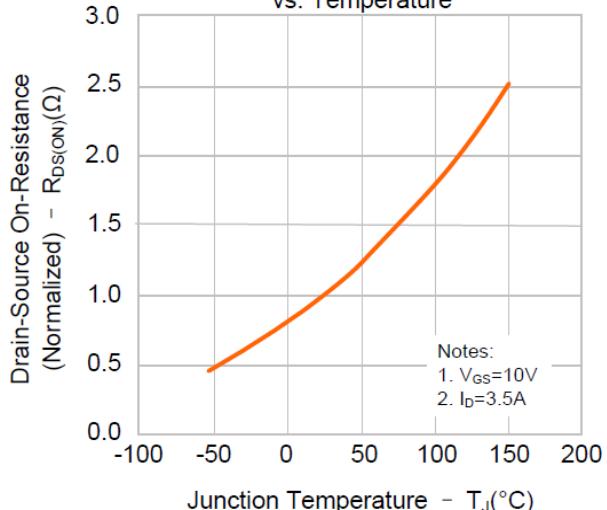


Figure 9. Max. Safe Operating Area

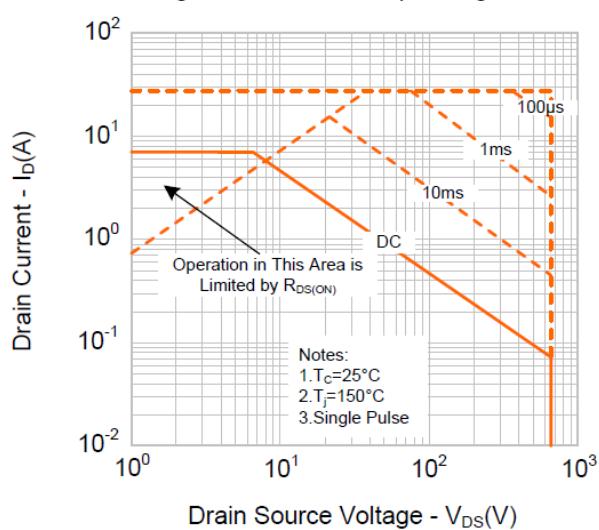
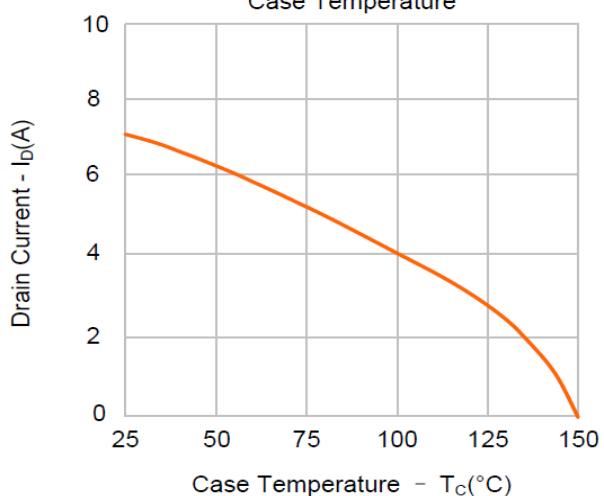
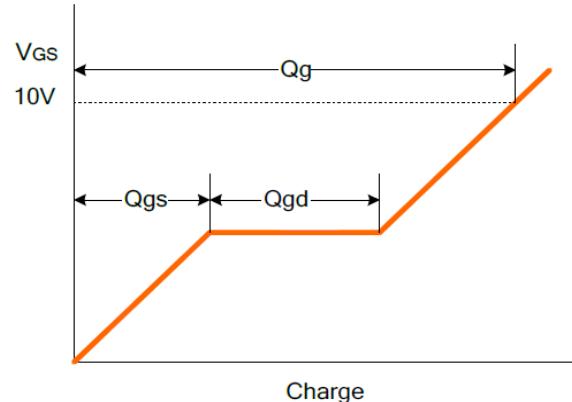
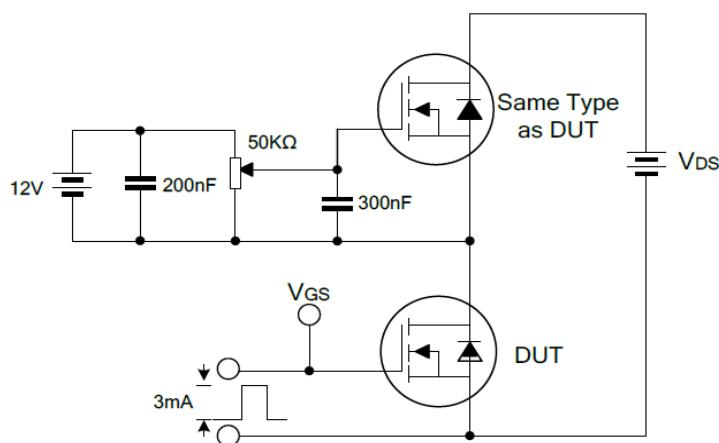


Figure 10. Maximum Drain Current vs. Case Temperature

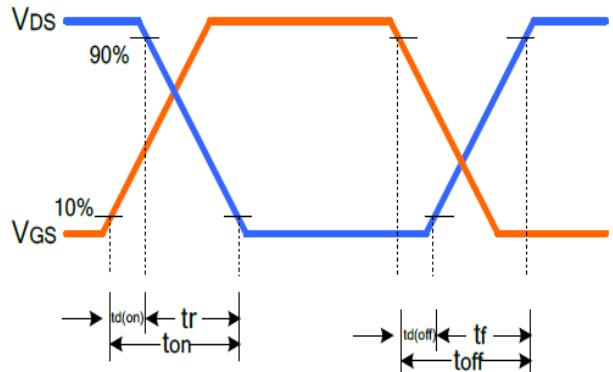
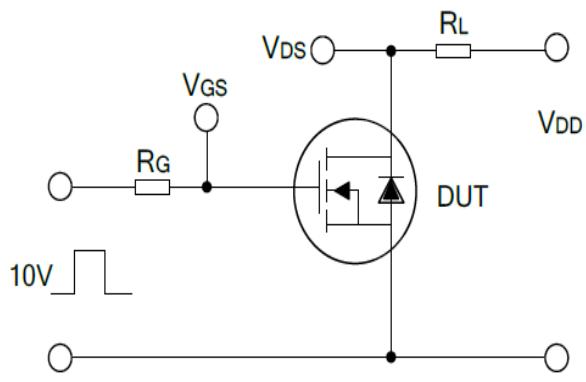


## TYPICAL TEST CURVES

Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveform



Unclamped Inductive Switching Test Circuit & Waveform

