



SPN3055

N-Channel Enhancement Mode MOSFET

DESCRIPTION

The SPN3055 is the N-Channel logic enhancement mode power field effect transistors are produced using high cell density , DMOS trench technology.

This high density process is especially tailored to minimize on-state resistance. These devices are particularly suited for low voltage application, such as DC/DC converter and Desktop computer power management.

The package is universally preferred for commercial industrial surface mount applications

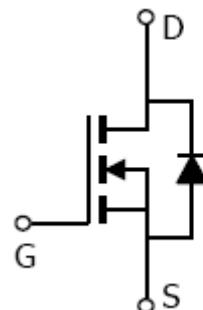
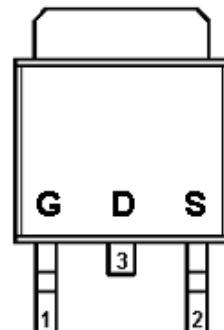
APPLICATIONS

- Power Management in Desktop Computer
- DC/DC Converter
- LCD Display inverter

FEATURES

- ◆ 30V/12A, $R_{DS(ON)}=60m\Omega$ @ $V_{GS}=10V$
- ◆ 30V/6A, $R_{DS(ON)}=80m\Omega$ @ $V_{GS}=4.5V$
- ◆ Super high density cell design for extremely low $R_{DS (ON)}$
- ◆ Exceptional on-resistance and maximum DC current capability
- ◆ TO-252-2L package design

PIN CONFIGURATION (TO-252-2L)



PART MARKING





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PIN DESCRIPTION

Pin	Symbol	Description
1	G	Gate
2	S	Source
3	D	Drain

ORDERING INFORMATION

Part Number	Package	Part Marking
SPN3055T252RG	TO-252-2L	SPN3055
SPN3055T252RGB	TO-252-2L	SPN3055

- ※ SPN3055T252RG : Tape Reel ; Pb – Free
※ SPN3055T252RGB : Tape Reel ; Pb – Free ; Halogen - Free

ABSOLUTE MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Typical	Unit
Drain-Source Voltage	V _{DSS}	30	V
Gate –Source Voltage	V _{GSS}	±20	V
Continuous Drain Current(T _J =150°C)	T _A =25°C	12	A
	T _A =70°C	8	
Pulsed Drain Current	I _{DM}	20	A
Continuous Source Current(Diode Conduction)	I _S	12	A
Power Dissipation	T _A =25°C	40	W
	T _A =70°C	20	
Operating Junction Temperature	T _J	-55/150	°C
Storage Temperature Range	T _{STG}	-55/150	°C
Thermal Resistance-Junction to Ambient	R _{θJA}	100	°C/W



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ELECTRICAL CHARACTERISTICS

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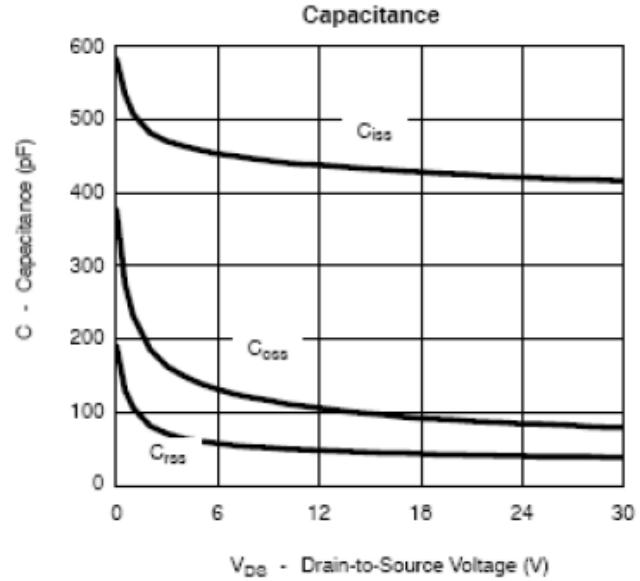
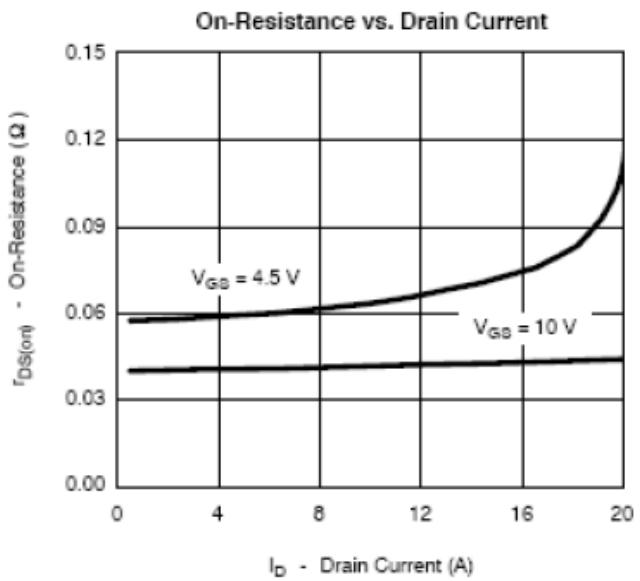
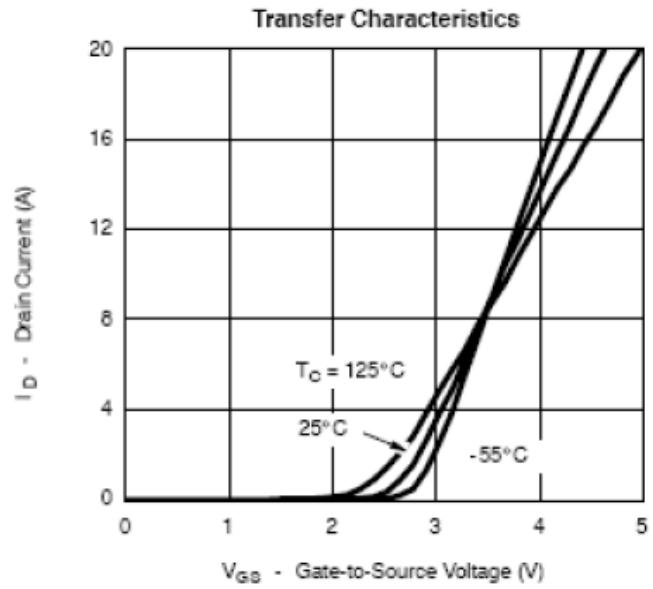
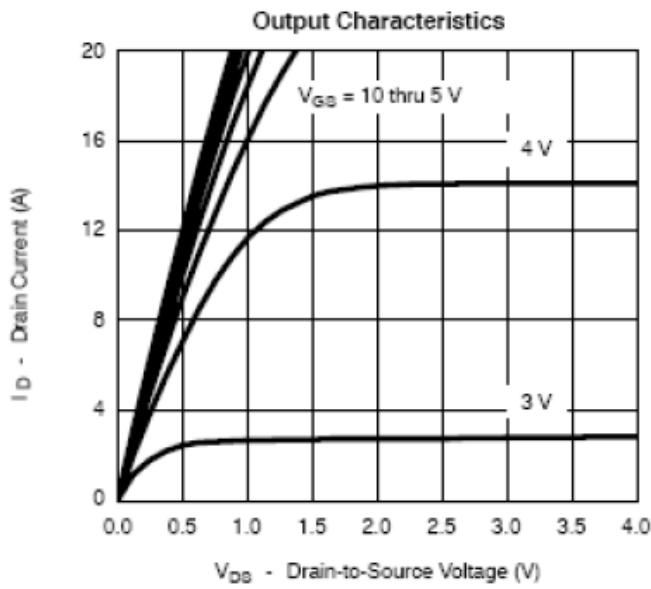
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V(BR)DSS	VGS=0V, ID=-250uA	30			V
Gate Threshold Voltage	VGS(th)	VDS=VGS, ID=-250uA	1.0		3.0	
Gate Leakage Current	IGSS	VDS=0V, VGS=±20V			±100	nA
Zero Gate Voltage Drain Current	IDSS	VDS=24V, VGS=0V			1	uA
		VDS=24V, VGS=0V TJ=85°C			10	
Drain-Source On-Resistance	RDS(on)	VGS=10V, ID=12A		0.050	0.06	Ω
		VGS=4.5V, ID=6A		0.067	0.080	
Forward Transconductance	gfs	VDS=10V, ID=12A		20		S
Diode Forward Voltage	VSD	IS=6A, VGS =0V		1.0	1.2	V
Dynamic						
Total Gate Charge	Qg	VDS=15V, VGS=10V ID=12A		4.5	10	nC
Gate-Source Charge	Qgs			0.8		
Gate-Drain Charge	Qgd			1.0		
Input Capacitance	Ciss	VDS=15V, VGS=0V f=1MHz		240		pF
Output Capacitance	Coss			110		
Reverse Transfer Capacitance	Crss			17		
Turn-On Time	td(on)	VDD=15V, RL=15Ω ID=1.0A, VGEN=10V RG=6Ω		8	20	ns
	tr			12	30	
Turn-Off Time	td(off)			17	35	
	tf			8	20	



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TYPICAL CHARACTERISTICS

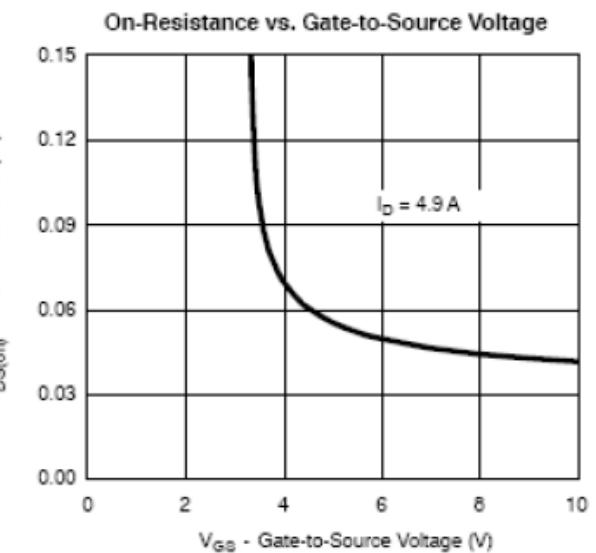
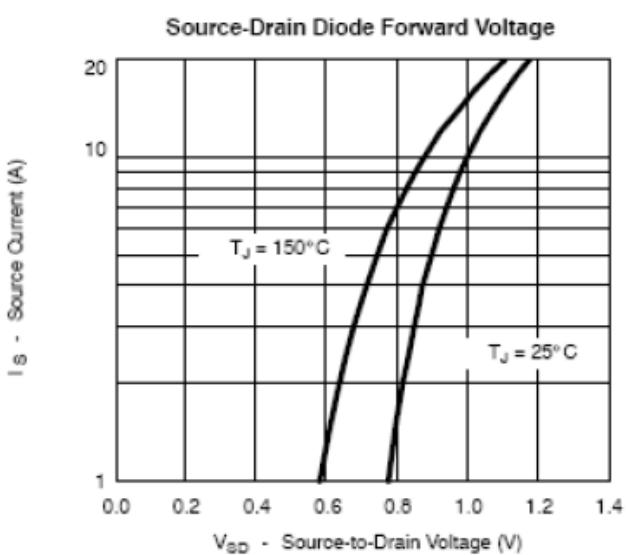
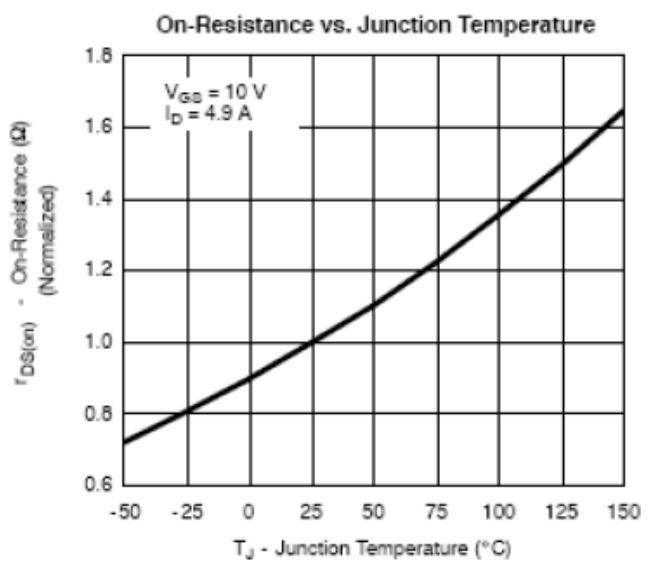
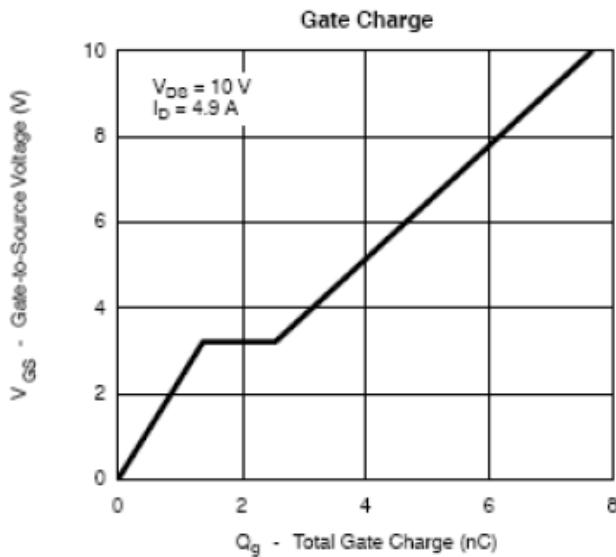




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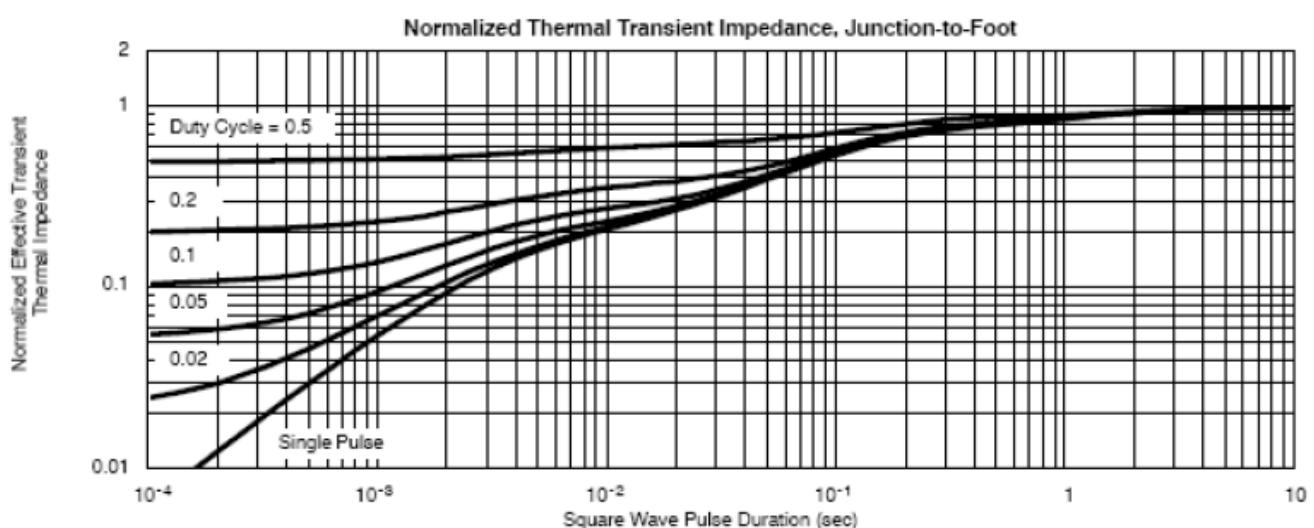
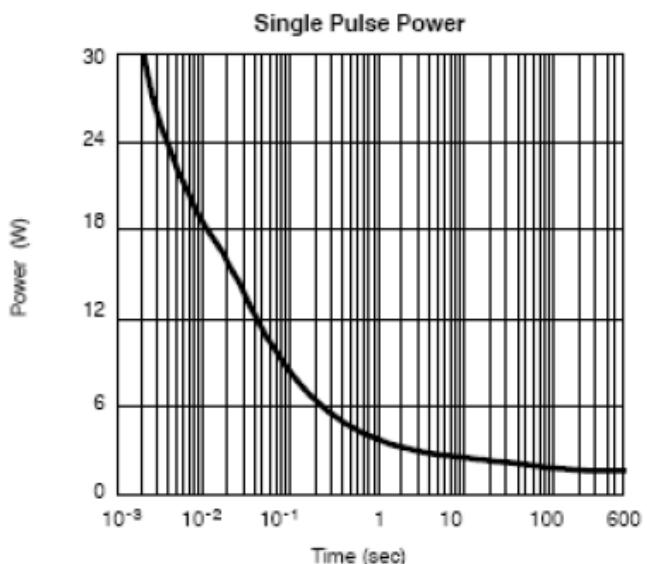
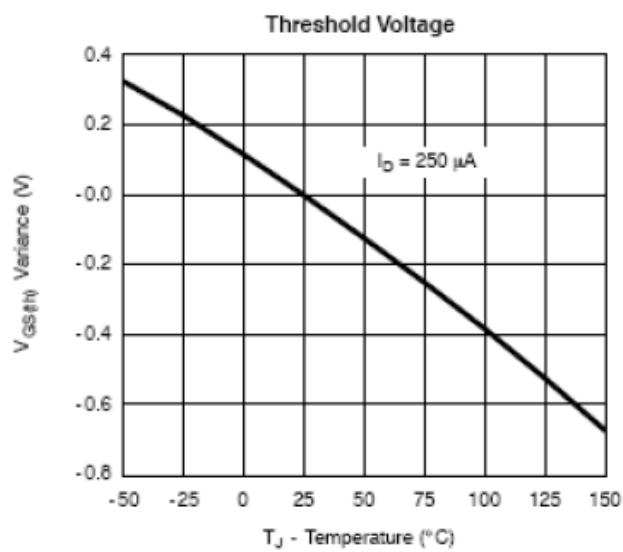




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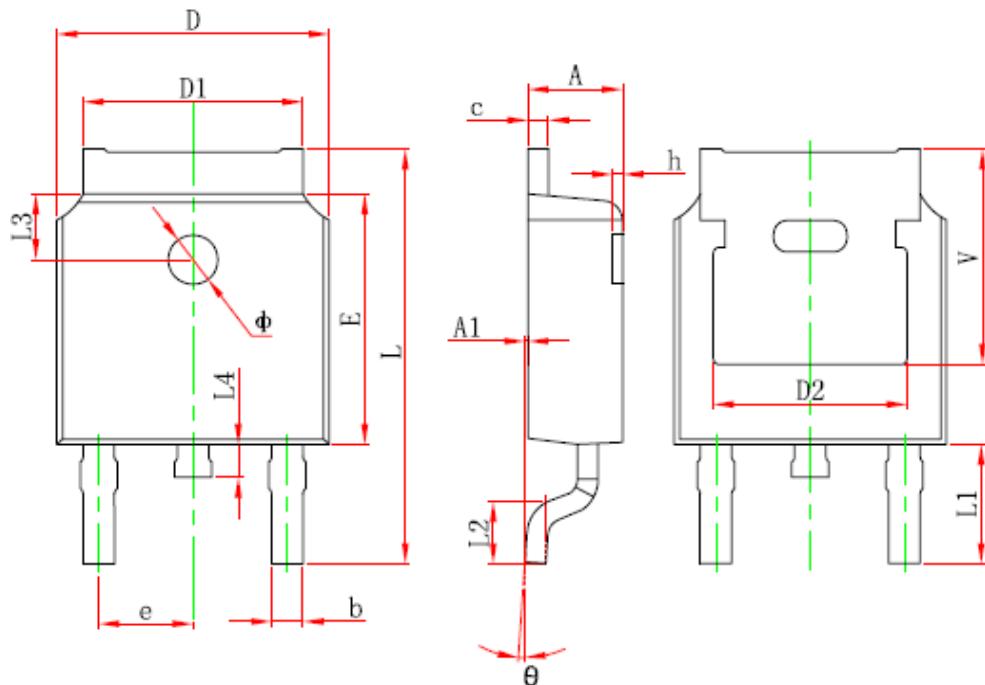




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TO-252-2L PACKAGE OUTLINE



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.660	0.860	0.026	0.034
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.830 REF.		0.190 REF.	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.800	10.400	0.386	0.409
L1	2.900 REF.		0.114 REF.	
L2	1.400	1.700	0.055	0.067
L3	1.600 REF.		0.063 REF.	
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.350 REF.		0.211 REF.	



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