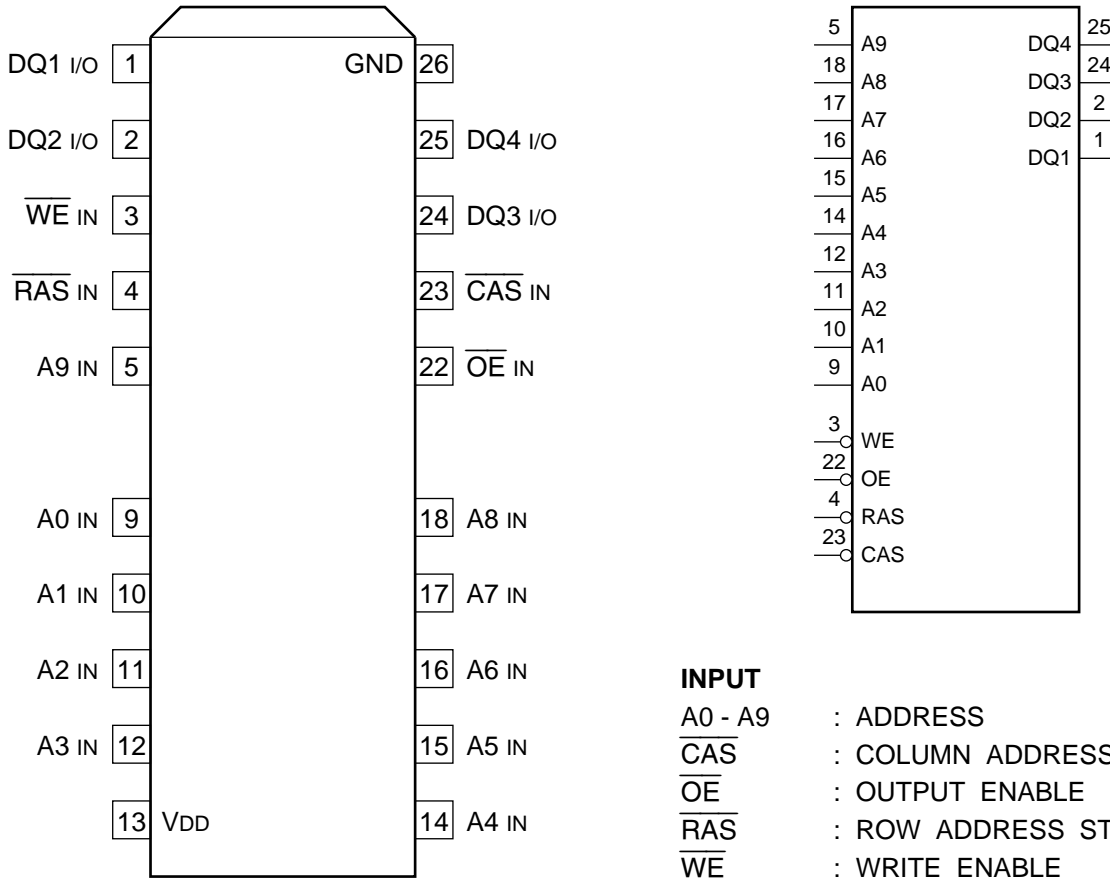


### C-MOS 4 M (1,048,576 × 4)-BIT DRAM

—TOP VIEW—



**INPUT**

- A0 - A9 : ADDRESS
- CAS : COLUMN ADDRESS STROBE
- OE : OUTPUT ENABLE
- RAS : ROW ADDRESS STROBE
- WE : WRITE ENABLE

**INPUT/OUTPUT**

- DQ1 - DQ4 : DATA

