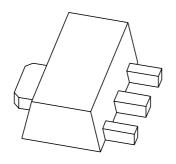
DISCRETE SEMICONDUCTORS

DATA SHEET



PXT2907A PNP switching transistor

Product data sheet Supersedes data of 2002 Mar 20 2004 Dec 09



PNP switching transistor

PXT2907A

FEATURES

- High current (max. 600 mA)
- Low voltage (max. 60 V).

APPLICATIONS

• Switching and linear amplification.

DESCRIPTION

PNP switching transistor in a SOT89 plastic package. NPN complement: PXT2222A.

MARKING

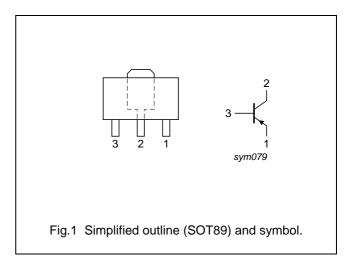
TYPE NUMBER	MARKING CODE ⁽¹⁾
PXT2907A	*2F

Note

- 1. * = p: Made in Hong Kong.
 - * = t: Made in Malaysia.
 - * = W: Made in China.

PINNING

PIN	DESCRIPTION
1	emitter
2	collector
3	base



ORDERING INFORMATION

TYPE NUMBER	PACKAGE			
TIPE NOWBER	NAME DESCRIPTION		VERSION	
PXT2907A	SC-62	plastic surface mounted package; collector pad for good heat transfer; 3 leads	SOT89	

PNP switching transistor

PXT2907A

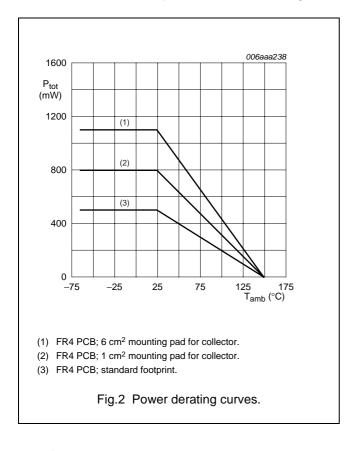
LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	-60	V
V _{CEO}	collector-emitter voltage	open base	_	-60	V
V _{EBO}	emitter-base voltage	open collector	_	-5	V
I _C	collector current (DC)		-	-600	mA
I _{CM}	peak collector current		_	-800	mA
I _{BM}	peak base current		-	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C			
		note 1	_	0.5	W
		note 2	_	0.8	W
		note 3	_	1.1	W
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		-	150	°C
T _{amb}	ambient temperature		-65	+150	°C

Notes

- 1. Device mounted on a printed-circuit board, single-sided copper, tin-plated and standard footprint.
- 2. Device mounted on a printed-circuit board, single-sided copper, tin-plated and mounting pad for collector 1 cm².
- 3. Device mounted on a printed-circuit board, single-sided copper, tin-plated and mounting pad for collector 6 cm².



PNP switching transistor

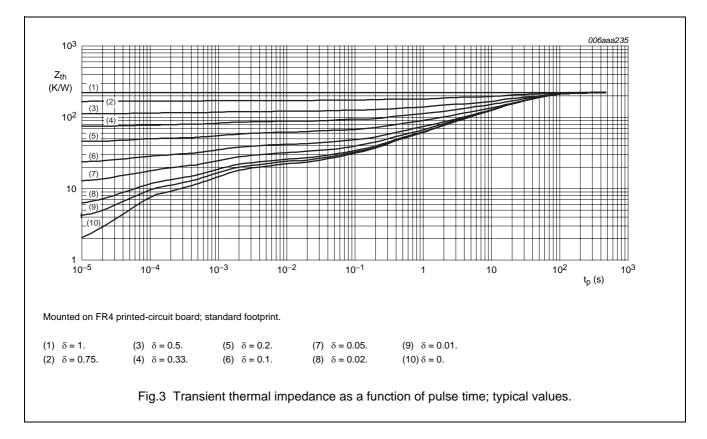
PXT2907A

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th(j-a)}	thermal resistance from junction to	in free air		
	ambient	note 1	250	K/W
		note 2	156	K/W
		note 3	113	K/W
R _{th(j-s)}	thermal resistance from junction to soldering point		30	K/W

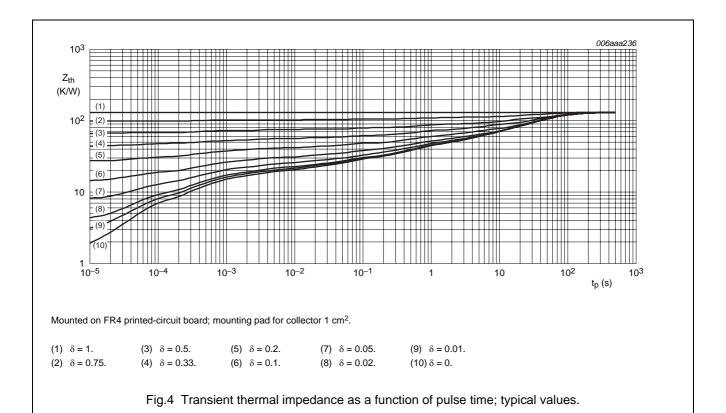
Notes

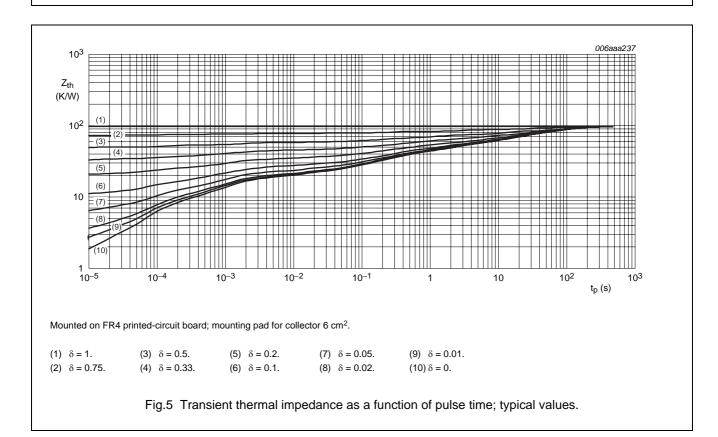
- 1. Device mounted on a printed-circuit board, single-sided copper, tin-plated and standard footprint.
- 2. Device mounted on a printed-circuit board, single-sided copper, tin-plated and mounting pad for collector 1 cm².
- 3. Device mounted on a printed-circuit board, single-sided copper, tin-plated and mounting pad for collector 6 cm².



PNP switching transistor

PXT2907A





PNP switching transistor

PXT2907A

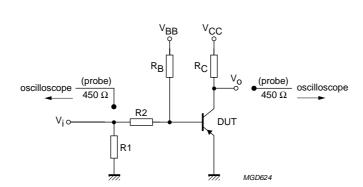
CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector-base cut-off current	$I_E = 0 \text{ A}; V_{CB} = -50 \text{ V}$	_	-10	nA
		$I_E = 0 \text{ A}; V_{CB} = -50 \text{ V}; T_{amb} = 125 ^{\circ}\text{C}$	_	-10	μΑ
I _{EBO}	emitter-base cut-off current	$I_C = 0 \text{ A}; V_{EB} = -5 \text{ V}$	_	-50	nA
h _{FE}	DC current gain	$I_C = -0.1 \text{ mA}; V_{CE} = -1 \text{ V}$	75	_	
		$I_C = -1 \text{ mA}; V_{CE} = -1 \text{ V}$	100	_	
		$I_C = -10 \text{ mA}; V_{CE} = -1 \text{ V}$	100	_	
		$I_C = -150 \text{ mA}; V_{CE} = -2 \text{ V}$	100	300	
ı		$I_C = -500 \text{ mA}; V_{CE} = -10 \text{ V}$	50	_	
V _{CEsat}	collector-emitter saturation	$I_C = -150 \text{ mA}; I_B = -15 \text{ mA}$	_	-400	mV
voltage	voltage	$I_C = -500 \text{ mA}; I_B = -50 \text{ mA}$	_	-1.6	V
V _{BEsat}	base-emitter saturation voltage	$I_C = -150 \text{ mA}; I_B = -15 \text{ mA}$	_	-1.3	V
		$I_C = -500 \text{ mA}; I_B = -50 \text{ mA}$	_	-2.6	V
C _c	collector capacitance	$I_E = i_e = 0 \text{ A}; V_{CB} = -10 \text{ V}; f = 1 \text{ MHz}$	_	8	pF
C _e	emitter capacitance	$I_C = i_c = 0 \text{ A}; V_{EB} = -500 \text{ mV};$ f = 1 MHz	_	35	pF
f _T	transition frequency	$I_C = -20 \text{ mA}; V_{CE} = -10 \text{ V};$ f = 100 MHz	200	_	MHz
Switching t	imes (between 10% and 90% leve	ls); (see Fig.6)		•	
t _{on}	turn-on time	$I_{Con} = -150 \text{ mA}; I_{Bon} = -15 \text{ mA};$	_	40	ns
t _d	delay time	I _{Boff} = 15 mA	_	12	ns
t _r	rise time	1	_	30	ns
t _{off}	turn-off time	1	_	365	ns
t _s	storage time	1	_	300	ns
t _f	fall time	1	_	65	ns

PNP switching transistor

PXT2907A



 $V_i = -9.5$ V; T = 500 $\mu s;$ $t_p = 10$ $\mu s;$ $t_r = t_f \leq 3$ ns.

R1 = 68 Ω ; R2 = 325 Ω ; R_B = 325 Ω ; R_C = 160 Ω .

 V_{BB} = 3.5 V; V_{CC} = –29.5 V.

Oscilloscope input impedance Z_i = 50 Ω .

Fig.6 Test circuit for switching times.

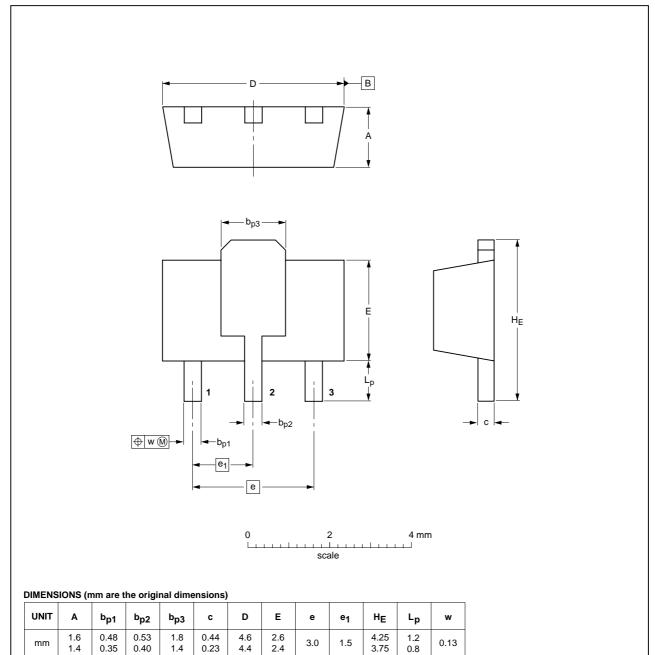
PNP switching transistor

PXT2907A

PACKAGE OUTLINE

Plastic surface-mounted package; collector pad for good heat transfer; 3 leads

SOT89



OUTLINE	REFERENCES		EUROPEAN	ICCUE DATE		
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT89		TO-243	SC-62			04-08-03 06-03-16

PNP switching transistor

PXT2907A

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

- 1. Please consult the most recently issued document before initiating or completing a design.
- 2. The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

DISCLAIMERS

General — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in medical, military, aircraft, space or life support equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) may cause permanent damage to the device. Limiting values are stress ratings only and operation of the device at these or any other conditions

above those given in the Characteristics sections of this document is not implied. Exposure to limiting values for extended periods may affect device reliability.

Terms and conditions of sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nxp.com/profile/terms, including those pertaining to warranty, intellectual property rights infringement and limitation of liability, unless explicitly otherwise agreed to in writing by NXP Semiconductors. In case of any inconsistency or conflict between information in this document and such terms and conditions, the latter will prevail.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from national authorities.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

NXP Semiconductors

Customer notification

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

Contact information

For additional information please visit: http://www.nxp.com
For sales offices addresses send e-mail to: salesaddresses@nxp.com

© NXP B.V. 2009

All rights are reserved. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner.

The information presented in this document does not form part of any quotation or contract, is believed to be accurate and reliable and may be changed without notice. No liability will be accepted by the publisher for any consequence of its use. Publication thereof does not convey nor imply any license under patent- or other industrial or intellectual property rights.

Printed in The Netherlands R75/05/pp10 Date of release: 2004 Dec 09 Document order number: 9397 750 13895

