

SOT-23-6L Plastic-Encapsulate MOSFETS

NCE8205 Dual N-Channel MOSFET

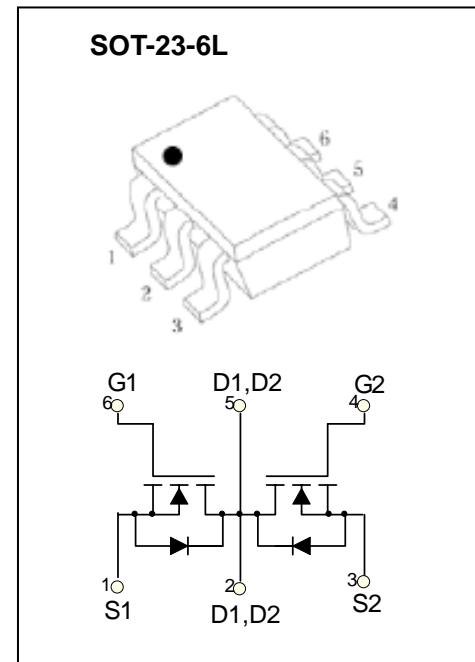
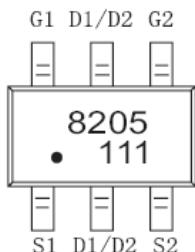
FEATURE

- TrenchFET Power MOSFET
- Excellent $R_{DS(on)}$
- Low Gate Charge
- High Power and Current Handling Capability
- Surface Mount Package

APPLICATION

- Battery Protection
- Load Switch
- Power Management

MARKING



ABSOLUTE MAXIMUM RATINGS ($T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	19	V
Gate-Source Voltage	V_{GS}	± 10	V
Continuous Drain Current	I_D	6	A
Pulsed Drain Current (note 1)	I_{DM}	25	A
Thermal Resistance from Junction to Ambient (note 2)	$R_{\theta JA}$	357	$^\circ\text{C}/\text{W}$
Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature	T_{STG}	-55~+150	$^\circ\text{C}$
Lead Temperature for Soldering Purposes(1/8" from case for 10 s)	T_L	260	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS($T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
STATIC CHARACTERISTICS						
Drain-source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_{\text{D}} = 250\mu\text{A}$	19			V
Zero gate voltage drain current	I_{DSS}	$V_{\text{DS}} = 18\text{V}, V_{\text{GS}} = 0\text{V}$			1	μA
Gate-body leakage current	I_{GSS}	$V_{\text{GS}} = \pm 10\text{V}, V_{\text{DS}} = 0\text{V}$			± 100	nA
Gate threshold voltage (note 3)	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_{\text{D}} = 250\mu\text{A}$	0.45		1.2	V
Drain-source on-resistance (note 3)	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 4.5\text{V}, I_{\text{D}} = 6\text{A}$			27	$\text{m}\Omega$
		$V_{\text{GS}} = 2.5\text{V}, I_{\text{D}} = 5\text{A}$			37	$\text{m}\Omega$
Forward transconductance (note 3)	g_{FS}	$V_{\text{DS}} = 5\text{V}, I_{\text{D}} = 4.5\text{A}$		10		S
Diode forward voltage (note 3)	V_{SD}	$I_{\text{S}} = 1.25\text{A}, V_{\text{GS}} = 0\text{V}$			1.2	V
DYNAMIC CHARACTERISTICS (note 4)						
Input Capacitance	C_{iss}	$V_{\text{DS}} = 8\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		800		pF
Output Capacitance	C_{oss}			155		pF
Reverse Transfer Capacitance	C_{rss}			125		pF
SWITCHING CHARACTERISTICS (note 4)						
Turn-on delay time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 10\text{V}, V_{\text{GS}} = 4\text{V}, I_{\text{D}} = 1\text{A}, R_{\text{GEN}} = 10\Omega$		18		ns
Turn-on rise time	t_{r}			5		ns
Turn-off delay time	$t_{\text{d}(\text{off})}$			43		ns
Turn-off fall time	t_{f}			20		ns
Total Gate Charge	Q_{g}	$V_{\text{DS}} = 10\text{V}, V_{\text{GS}} = 4.5\text{V}, I_{\text{D}} = 4\text{A}$		11		nC
Gate-Source Charge	Q_{gs}			2.3		nC
Gate-Drain Charge	Q_{gd}			2.5		nC

Notes :

- 1.Repetitive rating: Pulse width limited by maximum junction temperature
- 2.Surface Mounted on FR4 board, $t \leq 10$ sec.
3. Pulse test : Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production.