



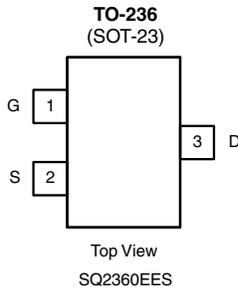
SQ2360EES



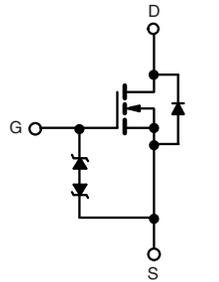
PRODUCT SUMMARY	
V _{DS} (V)	60
R _{DS(on)} (Ω) at V _{GS} = 10 V	0.085
R _{DS(on)} (Ω) at V _{GS} = 4.5 V	0.130
I _D (A)	4.4
Configuration	Single

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- AEC-Q101 Qualified^c
- 100 % R_G and UIS Tested
- Typical ESD Protection 800 V
- Compliant to RoHS Directive 2002/95/EC



Marking Code: 8Mxxx



ORDERING INFORMATION	
Package	SOT-23
Lead (Pb)-free and Halogen-free	SQ2360EES-T1-GE3

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)			
PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V _{DS}	60	V
Gate-Source Voltage	V _{GS}	± 20	
Continuous Drain Current	I _D	T _C = 25 °C	4.4
		T _C = 125 °C	2.5
Continuous Source Current (Diode Conduction)	I _S	3.7	A
Pulsed Drain Current ^a	I _{DM}	17	
Single Pulse Avalanche Current	I _{AS}	6	
Single Pulse Avalanche Energy	E _{AS}	1.8	mJ
Maximum Power Dissipation ^a	P _D	T _C = 25 °C	
		T _C = 125 °C	1
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to + 175	°C

THERMAL RESISTANCE RATINGS			
PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient	R _{thJA}	166	°C/W
Junction-to-Foot (Drain)	R _{thJF}	50	

Notes

- Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %.
- When mounted on 1" square PCB (FR-4 material).
- Parametric verification ongoing.



SQ2360EES

SPECIFICATIONS ($T_C = 25\text{ }^\circ\text{C}$, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static							
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0, I_D = 250\text{ }\mu\text{A}$	60	-	-	V	
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	1.5	-	2.5		
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$	-	-	± 5.5	μA	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{GS} = 0\text{ V}$	$V_{DS} = 60\text{ V}$	-	-	1	μA
		$V_{GS} = 0\text{ V}$	$V_{DS} = 60\text{ V}, T_J = 125\text{ }^\circ\text{C}$	-	-	50	
		$V_{GS} = 0\text{ V}$	$V_{DS} = 60\text{ V}, T_J = 175\text{ }^\circ\text{C}$	-	-	150	
On-State Drain Current ^a	$I_{D(on)}$	$V_{GS} = 10\text{ V}$	$V_{DS} \geq 5\text{ V}$	10	-	-	A
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = 10\text{ V}$	$I_D = 6\text{ A}, T_J = 25\text{ }^\circ\text{C}$	-	0.058	0.085	Ω
		$V_{GS} = 10\text{ V}$	$I_D = 6\text{ A}, T_J = 125\text{ }^\circ\text{C}$	-	-	0.197	Ω
		$V_{GS} = 10\text{ V}$	$I_D = 6\text{ A}, T_J = 175\text{ }^\circ\text{C}$	-	-	0.258	
		$V_{GS} = 4.5\text{ V}$	$I_D = 5\text{ A}$	-	0.081	0.130	
Forward Transconductance ^b	g_{fs}	$V_{DS} = -15\text{ V}, I_D = 1.9\text{ A}$		-	5.8	-	S
Dynamic^b							
Input Capacitance	C_{iss}	$V_{GS} = 0\text{ V}$	$V_{DS} = 25\text{ V}, f = 1\text{ MHz}$	-	295	370	μF
Output Capacitance	C_{oss}			-	55	70	
Reverse Transfer Capacitance	C_{rss}			-	35	55	
Total Gate Charge ^c	Q_g	$V_{GS} = 10\text{ V}$	$V_{DS} = 30\text{ V}, I_D = 2\text{ A}$	-	7.40	12	nC
Gate-Source Charge ^c	Q_{gs}			-	0.95	-	
Gate-Drain Charge ^c	Q_{gd}			-	1.94	-	
Gate Resistance	R_g	$f = 1\text{ MHz}$		1.24	2.46	3.68	Ω
Turn-On Delay Time ^c	$t_{d(on)}$	$V_{DD} = 30\text{ V}, R_L = 15\text{ }\Omega$ $I_D \cong 2\text{ A}, V_{GEN} = 10\text{ V}, R_g = 1\text{ }\Omega$		-	5	8	ns
Rise Time ^c	t_r			-	11	17	
Turn-Off Delay Time ^c	$t_{d(off)}$			-	10	15	
Fall Time ^c	t_f			-	8	12	
Source-Drain Diode Ratings and Characteristics^b							
Pulsed Current ^a	I_{SM}			-	-	17	A
Forward Voltage	V_{SD}	$I_F = 1.5\text{ A}, V_{GS} = 0$		-	0.8	1.2	V

Notes

- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.