

Spec. No. : C564QD Issued Date : 2012.12.11 Revised Date : Page No. : 1/10

Quadruple Operational Comparator

Description

The LM324QD consists of four independent high gain, internally frequency compensated operational amplifiers which are designed specifically to operate from a single power supply over a wide range of voltages. Operation from split power supplies is also possible and the low power supply current drain is independent of the magnitude of the power supply voltage.

Features

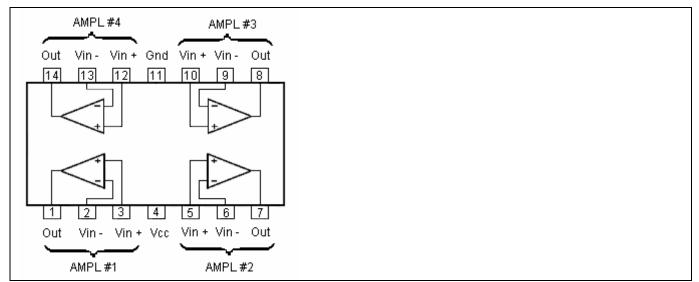
- Wide range of supply voltages
- •Low supply current drain independent of supply voltage
- Low input biasing current
- •Low input offset voltage and offset current
- Differential input voltage range equal to the power supply voltage
- Input common-mode voltage range includes ground
- DC voltage gain 100V/mV typ
- •Internally frequency compensation

Applications

- General purpose amplifier
- Transducer amplifier
- DC gain block



Pin Configurations



Absolute Maximum Rating(Ta=25°C)

Parameter	Symbol	Value	Unit
Power Supply Voltages			
Single Supply	Vcc	32	V
Split Supplies	Vcc, Vee	±16	
Input Differential Voltage Range	Vidr	±32	V
Input Common Mode Voltage Range	VICR	-0.3 ~ +32	V
Operating Temperature	Topr	$0 \sim +70$	°C
Storage Temperature	Tstg	$-65 \sim +150$	°C

Electrical Characteristics (Vcc=5V,Ta=25°C, VEE=GND, unless otherwise specified.)

Parameter	Test Condition		Symbol	Min.	Тур.	Max.	Unit
Input Offset Voltage	Vcc=5 to 30V	25°C	Vio	-	3	7	mV
mpar e mor e enge	VIC=VICR min, Vo=1.4V	0~70°C	, 10		-	9	
Average temperature coefficient of input offset voltage		0~70°C	∂ V io	-	7	-	$\mu V/^{\circ}C$
In must Official Command	$V_{0}-1$ $4V_{0}$	25°C	Iro		2	50	
Input Offset Current	Vo=1.4V	0~70°C	Ію	-	-	150	nA
Average temperature coefficient of input offset current		0~70°C	dio	-	10	-	pA/°C
Input Bias Current	Vo=1.4V	25°C	Ів	-	-20	-250	
		0~70°C			-	-500	nA
Input Common-Mode Voltage	Vcc=5V to 30V	25°C	VICR	0 to Vcc-1.5	-		V
		0~70°C	VICR	0 to Vcc-2	-		v



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Power Supply Current (four amplifiers)	Ro=2.5V, no load	0~70°C	Icc	-	1.5	2.4	mA
	Vcc=30V, Vo=0.5Vcc, no load	0~70°C		-	1.1	3	mA
Large Signal Differential Voltage	Vcc=15V, Vo(p)=1V to	25°C	Avd	25	100	-	V/mV
Amplification	11V, RL $\geq 2k\Omega$	0~70°C	1100	15	-	-	
High Level Output Voltage	$RL=2k\Omega$	25°C		Vcc-1.5	-	-	V
	VCC=30V, RL= $2k\Omega$	0~70°C	Voh	26	-	-	
	Vcc=30V, RL=10k Ω	0~70°C		27	28	-	
Low Level Output Voltage	$R_L=10k\Omega$	0~70°C	Vol	-	5	20	mV
Common Mode Rejection Ratio	Vcc=5V to 30V Vic=Vicr min	25°C	CMRR	65	80	-	dB
Power Supply Rejection Ratio	Vcc=5V to 30V	25°C	PSRR	65	100	-	dB
Channel Crosstalk Attenuation	f=1kHz to 20kHz	25°C	CS	-	120	-	dB
Short Circuit Output Current	Vcc at 5V, GND at -5V, Vo=0V	25°C	Isc	-	±40	±60	mA
Output Current	Vcc=15V, Vid=1V, Vo=0V	25°C	ю	-20	-30	-	mA mA
		0~70°C		-10	-	-	
	Vcc=15V, Vid=-1V,	25°C		10	20	-	
	Vo=15V	0~70°C		5	-	-	
	VID=-1V, VO=0.2V	25°C		12	30	-	μΑ

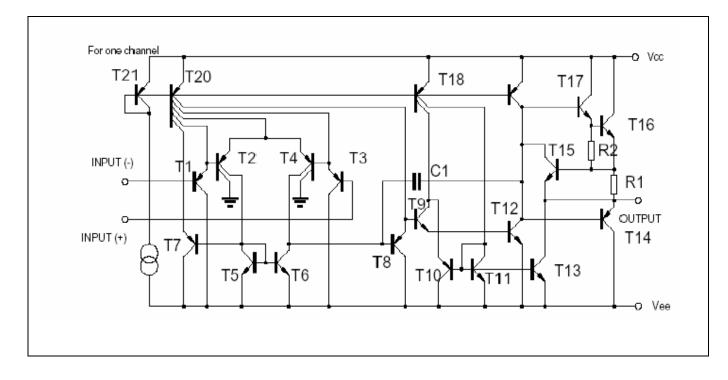
Ordering Information

Device	Package	Shipping
LM324QD-0-T3-G	SOP-14 (Pb-free lead plating and halogen-free package)	2500 pcs/ Tape & Reel



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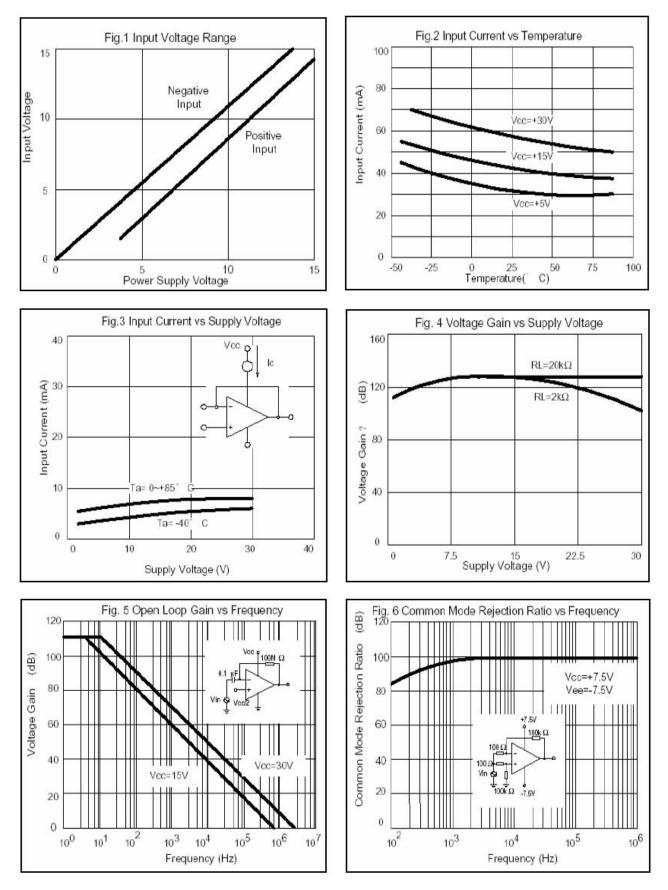
Block Diagram, each channel



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Characteristic Curves, each channel

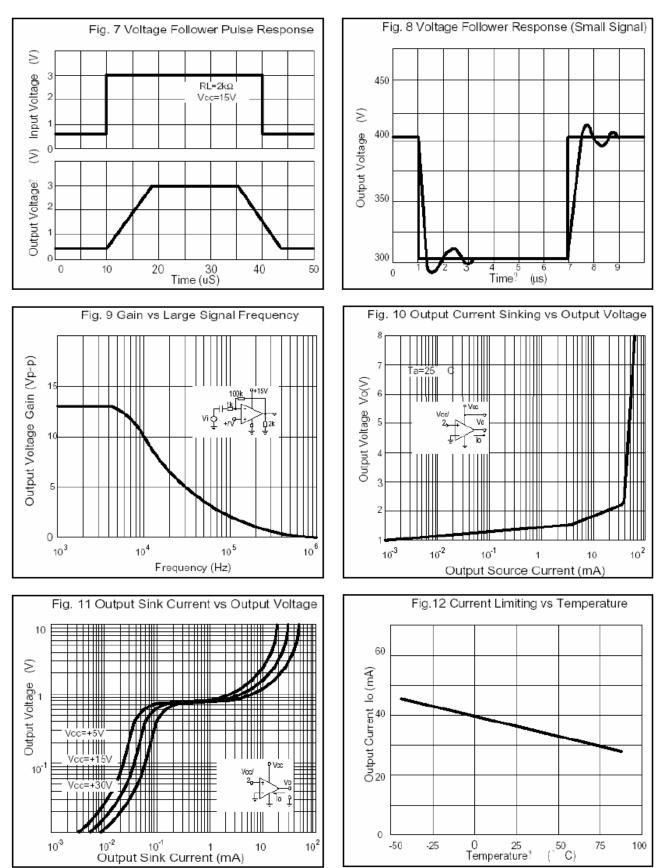
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Characteristic Curves, each channel(Cont.)

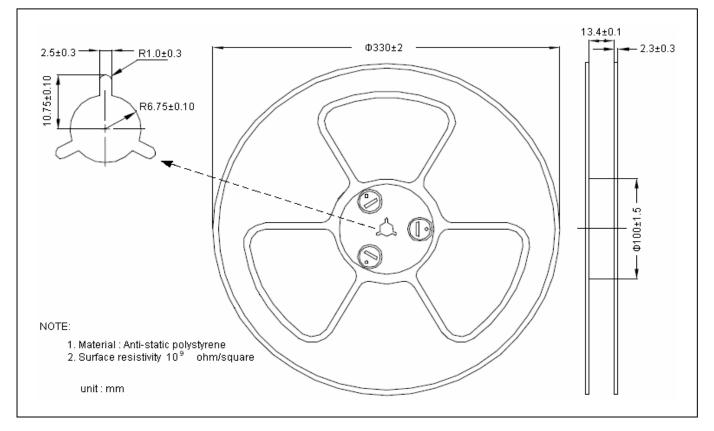
Cy/tek



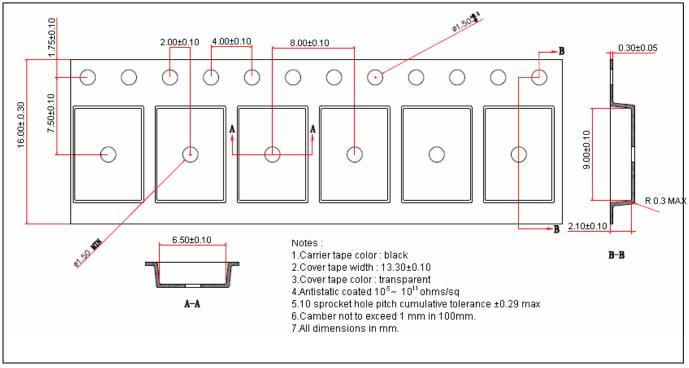


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Reel Dimension



Carrier Tape Dimension

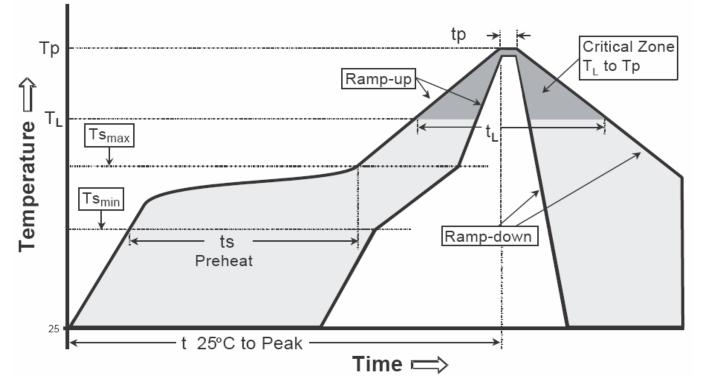




Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

Recommended temperature profile for IR reflow



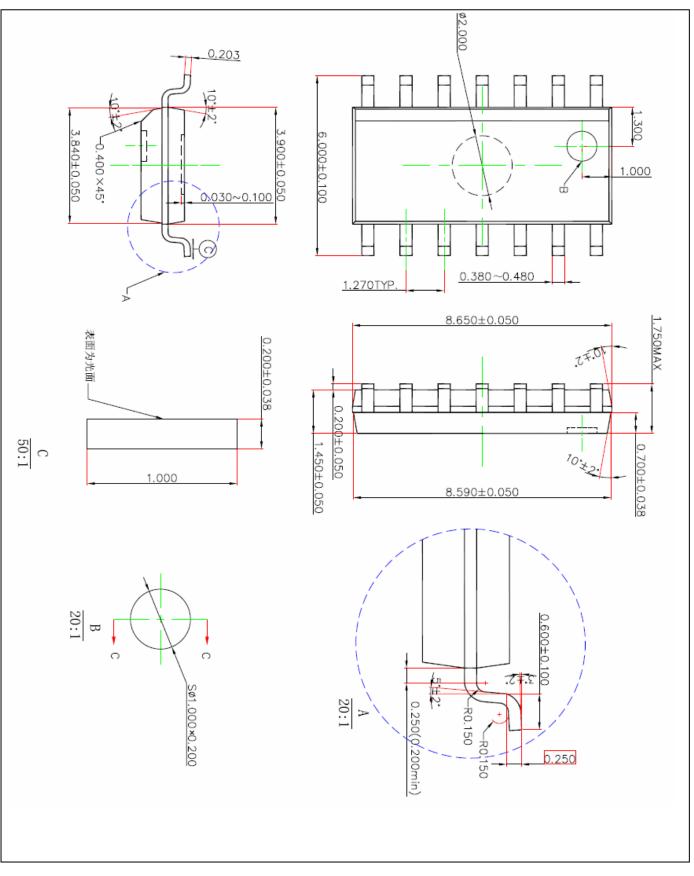
Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(Ts min)	100°C	150°C
-Temperature Max(Ts max)	150°C	200°C
-Time(ts min to ts max)	60-120 seconds	60-180 seconds
Time maintained above:		
–Temperature (TL)	183°C	217°C
– Time (t∟)	60-150 seconds	60-150 seconds
Peak Temperature(TP)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.



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SOP-14 Dimension





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Notes: 1.Controlling dimension: millimeters.

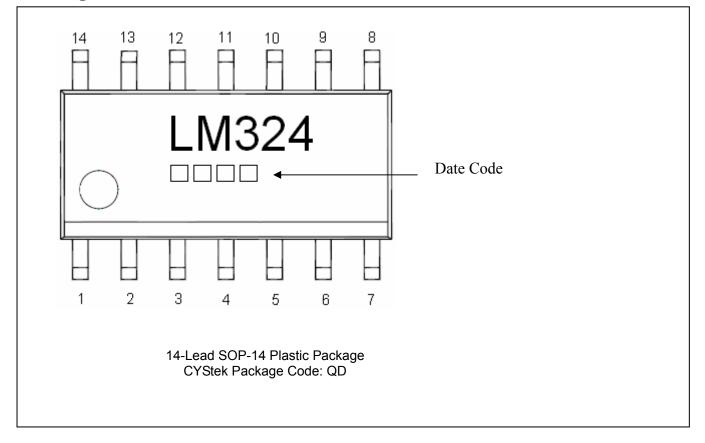
2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material. 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

• Lead: Pure tin plated.

• Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

Marking



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