



## **PWM Control 5A Step-Down Converter**

### ❖ GENERAL DESCRIPTION

AX3116 consists of step-down switching regulator with PWM control. These device include a reference voltage source, oscillation circuit, error amplifier, internal PMOS and etc.

AX3116 provides low-ripple power, high efficiency, and excellent transient characteristics. The PWM control circuit is able to the duty ratio linearly form 0 up to 100%. An enable function, an over current protect function and short circuit protect function are built inside, and when OCP or SCP happens, the operation frequency will be reduced. Also, an internal compensation block is built in to minimum external component count.

With the addition of an internal P-channel Power MOS, a coil, capacitors, and a diode connected externally, these ICs can function as step-down switching regulators. They serve as ideal power supply units for portable devices when coupled with the SOP-8L-EP package, providing such outstanding features as low current consumption. Since this converter can accommodate an input voltage up to 23V, it is also suitable for the operation via an AC adapter.

### ❖ FEATURES

- Input voltage : 4.5V to 23V
- Output voltage : 0.8V to  $V_{CC}$
- Duty ratio : 0% to 100% PWM control
- Oscillation frequency : 330KHz typ.
- Current Limit (CL), Enable function.
- External Soft-Start function.
- Thermal Shutdown function.
- Short Circuit Protect (SCP).
- Built-in internal SW P-channel MOS.
- SOP-8L with Exposed Pad Pb-Free package.



**❖ ABSOLUTE MAXIMUM RATINGS** (at  $T_A=25^{\circ}\text{C}$ )

Characteristics	Symbol	Rating	Unit
VCC Pin Voltage	$V_{CC}$	$V_{SS} - 0.3$ to $V_{SS} + 25$	V
Feedback Pin Voltage	$V_{FB}$	$V_{SS} - 0.3$ to $V_{CC}$	V
ON/OFF Pin Voltage	$V_{EN}$	$V_{SS} - 0.3$ to $V_{CC} + 0.3$	V
Switch Pin Voltage	$V_{SW}$	$V_{SS} - 0.3$ to $V_{CC} + 0.3$	V
Power Dissipation	PD	Internally limited	mW
Storage Temperature Range	$T_{ST}$	-40 to +150	$^{\circ}\text{C}$
Operating Junction Temperature	$T_{OPJ}$	-20 to +125	$^{\circ}\text{C}$
Junction Temperature Range	TJ	-40 to +150	$^{\circ}\text{C}$
Operating Supply Voltage	$V_{OP}$	+4.5 to +23	V
Switching Current	$I_{SW}$	6.5	A
Thermal Resistance from Junction to case	$\theta_{JC}$	15	$^{\circ}\text{C/W}$
Thermal Resistance from Junction to ambient	$\theta_{JA}$	40	$^{\circ}\text{C/W}$

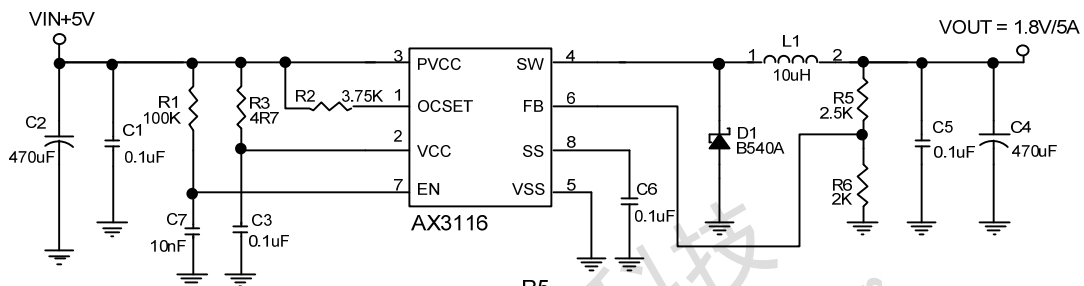
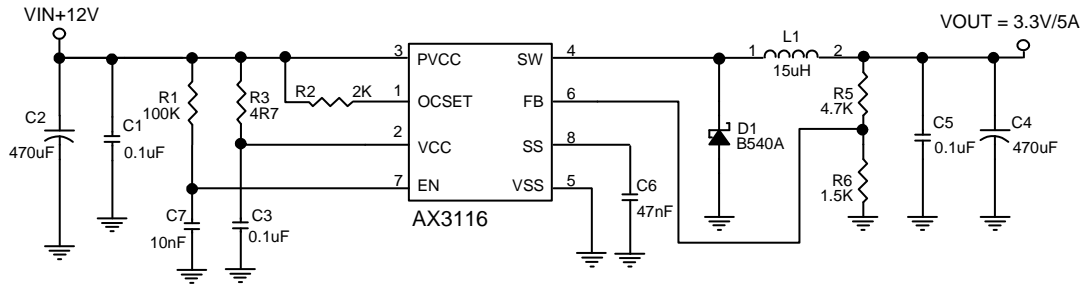
Note :  $\theta_{JA}$  is measured with the PCB copper area(need connect to Exposed pad) of approximately 1 in<sup>2</sup>(Multi-layer).

**❖ ELECTRICAL CHARACTERISTICS**

( $V_{IN} = 12\text{V}$ ,  $T_A=25^{\circ}\text{C}$ , unless otherwise specified)

Characteristics	Symbol	Conditions	Min	Typ	Max	Units	
Feedback Voltage	$V_{FB}$	$I_{OUT}=0.2\text{A}$	0.784	0.800	0.816	V	
Quiescent Current	$I_{CCQ}$	$V_{FB}=1.2\text{V}$ force driver off	-	3	5	mA	
Feedback Bias Current	$I_{FB}$	$I_{OUT}=0.2\text{A}$	-	0.1	0.5	$\mu\text{A}$	
Shutdown Supply Current	$I_{SD}$	$V_{EN}=0\text{V}$	-	2	10	$\mu\text{A}$	
Line Regulation	$\Delta V_{OUT}/V_{OUT}$	$V_{CC}=8\text{V}\sim 23\text{V}$ , $I_{OUT}=0.3\text{A}$	-	1	2	%	
Load Regulation	$\Delta V_{OUT}/V_{OUT}$	$I_{OUT} = 0.1$ to $2.5\text{A}$	-	0.2	0.5	%	
OCSET pin bias current	$I_{OCSET}$		95	110	140	$\mu\text{A}$	
SS pin Current	$I_{SS}$		-	4.0	-	$\mu\text{A}$	
Oscillation Frequency	$F_{OSC}$	SW pin	260	330	400	KHz	
Short Circuit Frequency	$F_{OSC1}$	$V_{FB}=0\text{V}$	-	80	-	KHz	
EN Pin Logic input threshold voltage	$V_{SH}$	High (regulator ON)	2.0	-	-	V	
	$V_{SL}$	Low (regulator OFF)	-	-	0.8		
EN Pin Input Current	$I_{SH}$	$V_{EN}=2.5\text{V}$ (ON)	-	20	-	$\mu\text{A}$	
	$I_{SL}$	$V_{EN}=0.3\text{V}$ (OFF)	-	-10	-	$\mu\text{A}$	
Internal MOSFET $R_{DS(ON)}$	$R_{D_{kgSON}}$	$V_{CC}=5\text{V}$ , $V_{FB}=0\text{V}$	-	70	100	m $\Omega$	
		$V_{CC}=12\text{V}$ , $V_{FB}=0\text{V}$	-	45	70		
Efficiency	EFFI	$V_{OUT} = 5\text{V}$	$I_{OUT} = 3\text{A}$	-	92	-	%
			$I_{OUT} = 5\text{A}$	-	91	-	
Thermal shutdown Temp	$T_{SD}$		-	135	-	$^{\circ}\text{C}$	

### ❖ APPLICATION CIRCUIT



$$V_{OUT} = V_{FB} \times \left(1 + \frac{R_5}{R_6}\right)$$

$$V_{FB} = 0.8V; R_6 \text{ suggest } 0.8K \sim 4k$$

L1 recommend value ( $V_{IN}=12V, I_{OUT}=5A$ )				
$V_{OUT}$	1.8 V	2.5V	3.3V	5V
L1	10uH	10uH	15uH	15uH

### ❖ FUNCTION DESCRIPTIONS

#### PWM Control

The AX3116 consists of DC/DC converters that employ a pulse-width modulation (PWM) system. In converters of the AX3116, the pulse width varies in a range from 0 to 100%, according to the load current. The ripple voltage produced by the switching can easily be removed through a filter because the switching frequency remains constant. Therefore, these converters provide a low-ripple power over broad ranges of input voltage and load current.

#### Setting the Output Voltage

Application circuit item shows the basic application circuit with adjustable output version. The external resistor sets the output voltage according to the following equation:

$$V_{OUT} = 0.8V \times \left(1 + \frac{R5}{R6}\right)$$

Table 1 Resistor select for output voltage setting

V <sub>OUT</sub>	R6	R5
5V	1.3K	6.8K
3.3V	1.5K	4.7K
2.5V	2.2K	4.7K
1.8V	2K	2.5K
1.5V	2.2K	2.0K
1.2V	3K	1.5K
1.0V	3K	0.75K

### RDS (ON) Current Limiting

The current limit threshold is setting by the external resistor (R2) connecting from V<sub>CC</sub> supply to OCSET pin. The internal 110uA sink current crossing the resistor sets the voltage at pin of OCSET. When the PWM voltage is less than the voltage at OCSET, an over-current condition is triggered. Please refer to the formula for setting the current limit value:

$$I_{SW(MIN)} = \frac{I_{OCSET} \times R2 + 0.05}{R_{DS(ON)}}$$

(Normally, The I<sub>SW(MAX)</sub> setting more than I<sub>OUT</sub> 1.0A .)

Example:

$$\begin{aligned} I_{SW} &= (110\mu \times 2K + 0.05) / 45m = 6.67A \quad (V_{IN}=12V) \\ I_{SW} &= (110\mu \times 3.75K + 0.05) / 70m = 6.6A \quad (V_{IN}=5V) \end{aligned} \quad (T_A=25^\circ C)$$

### Inductor Selection

For most designs, the operating inductor range is 10μH to 15μH. The inductor value can be derived from the following equation:

$$L = \frac{V_{OUT} \times (V_{IN} - V_{OUT})}{V_{IN} \times \Delta I_L \times f_{OSC}}$$

Where is inductor Ripple Current. Large value inductors lower ripple current and small value inductors result in high ripple currents. Choose inductor ripple current approximately 15% of the maximum load current 5A,  $\Delta I_L=0.75A$ . The DC current rating of the inductor should be at least equal to the maximum load current plus half the ripple current to prevent core saturation ( $5A+0.38A$ )

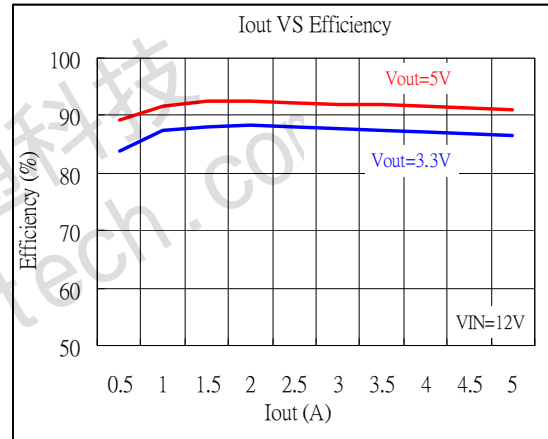
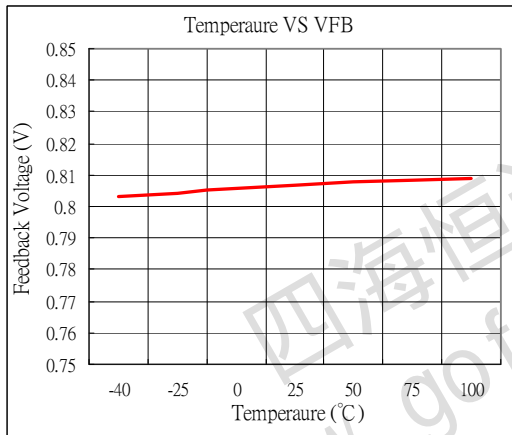
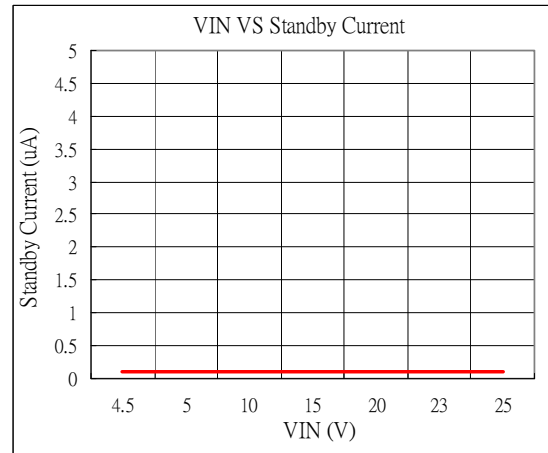
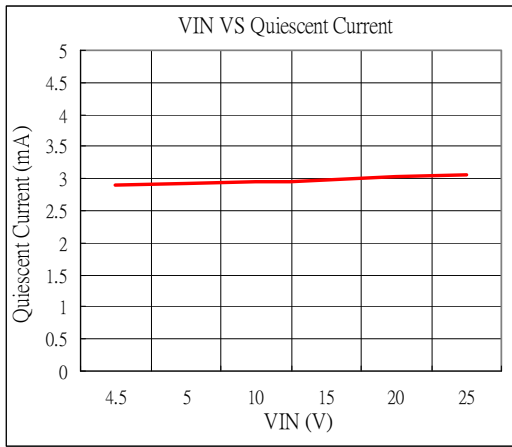
### Input Capacitor

The input current to the step-down converter is discontinuous, and so a capacitor is required to supply the AC current to the step-down converter while maintaining the DC input voltage. A low-ESR capacitor is required to keep the noise at the IC to a minimum. The low-ESR electrolytic capacitor may also suffice. The input capacitor value should be greater than  $470\mu F$ . However since it absorbs the input switching current it requires an adequate ripple current rating. Its RMS current rating should be greater than approximately 1/2 of the DC load current. For insuring stable operation C<sub>IN</sub> should be placed as close to the IC as possible. Alternately a smaller high quality ceramic 0.1uF capacitor may be placed closer to the IC and a larger capacitor placed further away. If using this technique, it is recommended that the larger capacitor be a electrolytic type.

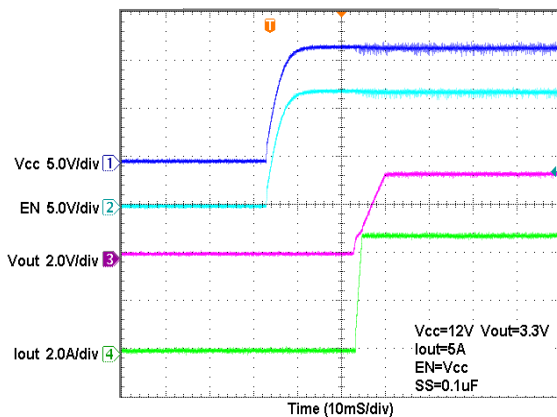
### Output Capacitor

The output capacitor is required to maintain the DC output voltage. Low ESR capacitors are preferred to keep the output voltage ripple low. The characteristics of the output capacitor also affect the stability of the regulation control system. The low-ESR electrolytic capacitors are recommended. The output capacitor value should be greater than  $470\mu F$ .

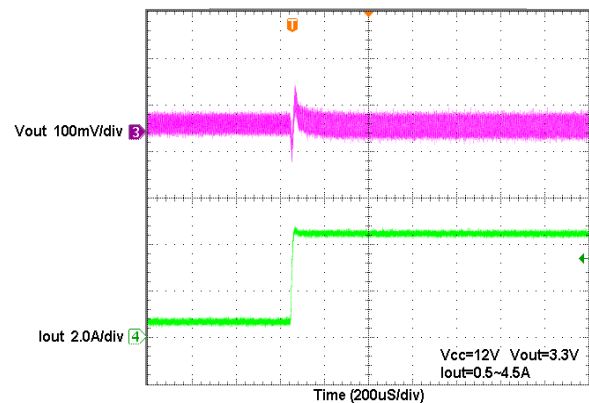
❖ TYPICAL CHARACTERISTICS



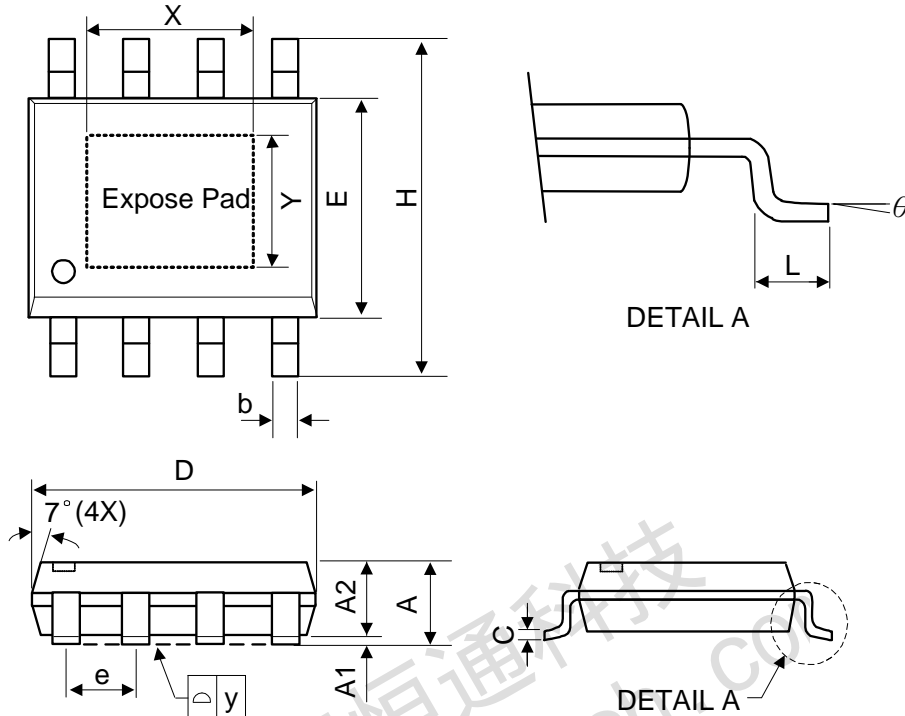
Power-ON



Load-On Transient



❖ PACKAGE OUTLINES



Symbol	Dimensions in Millimeters			Dimensions in Inches		
	Min.	Nom.	Max.	Min.	Nom.	Max.
A	-	-	1.75	-	-	0.069
A1	0	-	0.15	0	-	0.06
A2	1.25	-	-	0.049	-	-
C	0.1	0.2	0.25	0.0075	0.008	0.01
D	4.7	4.9	5.1	0.185	0.193	0.2
E	3.7	3.9	4.1	0.146	0.154	0.161
H	5.8	6	6.2	0.228	0.236	0.244
L	0.4	-	1.27	0.015	-	0.05
b	0.31	0.41	0.51	0.012	0.016	0.02
e	1.27 BSC			0.050 BSC		
y	-	-	0.1	-	-	0.004
X	-	2.34	-	-	0.092	-
Y	-	2.34	-	-	0.092	-
θ	0°	-	8°	0°	-	8°

Mold flash shall not exceed 0.25mm per side  
JEDEC outline: MS-012 BA