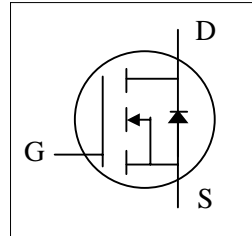
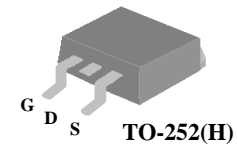




- ▼ Simple Drive Requirement
- ▼ Lower Gate Charge
- ▼ Fast Switching Characteristics
- ▼ RoHS Compliant & Halogen-Free



$BV_{DSS}$	200V
$R_{DS(ON)}$	250m $\Omega$
$I_D$	10A



## Description

Advanced Power MOSFETs from APEC provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-252 package is widely preferred for commercial-industrial surface mount applications and using infrared reflow technique and suited for high current application due to the low connection resistance.

## Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	200	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D@T_C=25^\circ C$	Continuous Drain Current, $V_{GS}$ @ 10V	10	A
$I_D@T_C=100^\circ C$	Continuous Drain Current, $V_{GS}$ @ 10V	6.1	A
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	26	A
$P_D@T_C=25^\circ C$	Total Power Dissipation	62.5	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$

## Thermal Data

Symbol	Parameter	Value	Unit
Rthj-c	Maximum Thermal Resistance, Junction-case	2	$^\circ C/W$
Rthj-a	Maximum Thermal Resistance, Junction-ambient (PCB mount) <sup>3</sup>	62.5	$^\circ C/W$



# AP15T20GH-HF

## Electrical Characteristics @T<sub>j</sub>=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	200	-	-	V
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V, I <sub>D</sub> =5A	-	-	250	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =3A	-	-	260	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	1	-	3	V
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =10V, I <sub>D</sub> =5A	-	20	-	S
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =160V, V <sub>GS</sub> =0V	-	-	25	uA
I <sub>GSS</sub>	Gate-Source Leakage	V <sub>GS</sub> = ±20V, V <sub>DS</sub> =0V	-	-	±100	nA
Q <sub>g</sub>	Total Gate Charge	I <sub>D</sub> =5A	-	45	72	nC
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =160V	-	5.5	-	nC
Q <sub>gd</sub>	Gate-Drain ("Miller") Charge	V <sub>GS</sub> =10V	-	15	-	nC
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DD</sub> =100V	-	10	-	ns
t <sub>r</sub>	Rise Time	I <sub>D</sub> =5A	-	10	-	ns
t <sub>d(off)</sub>	Turn-off Delay Time	R <sub>G</sub> =3.3Ω	-	37	-	ns
t <sub>f</sub>	Fall Time	V <sub>GS</sub> =10V	-	11	-	ns
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V	-	1950	3120	pF
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =25V	-	100	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	f=1.0MHz	-	65	-	pF
R <sub>g</sub>	Gate Resistance	f=1.0MHz	-	1.5	3	Ω

## Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V <sub>SD</sub>	Forward On Voltage <sup>2</sup>	I <sub>S</sub> =5A, V <sub>GS</sub> =0V	-	-	1.3	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>S</sub> =5A, V <sub>GS</sub> =0V,	-	100	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge	di/dt=100A/μs	-	380	-	nC

### Notes:

- 1.Pulse width limited by Max. junction temperature.
- 2.Pulse test
- 3.Surface mounted on 1 in<sup>2</sup> copper pad of FR4 board

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

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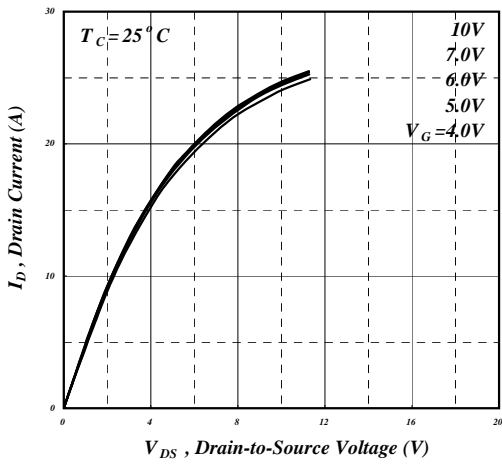


Fig 1. Typical Output Characteristics

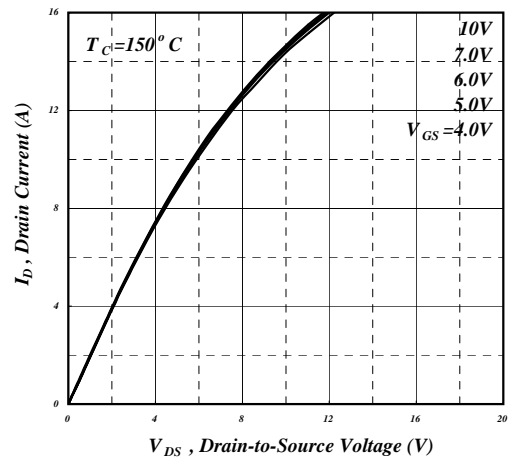


Fig 2. Typical Output Characteristics

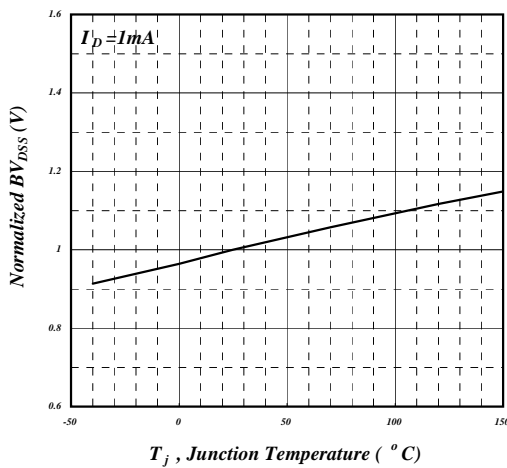


Fig 3. Normalized  $BV_{DSS}$  v.s. Junction Temperature

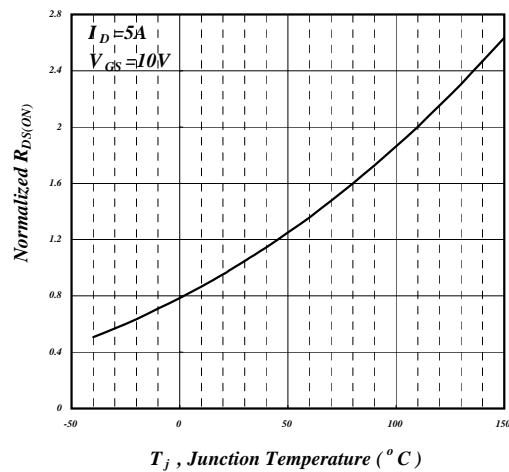


Fig 4. Normalized On-Resistance v.s. Junction Temperature

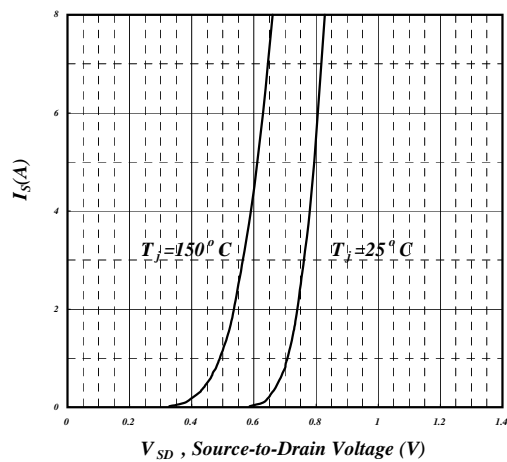


Fig 5. Forward Characteristic of Reverse Diode

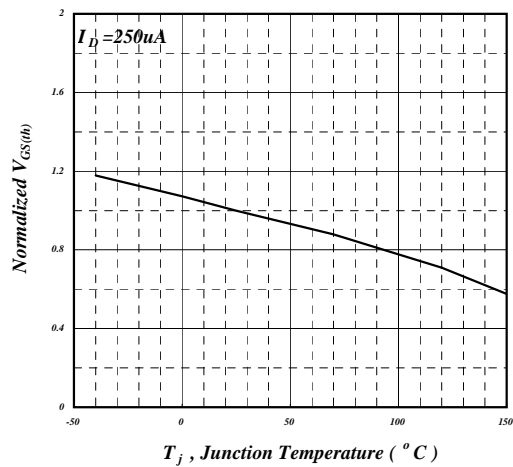


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

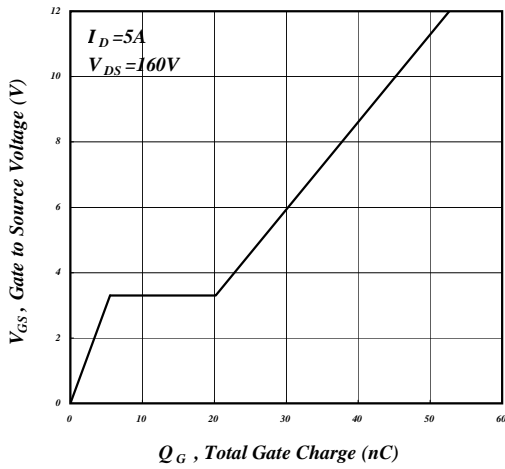


Fig 7. Gate Charge Characteristics

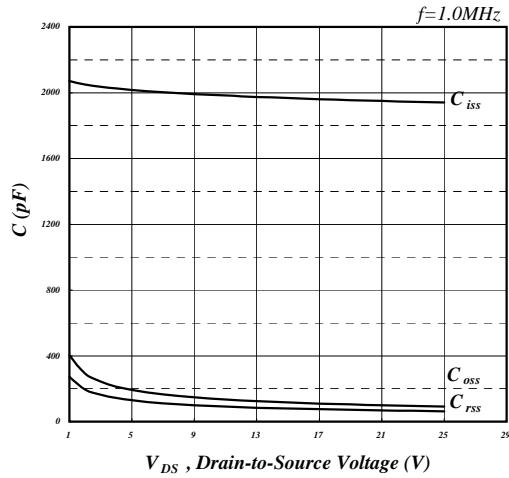


Fig 8. Typical Capacitance Characteristics

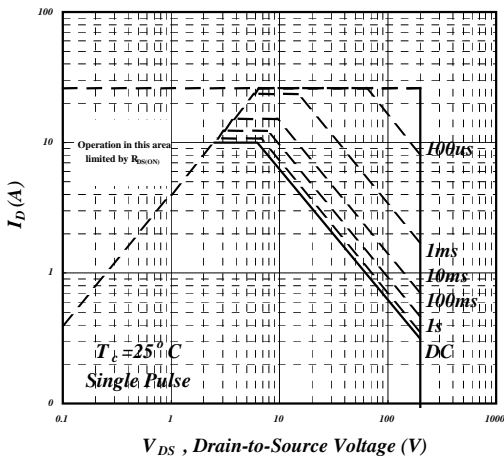


Fig 9. Maximum Safe Operating Area

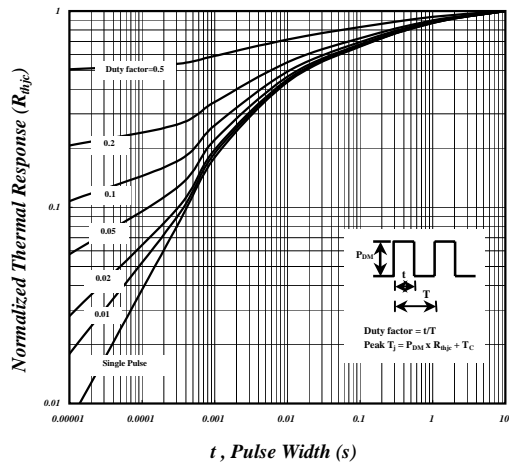


Fig 10. Effective Transient Thermal Impedance

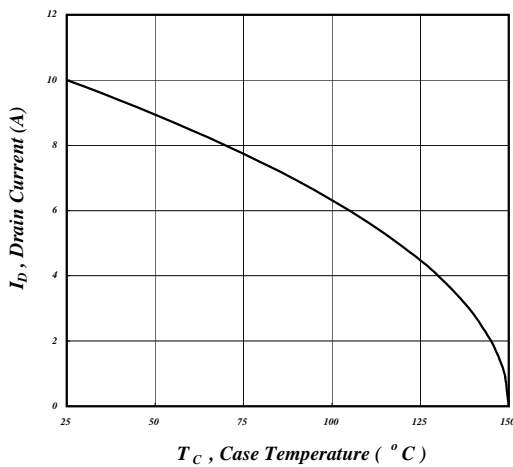


Fig 11. Maximum Continuous Drain Current v.s. Case Temperature

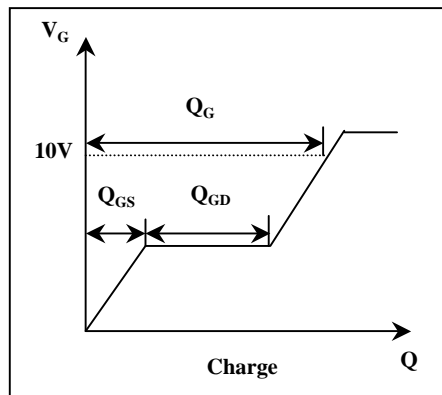


Fig 12. Gate Charge Waveform