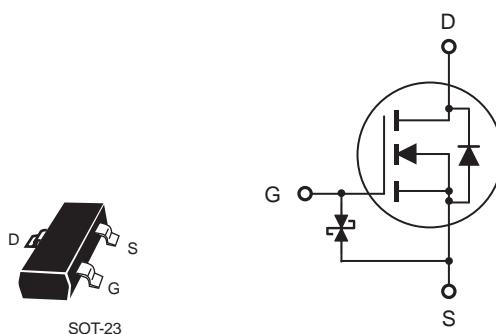


## FEATURES

- 20V, 5.4A,  $R_{DS(ON)} = 27m\Omega$  @  $V_{GS} = 4.5V$ .  
 $R_{DS(ON)} = 36m\Omega$  @  $V_{GS} = 2.5V$ .
- High dense cell design for extremely low  $R_{DS(ON)}$ .
- Rugged and reliable.
- Lead free product is acquired.
- SOT-23 package.



## ABSOLUTE MAXIMUM RATINGS $T_A = 25^\circ C$ unless otherwise noted

Parameter	Symbol	Limit	Units
Drain-Source Voltage	$V_{DS}$	20	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	V
Drain Current-Continuous	$I_D$	5.4	A
Drain Current-Pulsed <sup>a</sup>	$I_{DM}$	22	A
Maximum Power Dissipation	$P_D$	1.25	W
Operating and Store Temperature Range	$T_J, T_{Stg}$	-55 to 150	$^\circ C$

## Thermal Characteristics

Parameter	Symbol	Limit	Units
Thermal Resistance, Junction-to-Ambient <sup>b</sup>	$R_{\theta JA}$	100	$^\circ C/W$

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## Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	20			V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 20\text{V}, V_{\text{GS}} = 0\text{V}$		1		$\mu\text{A}$
Gate Body Leakage Current, Forward	$I_{\text{GSSF}}$	$V_{\text{GS}} = 12\text{V}, V_{\text{DS}} = 0\text{V}$		10		$\mu\text{A}$
Gate Body Leakage Current, Reverse	$I_{\text{GSSR}}$	$V_{\text{GS}} = -12\text{V}, V_{\text{DS}} = 0\text{V}$		-10		$\mu\text{A}$
<b>On Characteristics<sup>c</sup></b>						
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{GS}} = V_{\text{DS}}, I_D = 250\mu\text{A}$	0.5		1.2	V
Static Drain-Source	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 4.5\text{V}, I_D = 5.4\text{A}$		21	27	$\text{m}\Omega$
On-Resistance		$V_{\text{GS}} = 2.5\text{V}, I_D = 4.3\text{A}$		27	36	$\text{m}\Omega$
<b>Dynamic Characteristics<sup>d</sup></b>						
Forward Transconductance	$g_{\text{FS}}$	$V_{\text{DS}} = 10\text{V}, I_D = 5.4\text{A}$		17		S
<b>Switching Characteristics<sup>d</sup></b>						
Turn-On Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 10\text{V}, I_D = 1\text{A}, V_{\text{GS}} = 4.5\text{V}, R_{\text{GEN}} = 6\Omega$		0.35	0.7	$\mu\text{s}$
Turn-On Rise Time	$t_r$			0.87	1.8	$\mu\text{s}$
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$			3.60	7.5	$\mu\text{s}$
Turn-Off Fall Time	$t_f$			2.01	4.3	$\mu\text{s}$
Total Gate Charge	$Q_g$	$V_{\text{DS}} = 10\text{V}, I_D = 5.4\text{A}, V_{\text{GS}} = 4.5\text{V}$		4.3	7.5	nC
Gate-Source Charge	$Q_{\text{gs}}$			1.1		nC
Gate-Drain Charge	$Q_{\text{gd}}$			2.5		nC
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
Drain-Source Diode Forward Current <sup>b</sup>	$I_S$				5.4	A
Drain-Source Diode Forward Voltage <sup>c</sup>	$V_{\text{SD}}$	$V_{\text{GS}} = 0\text{V}, I_S = 1\text{A}$			1.2	V

Notes :

a.Repetitive Rating : Pulse width limited by maximum junction temperature.

b.Surface Mounted on FR4 Board,  $t \leq 10$  sec.

c.Pulse Test : Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$ .

d.Guaranteed by design, not subject to production testing.