MOSFETs Silicon N-channel MOS (U-MOSIV)

TK75J04K3Z

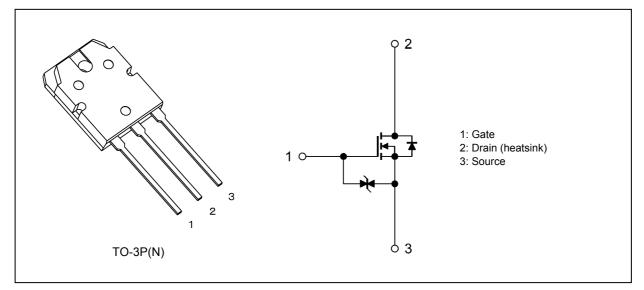
1. Applications

- Motor Drivers
- Switching Voltage Regulators

2. Features

- (1) Low drain-source on-resistance: $R_{DS(ON)} = 2.2 \text{ m}\Omega \text{ (typ.)} (V_{GS} = 10 \text{ V})$
- (2) Low leakage current: $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 40 \ V)$
- (3) Enhancement mode: V_{th} = 3.0 to 4.0 V (V_{DS} = 10 V, I_D = 1 mA)

3. Packaging and Internal Circuit



4. Absolute Maximum Ratings (Note) ($T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics			ymbol	Rating	Unit
Drain-source voltage		,	V _{DSS}	40	V
Gate-source voltage		Ņ	V _{GSS}	±20	
Drain current (DC)	(Note	1)	I _D	75	А
Drain current (pulsed)	(Note	1)	I _{DP}	300	
Power dissipation	(T _c = 25°C)		PD	150	W
Single-pulse avalanche energy	(Note	2)	E _{AS}	443	mJ
Avalanche current			I _{AR}	75	А
Channel temperature	(Note	3)	T _{ch}	175	°C
Storage temperature	(Note	3)	T _{stg}	-55 to 175	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

5. Thermal Characteristics

Characteristics	Symbol	Max	Unit
Channel-to-case thermal resistance	R _{th(ch-c)}	1.0	°C/W
Channel-to-ambient thermal resistance	R _{th(ch-a)}	50	

Note 1: Ensure that the channel temperature does not exceed 175°C.

Note 2: V_DD = 25 V, T_ch = 25°C (initial), L = 82 $\mu H,\,R_G$ = 25 $\Omega,\,I_{AR}$ = 75 A

Note 3: The definitions of the absolute maximum channel and storage temperatures are based on AEC-Q101.

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

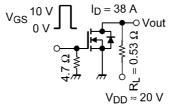
6. Electrical Characteristics

6.1. Static Characteristics (Ta = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I _{GSS}	V_{GS} = ±16 V, V_{DS} = 0 V		_	±10	μA
Drain cut-off current	I _{DSS}	V _{DS} = 40 V, V _{GS} = 0 V	_		10	
Drain-source breakdown voltage	V _{(BR)DSS}	I _D = 10 mA, V _{GS} = 0 V	40		_	V
	V _{(BR)DSX}	I _D = 10 mA, V _{GS} = -20 V	20	_	_	
Gate threshold voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	3.0	_	4.0	
Drain-source on-resistance	R _{DS(ON)}	V _{GS} = 10 V, I _D = 38 A	_	2.2	3.0	mΩ

6.2. Dynamic Characteristics ($T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	8450	—	pF
Reverse transfer capacitance	C _{rss}		_	1480	_	
Output capacitance	C _{oss}		_	2060	_	
Switching time (rise time)	t _r	See Figure 6.2.1.	_	35	—	ns
Switching time (turn-on time)	t _{on}		_	65	—	
Switching time (fall time)	t _f		_	50	_	
Switching time (turn-off time)	t _{off}		-	150	_	



Duty \leq 1%, t_w = 10 µs

Fig. 6.2.1 Switching Time Test Circuit

6.3. Gate Charge Characteristics ($T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	$V_{DD}\approx 32 \text{ V}, \text{ V}_{GS} \text{ = } 10 \text{ V}, \text{ I}_{D} \text{ = } 70 \text{ A}$	_	190	—	nC
Gate-source charge	Q _{gs}		_	108	_	
Gate-drain charge	Q _{gd}		_	82	_	

6.4. Source-Drain Characteristics ($T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (DC)	(Note 4)	I _{DR}	—	_	—	75	А
Reverse drain current (pulsed)	(Note 4)	I _{DRP}		_	_	300	
Diode forward voltage		V _{DSF}	I _{DR} = 75 A, V _{GS} = 0 V	_	_	-1.2	V
Reverse recovery time		t _{rr}	I _{DR} = 75 A, V _{GS} = 0 V		65	_	ns
Reverse recovery charge		Q _{rr}	-dI _{DR} /dt = 50 A/μs	_	46	—	nC

Note 4: Ensure that the channel temperature does not exceed 175°C.

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TOSHIBA 7. Marking (Note)

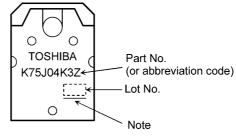


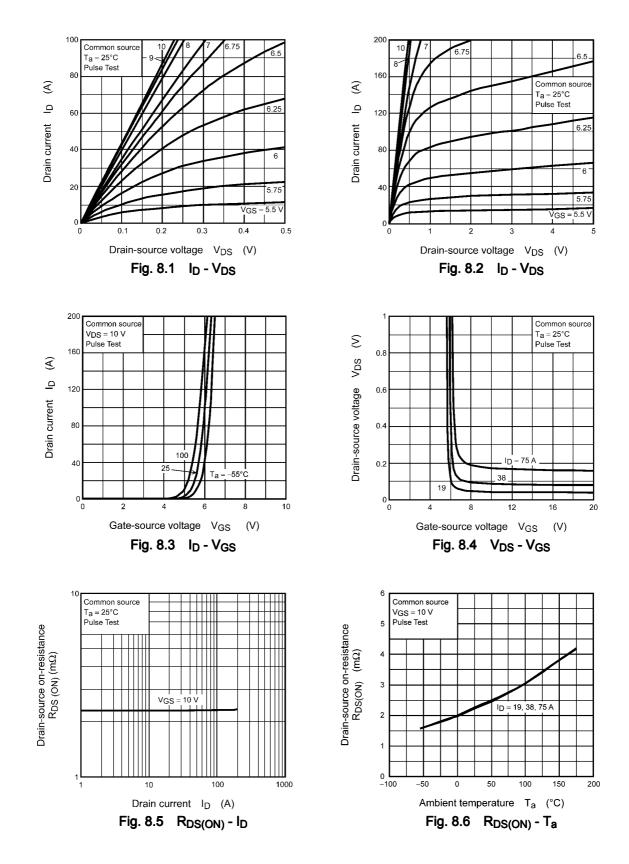
Fig. 7.1 Marking

 Note:
 A line under a Lot No. identifies the indication of product Labels.

 Not underlined: [[Pb]]/INCLUDES > MCV
 Underlined: [[C]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

 Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.
 The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

8. Characteristics Curves (Note)



0 L 0

100

Case temperature T_c (°C) Fig. 8.11 P_D - T_c (Guaranteed Maximum)

150

200

50

100

16

12

8

4

0

300

S

Gate-source voltage VGS

10

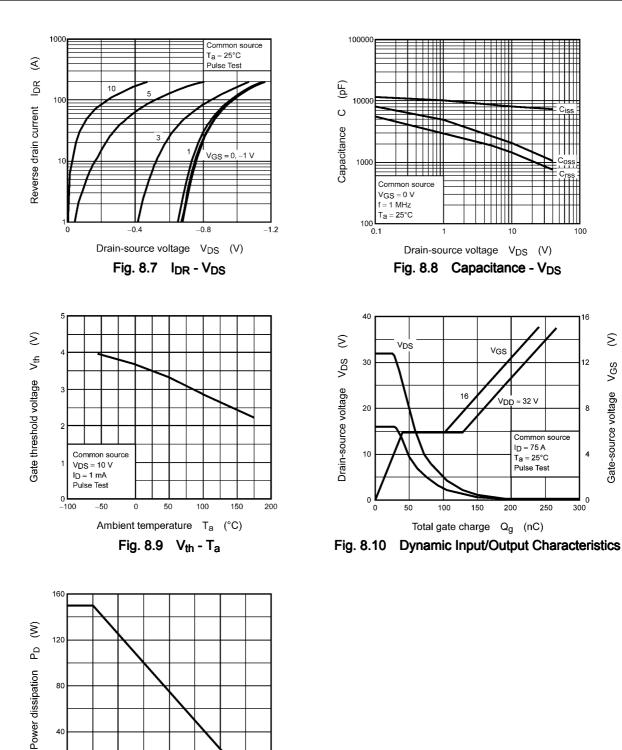
10

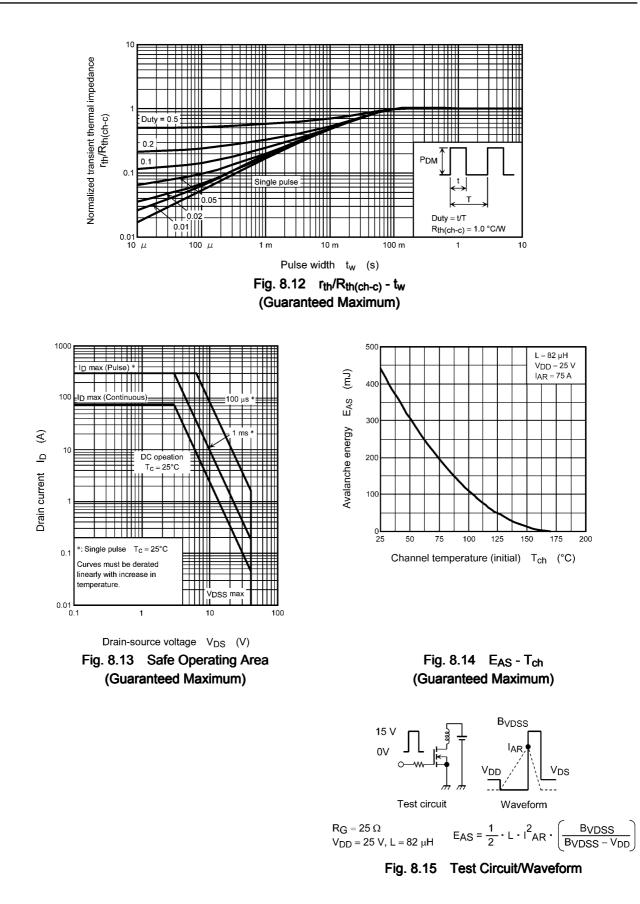
VDD ≈ 32 V

200

Common source $I_D = 75 \text{ A}$ $T_a = 25^{\circ}C$ Pulse Test

250



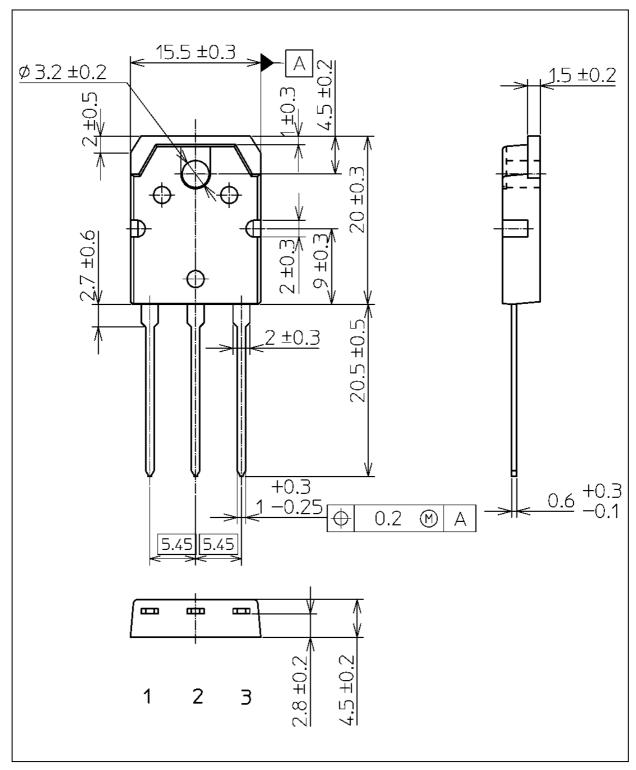


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

Package Dimensions

TK75J04K3Z

Unit: mm



Weight: 4.6 g (typ.)

Package Name(s)	
JEITA: SC-65	
TOSHIBA: 2-16C1S	
Nickname: TO-3P(N)	

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