MOSFETs Silicon N-channel MOS (U-MOSIV)

# TK75J04K3Z

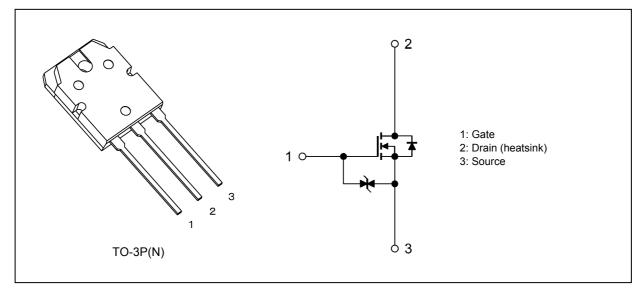
### 1. Applications

- Motor Drivers
- Switching Voltage Regulators

#### 2. Features

- (1) Low drain-source on-resistance:  $R_{DS(ON)} = 2.2 \text{ m}\Omega \text{ (typ.)} (V_{GS} = 10 \text{ V})$
- (2) Low leakage current:  $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 40 \ V)$
- (3) Enhancement mode:  $V_{th}$  = 3.0 to 4.0 V ( $V_{DS}$  = 10 V,  $I_D$  = 1 mA)

### 3. Packaging and Internal Circuit



### 4. Absolute Maximum Ratings (Note) ( $T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics			ymbol	Rating	Unit
Drain-source voltage		,	V <sub>DSS</sub>	40	V
Gate-source voltage		Ņ	V <sub>GSS</sub>	±20	
Drain current (DC)	(Note	1)	I <sub>D</sub>	75	А
Drain current (pulsed)	(Note	1)	I <sub>DP</sub>	300	
Power dissipation	(T <sub>c</sub> = 25°C)		PD	150	W
Single-pulse avalanche energy	(Note	2)	E <sub>AS</sub>	443	mJ
Avalanche current			I <sub>AR</sub>	75	А
Channel temperature	(Note	3)	T <sub>ch</sub>	175	°C
Storage temperature	(Note	3)	T <sub>stg</sub>	-55 to 175	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### 5. Thermal Characteristics

Characteristics	Symbol	Max	Unit
Channel-to-case thermal resistance	R <sub>th(ch-c)</sub>	1.0	°C/W
Channel-to-ambient thermal resistance	R <sub>th(ch-a)</sub>	50	

Note 1: Ensure that the channel temperature does not exceed 175°C.

Note 2: V\_DD = 25 V, T\_ch = 25°C (initial), L = 82  $\mu H,\,R_G$  = 25  $\Omega,\,I_{AR}$  = 75 A

Note 3: The definitions of the absolute maximum channel and storage temperatures are based on AEC-Q101.

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

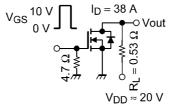
#### 6. Electrical Characteristics

#### 6.1. Static Characteristics (Ta = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I <sub>GSS</sub>	$V_{GS}$ = ±16 V, $V_{DS}$ = 0 V		_	±10	μA
Drain cut-off current	I <sub>DSS</sub>	V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0 V	_		10	
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	40		_	V
	V <sub>(BR)DSX</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = -20 V	20	_	_	
Gate threshold voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	3.0	_	4.0	
Drain-source on-resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 38 A	_	2.2	3.0	mΩ

## 6.2. Dynamic Characteristics ( $T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	8450	—	pF
Reverse transfer capacitance	C <sub>rss</sub>		_	1480	_	
Output capacitance	C <sub>oss</sub>		_	2060	_	
Switching time (rise time)	t <sub>r</sub>	See Figure 6.2.1.	_	35	—	ns
Switching time (turn-on time)	t <sub>on</sub>		_	65	—	
Switching time (fall time)	t <sub>f</sub>		_	50	_	
Switching time (turn-off time)	t <sub>off</sub>		-	150	_	



Duty  $\leq$  1%, t<sub>w</sub> = 10 µs

Fig. 6.2.1 Switching Time Test Circuit

## 6.3. Gate Charge Characteristics ( $T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	$V_{DD}\approx 32 \text{ V}, \text{ V}_{GS} \text{ = } 10 \text{ V}, \text{ I}_{D} \text{ = } 70 \text{ A}$	_	190	—	nC
Gate-source charge	Q <sub>gs</sub>		_	108	_	
Gate-drain charge	Q <sub>gd</sub>		_	82	_	

## 6.4. Source-Drain Characteristics ( $T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (DC)	(Note 4)	I <sub>DR</sub>	—	_	—	75	А
Reverse drain current (pulsed)	(Note 4)	I <sub>DRP</sub>		_	_	300	
Diode forward voltage		V <sub>DSF</sub>	I <sub>DR</sub> = 75 A, V <sub>GS</sub> = 0 V	_	_	-1.2	V
Reverse recovery time		t <sub>rr</sub>	I <sub>DR</sub> = 75 A, V <sub>GS</sub> = 0 V		65	_	ns
Reverse recovery charge		Q <sub>rr</sub>	-dI <sub>DR</sub> /dt = 50 A/μs	_	46	—	nC

Note 4: Ensure that the channel temperature does not exceed 175°C.

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## TOSHIBA 7. Marking (Note)

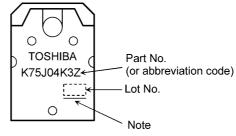


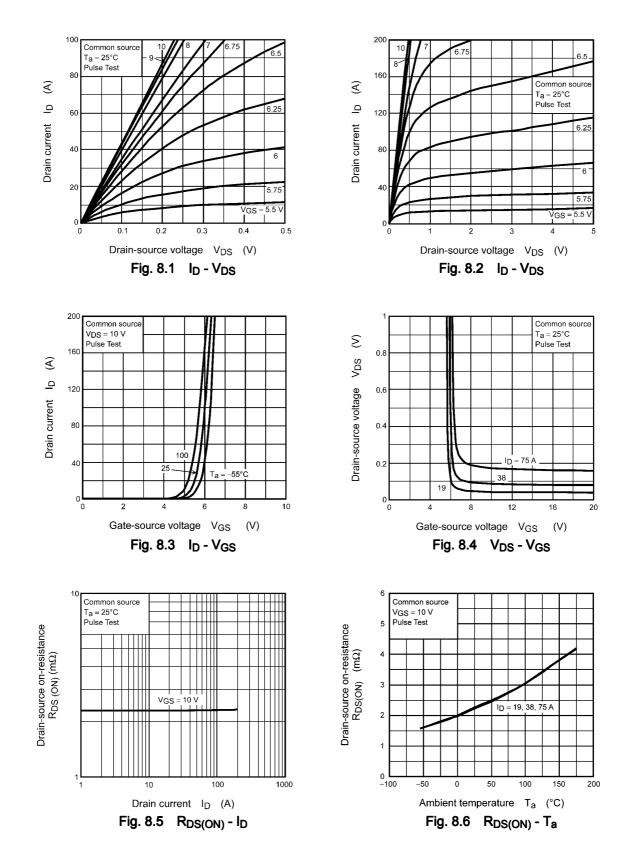
Fig. 7.1 Marking

 Note:
 A line under a Lot No. identifies the indication of product Labels.

 Not underlined: [[Pb]]/INCLUDES > MCV
 Underlined: [[C]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

 Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.
 The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

### 8. Characteristics Curves (Note)



0 L 0

100

Case temperature  $T_c$  (°C) Fig. 8.11 P<sub>D</sub> - T<sub>c</sub> (Guaranteed Maximum)

150

200

50

100

16

12

8

4

0

300

S

Gate-source voltage VGS

10

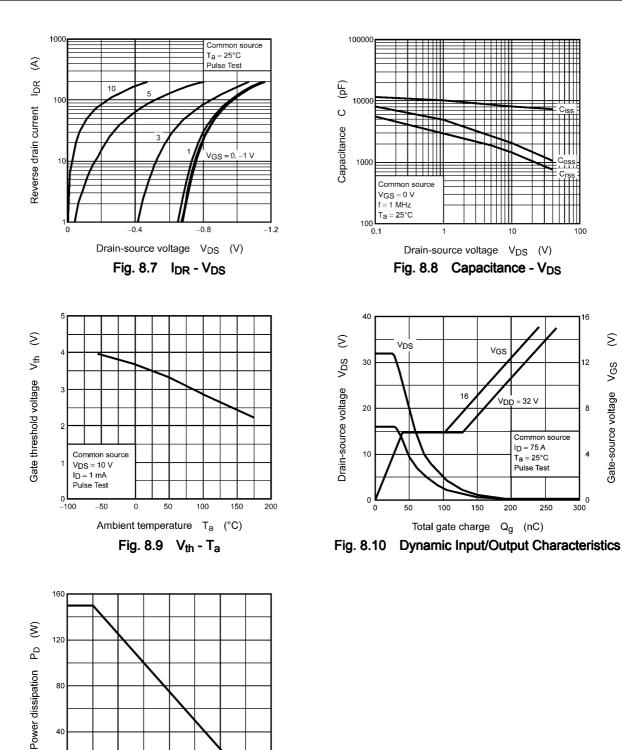
10

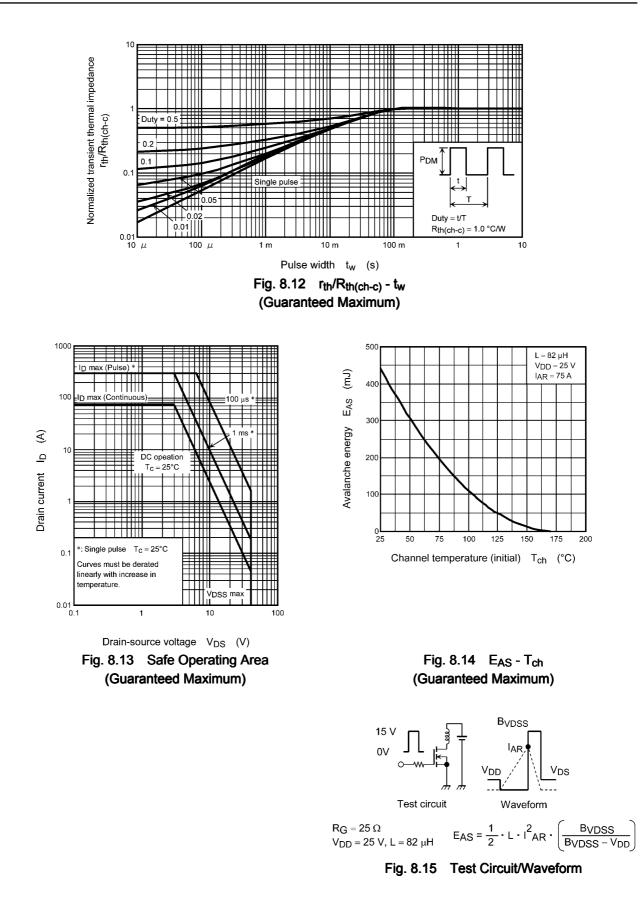
VDD ≈ 32 V

200

Common source  $I_D = 75 \text{ A}$  $T_a = 25^{\circ}C$ Pulse Test

250



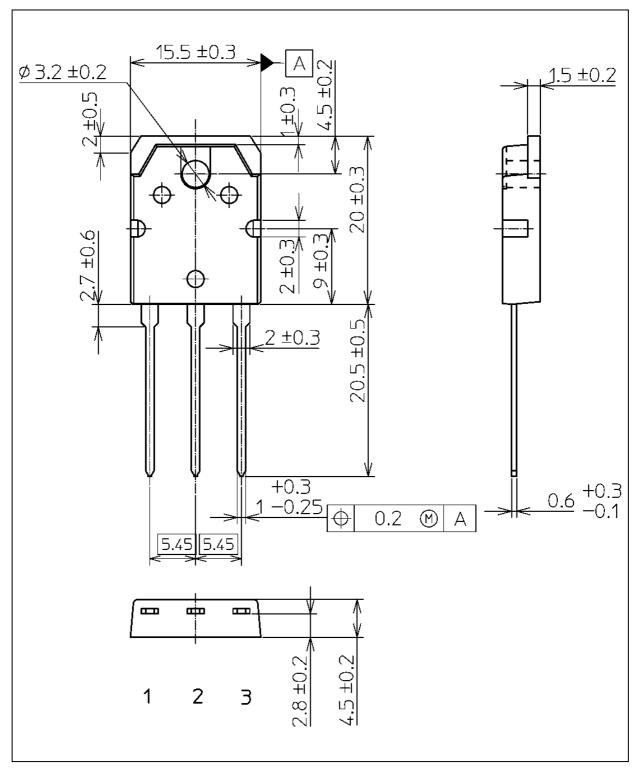


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

### Package Dimensions

TK75J04K3Z

Unit: mm



Weight: 4.6 g (typ.)

Package Name(s)	
JEITA: SC-65	
TOSHIBA: 2-16C1S	
Nickname: TO-3P(N)	

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