

# GaAs INTEGRATED CIRCUIT $\mu PG2130TB$

# L-BAND PA DRIVER AMPLIFIER

#### DESCRIPTION

The  $\mu$ PG2130TB is GaAs MMIC for PA driver amplifier which were developed for mobile phone and another Lband application. The device can operate with 3.0 V TYP., having the high gain and low distortion.

#### **FEATURES**

Operation frequency	: f <sub>opt</sub> = 1 429 to 1 453 MHz (1 441 MHz TYP.)
Supply voltage	: VDD1 = 2.7 to 3.3 V (3.0 V TYP.)
	: VDD2 = 3.0 to 4.2 V (3.5 V TYP.)
Circuit current	: Idd = 25 mA TYP. @ Vdd1 = $3.0 \text{ V}$ , Vdd2 = $3.2 \text{ V}$ , Vagc = $2.5 \text{ V}$ , Pout = +10 dBm
High power gain	: GP = 30 dB TYP. @ Vdd1 = 3.0 V, Vdd2 = 3.2 V, Vagc = 2.5 V, Pin = $-20 \text{ dBm}$
Gain control range	: GCR = 42 dB TYP. @ Vdd1 = $3.0 \text{ V}$ , Vdd2 = $3.2 \text{ V}$ , Vagc = $0.5 \text{ to } 2.5 \text{ V}$ ,
	$P_{in} = -20 \text{ dBm}$
Low distortion	: Padj1 = $-60 \text{ dBc TYP}$ . @ VDD1 = 3.0 V, VDD2 = 3.2 V, VAGC = 2.5 V, Pout = +10 dBm,
	$\Delta f = \pm 50 \text{ kHz}, 21 \text{ kHz}$ Bandwidth.
High-density surface mounting	: 6-pin super minimold package (2.0 $ imes$ 1.25 $ imes$ 0.9 mm)

#### APPLICATION

• Digital Cellular: PDC 1.5 GHz etc.

#### ORDERING INFORMATION

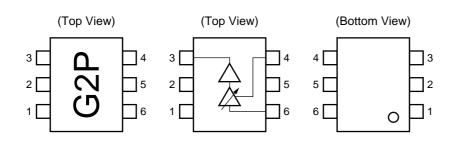
Part Number	Package	Marking	Supplying Form
μPG2130TB-E3	6-pin super minimold	G2P	<ul> <li>Embossed tape 8 mm wide</li> <li>Pin 1, 2, 3 face the perforation side of the tape</li> <li>Qty 3 kpcs/reel</li> </ul>

**Remark**To order evaluation samples, contact your nearby sales office.Part number for sample order:  $\mu$ PG2130TB

#### Caution Electro-static sensitive devices

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## PIN CONNECTIONS, MARKING AND INTERNAL BLOCK DIAGRAM



Pin No.	Pin Name
1	V <sub>DD1</sub>
2	GND
3	OUTPUT/VDD2
4	Vagc
5	GND
6	INPUT

#### ABSOLUTE MAXIMUM RATINGS (Unless otherwise specified, T<sub>A</sub> = +25°C)

Parameter	Symbol	Ratings	Unit
Supply Voltage1, 2	VDD1, 2	6.0	V
AGC Control Voltage	VAGC	6.0	V
Input Power	Pin	-8	dBm
Power Dissipation	PD	140 <sup>Note</sup>	mW
Operating Ambient Temperature	TA	-30 to +90	°C
Storage Temperature	Tstg	-35 to +150	°C

**Note** Mounted on double copper-clad  $50 \times 50 \times 1.6$  mm epoxy glass PWB, T<sub>A</sub> = +85°C

# **RECOMMENDED OPERATING RENGE (TA = +25°C)**

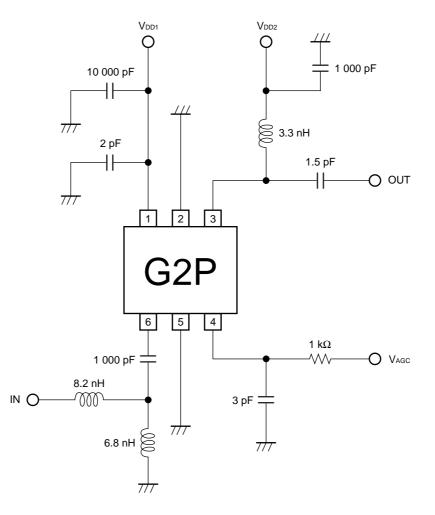
Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage1	V <sub>DD1</sub>	2.7	3.0	3.3	V
Supply Voltage2	Vdd2	3.0	3.5	4.2	V
AGC Control Voltage	VAGC	0	-	2.5	V
Input Power	Pin	-	-18	-10	dBm

## ELECTRICAL CHARACTERISTICS

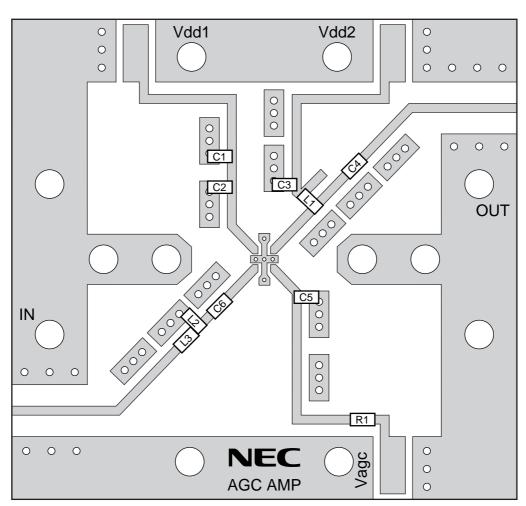
(Unless otherwise specified, T<sub>A</sub> = +25°C, V<sub>DD1</sub> = 3.0 V, V<sub>DD2</sub> = 3.2 V,  $\pi$ /4DQPSK modulated signal input, External input and output matching)

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Operating Frequency	fopt		1 429	_	1 453	MHz
Circuit Current	ldd	Pout = +10 dBm, VAGC = 2.5 V	-	25	35	mA
Power Gain	G₽	$P_{in} = -20 \text{ dBm}, \text{ V}_{AGC} = 2.5 \text{ V}$	28	30	_	dB
Adjacent Channel Power Leakage 1	Padj1	$P_{out} = +10 \text{ dBm}, V_{AGC} = 2.5 \text{ V}$ $\Delta f = \pm 50 \text{ kHz}, 21 \text{ kHz Bandwidth}$	-	-60	-55	dBc
Adjacent Channel Power Leakage 2	Padj2	$P_{out} = +10 \text{ dBm}, V_{AGC} = 2.5 \text{ V}$ $\Delta f = \pm 100 \text{ kHz}, 21 \text{ kHz} Bandwidth$	-	-70	-65	dBc
Gain Control Range	GCR	$P_{in} = -20 \text{ dBm}, \text{ V}_{AGC} = 0.5 \text{ to } 2.5 \text{ V}$	37	42	-	dB
Gain Control Current	lagc	V <sub>AGC</sub> = 0.5 to 2.5 V	_	200	500	μA

#### EVALUATION CIRCUIT (VDD1 = 3.0 V, VDD2 = 3.2 V, f = 1 441 MHz)



The application circuits and their parameters are for reference only and are not intended for use in actual design-ins.



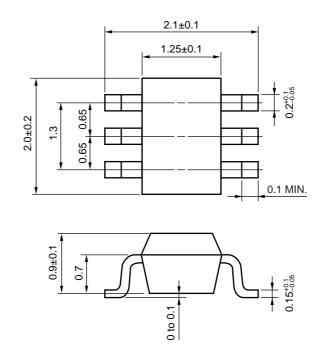
## ILLUSTRATION OF THE TEST CIRCUIT ASSEMBLED ON EVALUATION BOARD

#### USING THE NEC EVALUATION BOARD

Symbol	Values	Part Number	Maker
L1	3.3 nH	TFL0816-3N3	Susumu
L2	6.8 nH	TFL0816-6N8	Susumu
L3	8.2 nH	TFL0816-8N2	Susumu
C1	10 000 pF	GRM39CH103J50	muRata
C3, C6	1 000 pF	GRM39CH102J50	muRata
C2	2 pF	GRM39CK020C50	muRata
C4	1.5 pF	GRM39CJ015C50	muRata
C5	3 pF	GRM39CJ030C50	muRata
R1	1 kΩ	RR0816	Susumu

# PACKAGE DIMENSIONS

# 6-PIN SUPER MINIMOLD (UNIT: mm)



#### **RECOMMENDED SOLDERING CONDITIONS**

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions		Condition Symbol
Infrared Reflow	Peak temperature (package surface temperature) Time at peak temperature Time at temperature of 220°C or higher Preheating time at 120 to 180°C Maximum number of reflow processes Maximum chlorine content of rosin flux (% mass)	: 260°C or below : 10 seconds or less : 60 seconds or less : 120±30 seconds : 3 times : 0.2%(Wt.) or below	IR260
VPS	Peak temperature (package surface temperature) Time at temperature of 200°C or higher Preheating time at 120 to 150°C Maximum number of reflow processes Maximum chlorine content of rosin flux (% mass)	: 215°C or below : 25 to 40 seconds : 30 to 60 seconds : 3 times : 0.2%(Wt.) or below	VP215
Wave Soldering	Peak temperature (molten solder temperature) Time at peak temperature Preheating temperature (package surface temperature) Maximum number of flow processes Maximum chlorine content of rosin flux (% mass)	: 260°C or below : 10 seconds or less : 120°C or below : 1 time : 0.2%(Wt.) or below	WS260
Partial Heating	Peak temperature (pin temperature) Soldering time (per side of device) Maximum chlorine content of rosin flux (% mass)	: 350°C or below : 3 seconds or less : 0.2%(Wt.) or below	HS350

Caution Do not use different soldering methods together (except for partial heating).

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#### SAFETY INFORMATION ON THIS PRODUCT

Caution GaAs Products	The product contains gallium arsenide, GaAs. GaAs vapor and powder are hazardous to human health if inhaled or ingested.
	Do not destroy or burn the product.
	Do not cut or cleave off any part of the product.
	Do not crush or chemically dissolve the product.
	Do not put the product in the mouth.
	Follow related laws and ordinances for disposal. The product should be excluded from general industrial waste or household garbage.

#### ▶ Business issue

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► Technical issue

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