AN7420

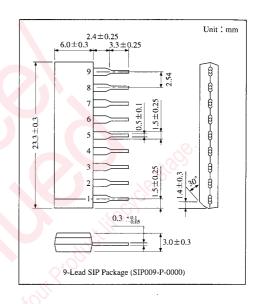
FM Stereo Multiplex Demodulator

Overview

The AN7420 is an integrated circuit designed for PLL-applied low voltage FM stereo multiplex demodulator.

Features

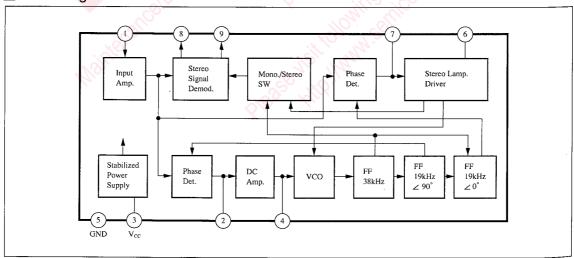
- Fewer external components, simple alignment
- \bullet Low and wide voltage operation : $V_{\text{CC}}{=}3.5V{\sim}\,12V$
- Provided with forced monaural and VCO stop circuits (Pin⑦)
- No gain loss in multiplex stage with 0 dB voltage gain



Pin Descriptions

Pin No.	Pin Name
1	Composite Sig. Input
2	PLL Low-pass Filter
3	V _{cc}
4	VCO RC Time Constant
5	GND
6	Stereo Indicator and VCO Freq. Monitor
7	Stereo Signal Det. Low-pass Filter
8	L Ch. Signal Output
9	R Ch. Signal Output

■ Block Diagram



■ Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Rating	Unit	
Supply Voltage	V _{CC}	12	V	
Power Dissipation	P _D	500 .	mW	
Operating Ambient Temperature	$T_{ m opr}$	<u>−25</u> ~+75	r	
Storage Temperature	$T_{ m stg}$	$-40 \sim +150$	°C	

■ Electrical Characteristics (V_{CC} =8V, Ta=25 $^{\circ}$ C)

Parameter	Symbol	Condition	min.	typ.	max.	Unit
Total Circuit Current	I _{tot}	Without input signal		11	18	mA
Separation	Sep.	$V_{(L+R)} = 180 \text{mVrms},$	36	50		dB
Total Harmonic Distortion (L+R)	THD	$V_p = 20 \text{mVrms}, f_m = 1 \text{kHz}$		0.08	0.3	%
Output Voltage (Mono.)	Vo	$V_0 V_i = 200 \text{mVrms}$		200	250	mVrms
Channel Balance	СВ	$f_m = 1kHz$	<u> </u>	. 0	1.5	dB
Stereo Lamp ON Voltage	tereo Lamp ON Voltage V _{P(ON)} Pilot signal 19kHz			10,0	15	mVrms
Stereo Lamp OFF Voltage	V _{P (OFF)}	1 not signal 17kHz	2	6		mVrms



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