

FEATURES

- ROM Capacity
 - Program ROM $8K \times 23\text{-bits}$
 - Character ROM $5 \times 9 \times 128\text{-bits}$
 - Constant ROM $208 \times 4\text{-bits}$
- RAM Capacity
 - Working RAM $256 \times 4\text{-bits}$
 - Display RAM $60 \times 9\text{-bits}$
 - Data RAM $8K \times 8\text{-bits}$
- Memory Expansion (External) $8K \times 8\text{-bits}$
- LCD Display 60 segment \times 9 common
- I/O Ports
 - Parallel I/O 8-bits
 - Input 1-bit
 - Output 2-bits
 - Buzzer Output 1-bit (4 kHz)
 - Key Input 6-bits

- Standby Release 2 events
 - 2 Hz Signal
 - Key Input
- Built-in Oscillator
 - System Clock (Built-in CR Oscillator) 250 kHz
 - Timer (Built-in CR Oscillator) 32.8 kHz
 - Timer (External Crystal) 32.768 kHz
- Instruction Cycle Time 12 μs
- Operating Temperature -10°C to $+60^{\circ}\text{C}$
- Supply Voltage 2.5 V to 3.4 V
- 100-pin QFP Package (QFP100-P-1420)

DESCRIPTION

The SM3512 is a CMOS 4-bit single-chip microcomputer for data bank use incorporating data memory RAM, LCD driver key/switch input circuit, 8-bit parallel I/O port, two output ports, external memory control circuit, and buzzer output circuit.

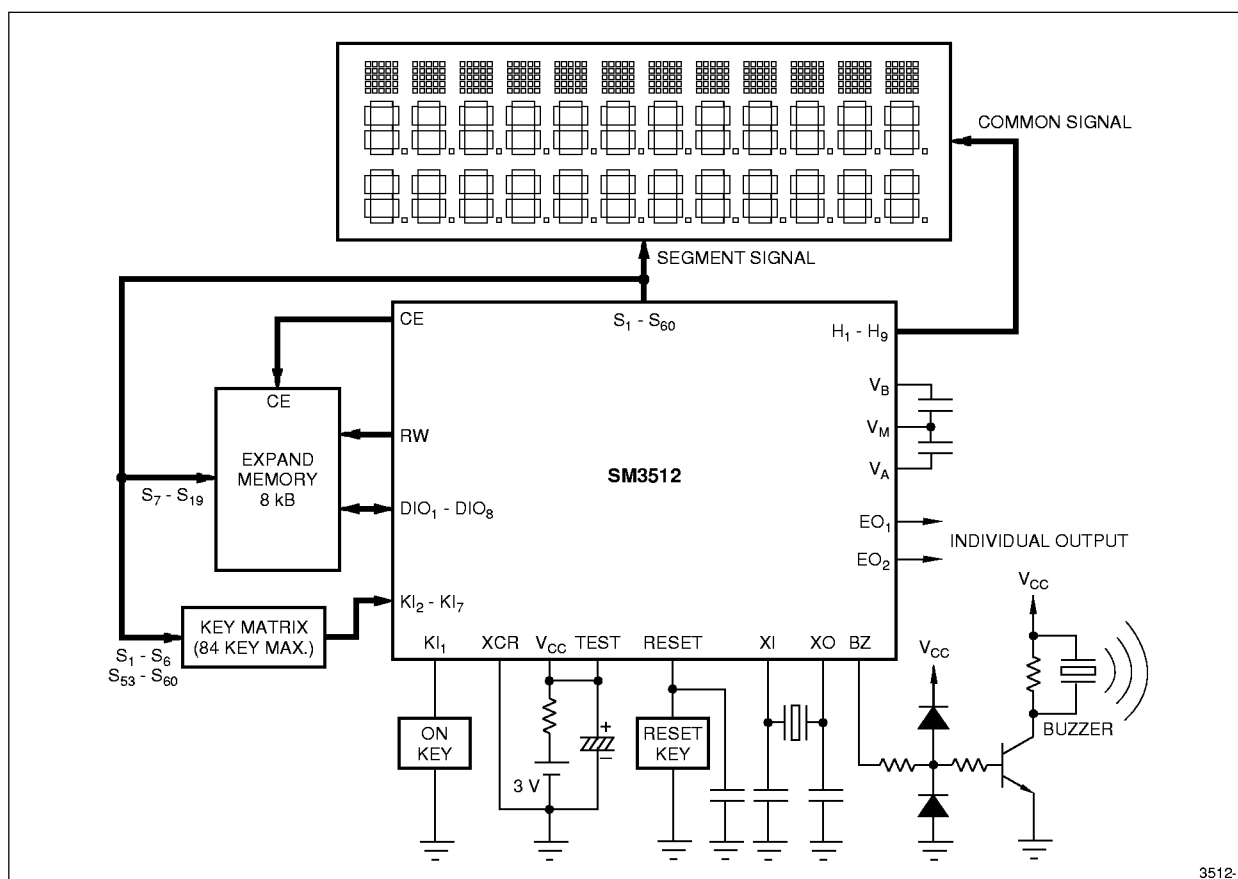


Figure 1. System Configuration Example

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