

# SIOV metal oxide varistors

Housed (ThermoFuse) varistors, AdvanceD series

Series/Type: ETFV14 Date: April 2011

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#### ThermoFuse varistors, ETFV14 series

# Construction

- Round varistor element, leaded
- Coating: epoxy resin, flame-retardant to UL 94 V-0
- Terminals: tinned copper wire, metal compound wire
- Housing: thermoplastic, flame-retardant to UL 94 V-0

### Features

- Wide operating voltage range 130 ... 420 V<sub>RMS</sub>
- Self-protected under abnormal overvoltage conditions
- High-energy AdvanceD series E2

### Approvals

- UL 🛛
- IEC
- VDE

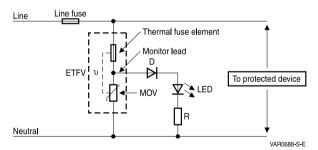
#### Applications

- Air conditioner, refrigerator, TV, etc.
- Power meter, inverter, telecom equipment, etc.
- Transient voltage surge suppressors (TVSS)
- Solar inverter

#### **Delivery mode**

Bulk (standard)

# **Typical applications**



# General technical data

Climatic category	to IEC 60068-1	40/85/56	
Operating temperature	to IEC 61051	-40 + 85	°C
Storage temperature		-40 + 85	°C
Electric strength	to IEC 61051	≥ 2.5	kV <sub>RMS</sub>
Insulation resistance	to IEC 61051	≥ 100	MΩ
Response time		< 25	ns

Please read *Cautions and warnings* and *Important notes* at the end of this document.

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# Electrical specifications and ordering codes Maximum ratings ( $T_A = 85 \ ^{\circ}C$ )

Ordering code	Туре	V <sub>RMS</sub>	V <sub>DC</sub>	i <sub>max</sub>	W <sub>max</sub>	P <sub>max</sub>
	(untaped)			(8/20 µs)	(2 ms)	
	SIOV-	V	V	А	J	W
B72214T2131K101	ETFV14K130E2	130	170	6000	50	0.6
B72214T2141K101	ETFV14K140E2	140	180	6000	55	0.6
B72214T2151K101	ETFV14K150E2	150	200	6000	60	0.6
B72214T2171K101	ETFV14K175E2	175	225	6000	70	0.6
B72214T2211K101	ETFV14K210E2	210	270	6000	80	0.6
B72214T2231K101	ETFV14K230E2	230	300	6000	90	0.6
B72214T2251K101	ETFV14K250E2	250	320	6000	100	0.6
B72214T2271K101	ETFV14K275E2	275	350	6000	110	0.6
B72214T2301K101	ETFV14K300E2	300	385	6000	125	0.6
B72214T2321K101	ETFV14K320E2	320	420	6000	136	0.6
B72214T2351K101	ETFV14K350E2	350	460	6000	136	0.6
B72214T2381K101	ETFV14K385E2	385	505	6000	136	0.6
B72214T2421K101	ETFV14K420E2	420	560	6000	136	0.6

# Characteristics (T<sub>A</sub> = 25 $^{\circ}$ C)

Ordering code	Туре	V <sub>v</sub>	$\Delta V_v$	V <sub>c,max</sub>	i <sub>c</sub>	C <sub>typ</sub>
	(untaped)	(1 mA)	(1 mA)	(i <sub>c</sub> )		(1 kHz)
	SIOV-	V	%	V	А	pF
B72214T2131K101	ETFV14K130E2	205	±10	340	50	650
B72214T2141K101	ETFV14K140E2	220	±10	360	50	610
B72214T2151K101	ETFV14K150E2	240	±10	395	50	570
B72214T2171K101	ETFV14K175E2	270	±10	455	50	490
B72214T2211K101	ETFV14K210E2	330	±10	545	50	410
B72214T2231K101	ETFV14K230E2	360	±10	595	50	380
B72214T2251K101	ETFV14K250E2	390	±10	650	50	350
B72214T2271K101	ETFV14K275E2	430	±10	710	50	320
B72214T2301K101	ETFV14K300E2	470	±10	775	50	300
B72214T2321K101	ETFV14K320E2	510	±10	840	50	280
B72214T2351K101	ETFV14K350E2	560	±10	910	50	260
B72214T2381K101	ETFV14K385E2	620	±10	1025	50	240
B72214T2421K101	ETFV14K420E2	680	±10	1120	50	220

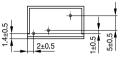


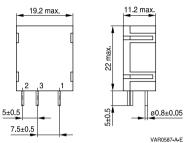


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# **Dimensional drawings**

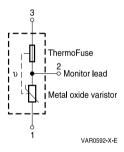




# Weight

Nominal diameter	V <sub>RMS</sub>	Weight
mm	V	g
14	130 420	4.0 5.6

# Lead configuration





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# Reliability data

Test	Test methods/conditions	Requirement
Varistor voltage	The voltage between two terminals with the specified measuring current applied is called $V_v$ (1 mA <sub>DC</sub> @ 0.2 2 s).	To meet the specified value
Clamping voltage	The maximum voltage between two terminals with the specified standard impulse current (8/20 µs) applied.	To meet the specified value
Endurance at upper category temperature	1000 h at UCT After having continuously applied the maximum allowable AC voltage at UCT $\pm 2$ °C for 1000 h, the specimen shall be stored at room temperature and normal humidity for 1 to 2 h. Thereafter, the change of V <sub>v</sub> shall be measured.	ΙΔV/V (1 mA)Ι ≤10%
Surge current derating, 8/20 μs	10 surge currents (8/20 μs), unipolar, interval 30 s, amplitude corresponding to derating curve for 10 impulses at 20 μs	I∆V/V (1 mA)I ≤10% (measured in direction of surge current) No visible damage
Surge current derating, 2 ms	10 surge currents (2 ms), unipolar, interval 120 s, amplitude corresponding to derating curve for 10 impulses at 2 ms	I∆V/V (1 mA)I ≤10% (measured in direction of surge current) No visible damage
Electric strength	IEC 61051-1, test 4.9.2 Metal balls method, 2500 $V_{\text{RMS}}$ , 60 s The varistor is placed in a container holding 1.6 ±0.2 mm diameter metal balls such that only the terminations of the varistor are protruding. The specified voltage shall be applied between both terminals of the specimen connected together and the electrode inserted between the metal balls.	No breakdown





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Test	Test methods/conditions	Requirement
Climatic sequence	The specimen shall be subjected to: a) dry heat at UCT, 16 h, IEC 60068-2-2, test Ba b) damp heat, 1st cycle: $55 \degree$ C, 93% r. H., 24 h, IEC 60068-2-30, test Db c) cold, LCT, 2 h, IEC 60068-2-1, test Aa d) damp heat, additional 5 cycles: $55 \degree$ C/25 °C, 93% r. H., 24 h/cycle, IEC 60068-2-30, test Db.	I∆V/V (1 mA)I ≤10% R <sub>ins</sub> ≥100 MΩ
	Then the specimen shall be stored at room temperature and normal humidity for 1 to 2 h. Thereafter, the change of $V_v$ shall be measured. Thereafter, insulation resistance $R_{ins}$ shall be measured at V = 500 V.	
Rapid change of temperature	IEC 60068-2-14, test Na, LCT/UCT, dwell time 30 min, 5 cycles	l∆V/V (1 mA)l ≤5% No visible damage
Damp heat, steady state	IEC 60068-2-78, test Ca	l∆V/V (1 mA)l ≤10%
	The specimen shall be subjected to 40 ±2 °C, 90 to 95% r. H. for 56 days without load / with 10% of the maxi- mum continuous DC operating voltage $V_{DC}$ . Then stored at room temperature and normal humidity for 1 to 2 h. Thereafter, the change of $V_V$ shall be measured. Thereafter, insulation resis- tance $R_{ins}$ shall be measured at V = 500 V (insulated varistors only).	R <sub>ins</sub> ≥100 MΩ



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Test	Test methods/conditions	Requirement
Solderability	IEC 60068-2-20, test Ta, method 1 with modified conditions for lead-free solder alloys: 245 °C, 3 s: After dipping the terminals to a depth of approximately 3 mm from the body in a soldering bath of 245 °C for 3 s, the terminals shall be visually examined.	The inspection shall be carried out under adequate light with normal eyesight or with the assistance of a magnifier capable of giving a magnification of 4 to 10 times. The dipped surface shall be covered with a smooth and bright solder coating with no more than small amounts of scattered imperfections such as pinholes or un-wetted or de-wetted areas. These imperfections shall not be concentrated in one area.
Resistance to soldering heat	IEC 60068-2-20, test Tb, method 1A, 260 °C, 10 s: Each lead shall be dipped into a solder bath having a temperature of 260 $\pm$ 5 °C to a point 2.0 to 2.5 mm from the body of the specimen, be held there for 10 $\pm$ 1 s and then be stored at room temperature and normal humidity for 1 to 2 h. The change of V <sub>V</sub> shall be measured and the specimen shall be visually examined.	I∆V/V (1 mA)I ≤5% No visible damage
Tensile strength	IEC 60068-2-21, test Ua1 After gradually applying the force specified below and keeping the unit fixed for 10 s, the terminal shall be visually examined for any damage. Force for wire diameter: 0.6 mm = 10 N 0.8 mm = 10 N 1.0 mm = 20 N	I∆V/V (1 mA)I ≤5% No break of solder joint, no wire break





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Test	Test methods/conditions	Requirement
Vibration	IEC 60068-2-6, test Fc, method B4	l∆V/V (1 mA)l ≤5%
	Frequency range: $10 \dots 55 \text{ Hz}$ Amplitude: $0.75 \text{ mm or } 98 \text{ m/s}^2$ Duration: $6 \text{ h} (3 \cdot 2 \text{ h})$ Pulse:sine waveAfter repeatedly applying a singleharmonic vibrationaccording to thetable above.The change of V <sub>V</sub> shall be measuredand the specimen shall be visuallyexamined.	No visible damage
Bump	IEC 60068-2-29, test Eb Pulse duration: 6 ms Max. acceleration: 400 m/s <sup>2</sup> Number of bumps: 4000 Pulse: half sine	l∆V/V (1 mA)l ≤5% No visible damage
Fire hazard	IEC 60695-11-5 (needle flame test) Severity: vertical 10 s	5 s max.



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Test	Test methods/conditions			Requirement			
Abnormal overvoltage test	The device is designed to meet the limited current abnormal overvoltage condition, outlined in section 39.4 of UL 1449, 3 <sup>rd</sup> edition. Detailed test voltage applied onto the device for different types as in the following table:			<ul> <li>Any of these phenomena shall not be observed, or this specimen will be judged as failed part:</li> <li>1. Emission of flame, molt metal, glowing or flamir particles through any</li> </ul>			
	Туре	Device rating V	Test voltage V		openings (pre-existed or created as a result of the test) in the product.		
	ETFV14K130E2	130	260	2.	Charring, glowing, or		
	ETFV14K140E2	140	280		flaming of the supporting		
	ETFV14K150E2	150	300		surface, tissue paper, or cheesecloth.		
	ETFV14K175E2	175	350	3.	Ignition of the enclosure.		
	ETFV14K210E2	210	420	4.	Creation of any opening		
	ETFV14K230E2	230	415		in the enclosure that		
	ETFV14K250E2	250	500		result in accessibility of		
	ETFV14K275E2	275	480		live parts, when		
	ETFV14K300E2	300	600		evaluated in accordance		
	ETFV14K320E2	320	600		with accessibility of live parts test in section 58.2		
	ETFV14K350E2	350	600		of UL1449, 3 <sup>rd</sup> edition.		
	ETFV14K385E2	385	600		,		
	ETFV14K420E2	420	600				

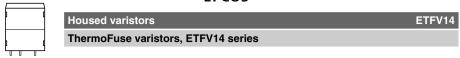
# Note:

UCT = Upper category temperature

LCT = Lower category temperature

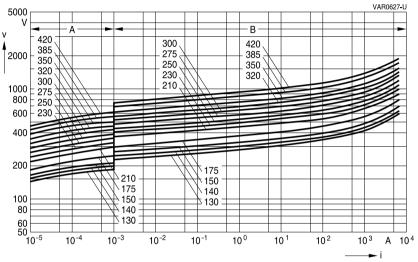
R<sub>ins</sub> = Insulation resistance





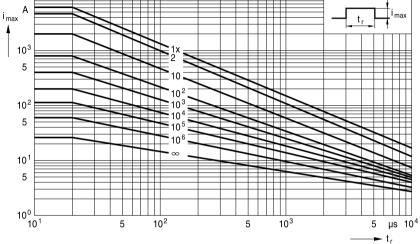
# v/i characteristics

v = f (i) for explanation of the characteristics refer to "General technical information", chapter 1.6.3 A = Leakage current, B = Protection level } for worst-case varistor tolerances



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#### **Derating curves**

Maximum surge current i<sub>max</sub> = f (t<sub>r</sub>, pulse train)

For explanation of the derating curves refer to "General technical information", section 1.8.1



Please read Cautions and warnings and

Important notes at the end of this document.





VAR0435-Z

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# Cautions and warnings

# General

- EPCOS metal oxide varistors are designed for specific applications and should not be used for purposes not identified in our specifications, application notes and data books unless otherwise agreed with EPCOS during the design-in-phase.
- 2. Ensure suitability of SIOVs through reliability testing during the design-in phase. SIOVs should be evaluated taking into consideration worst-case conditions.
- 3. For applications of SIOVs in line-to-ground circuits based on various international and local standards there are restrictions existing or additional safety measures required.

# Storage

1. Store SIOVs only in original packaging. Do not open the package before storage.

2.	. Storage conditions in original packaging:				
	Storage temperature:	−25 °C +45 °C,			
	Relative humidity:	<75% annual average,			
		<95% on maximum 30 days a year.			
	Dew precipitation:	is to be avoided.			

- 3. Avoid contamination of an SIOV's during storage, handling and processing.
- 4. Avoid storage of SIOVs in harmful environments that can affect the function during long-term operation (examples given under operation precautions).
- 5. The SIOV type series should be soldered within the time specified:

SIOV-S, -Q, -LS, -B, -SFS	24 months
ETFV	12 months.

# Handling

- 1. SIOVs must not be dropped.
- 2. Components must not be touched with bare hands. Gloves are recommended.
- 3. Avoid contamination of the surface of SIOV electrodes during handling, be careful of the sharp edge of SIOV electrodes.

#### Soldering (where applicable)

- 1. Use rosin-type flux or non-activated flux.
- 2. Insufficient preheating may cause ceramic cracks.
- 3. Rapid cooling by dipping in solvent is not recommended.
- 4. Complete removal of flux is recommended.



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# Mounting

- 1. Potting, sealing or adhesive compounds can produce chemical reactions in the SIOV ceramic that will degrade the component's electrical characteristics.
- 2. Overloading SIOVs may result in ruptured packages and expulsion of hot materials. For this reason SIOVs should be physically shielded from adjacent components.

# Operation

- 1. Use SIOVs only within the specified temperature operating range.
- 2. Use SIOVs only within the specified voltage and current ranges.
- Environmental conditions must not harm SIOVs. Use SIOVs only in normal atmospheric conditions. Avoid use in deoxidizing gases (chlorine gas, hydrogen sulfide gas, ammonia gas, sulfuric acid gas etc), corrosive agents, humid or salty conditions.Contact with any liquids and solvents should be prevented.





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# Symbols and terms

Symbol	Term
С	Capacitance
C <sub>typ</sub>	Typical capacitance
i	Current
i <sub>c</sub>	Current at which $V_{c, max}$ is measured
I <sub>leak</sub>	Leakage current
i <sub>max</sub>	Maximum surge current (also termed peak current)
l <sub>max</sub>	Maximum discharge current to IEC 61643-1
I <sub>nom</sub>	Nominal discharge current to IEC 61643-1
LCT	Lower category temperature
L <sub>typ</sub>	Typical inductance
P <sub>max</sub>	Maximum average power dissipation
R <sub>ins</sub>	Insulation resistance
R <sub>min</sub>	Minimum resistance
T <sub>A</sub>	Ambient temperature
t <sub>r</sub>	Duration of equivalent rectangular wave
UCT	Upper category temperature
v	Voltage
V <sub>clamp</sub>	Clamping voltage
V <sub>c, max</sub>	Maximum clamping voltage at specified current $i_{\rm c}$
V <sub>DC</sub>	DC operating voltage
$V_{jump}$	Maximum jump start voltage
V <sub>max</sub>	Maximum voltage
V <sub>op</sub>	Operating voltage
V <sub>RMS</sub>	AC operating voltage, root-mean-square value
$V_{\text{RMS, op, max}}$	Root-mean-square value of max. DC operating voltage incl. ripple current
V <sub>surge</sub>	Super imposed surge voltage
$V_{v}$	Varistor voltage
$\Delta V_V$	Tolerance of varistor voltage
$W_{LD}$	Maximum load dump
W <sub>max</sub>	Maximum energy absorption
e	Lead spacing

All dimensions are given in mm.

The commas used in numerical values denote decimal points.

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