International

HYBRID-HIGH RELIABILITY RADIATION HARDENED LOW POWER DC-DC CONVERTER

Description

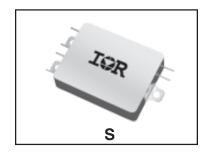
The S-Series of DC-DC converters are low power radiation hardened, high reliability devices designed for hostile radiation environments such as those encountered by geostationary earth orbit satellites, deep space probes and communication systems. Features include small size, high efficiency, low weight and a high tolerance to total ionizing dose, single event effects, and environmental stresses such as temperature extremes, mechanical shock, and vibration. Extensive documentation including Radiation Susceptibility, Thermal Analysis, Stress Analysis and MTBF are available.

The converters incorporate a fixed frequency single ended forward topology with magnetic feedback and an internal EMI filter utilizing large multilayer ceramic capacitors processed per MIL-PRF-49470 for improved reliability. All models include an external inhibit port. They are encased in a hermetic 1.71" x 1.31" x 0.45" steel package and weigh less than 50 grams. The package utilizes rugged ceramic feed-through copper core pins and is hermetically sealed using parallel seam welding.

Manufactured in a facility fully qualified to MIL-PRF-38534, these converters are fabricated utilizing DLA Land and Maritime qualified processes. For available screening options, refer to device screening table in the data sheet.

Non-flight versions of the S-Series converters are available for system development purposes. Variations inelectrical specifications and screening to meet custom requirements can be accommodated. PD-94584K

S-SERIES 28V Input, Single/Dual Output



Features

- Total Dose >100K Rad(Si)
- SEE Hardened to LET up to 82 MeV.cm²/mg
- Low Weight < 50 grams
- Magnetically Coupled Feedback
- 18V to 40V DC Input Range
- Up to 10W Output Power
- Single and Dual Output Models Include 3.3, 5, 7,12,15, ±5, ±12 and ±15V
- High Efficiency to 82%
- -55°C to +125°C Operating Temperature Range
- 100MΩ @ 500VDC Isolation
- Under-Voltage Protection
- Short Circuit and Overload Protection
- Output Over Voltage Limiter
- External Inhibit
- Standard Microcircuit Drawings Available

Applications

- Geostationary or Low Earth Orbit Satellites
- Launch Vehicles
- Communication Systems

Circuit Description

The S-Series converters utilize a single-ended forward topology with resonant reset. The nominal switching frequency is 525KHz. Electrical isolation and tight output regulation are achieved through the use of a magnetically coupled feedback. Voltage feed-forward with duty factor limiting provides high line rejection.

Output power is limited under any load fault condition to approximately 135% for singles and 145% for duals of rated. An overload condition causes the converter output to behave like a constant current source with the output voltage dropping below nominal. The converter will resume normal operation when the load current is reduced below the current limit point. This protects the converter from both overload and short circuit conditions. There are no latching elements included in the load fault protection circuits to eliminate the possibility of falsely triggering the protection circuits during single event radiation exposure.

An under-voltage protection circuit prohibits the converter from operating when the line voltage is too low for safe operation. The converter will not start until the line voltage rises to approximately 12V.

An external enable port is provided to control converter operation. This input is intended for operation with an open collector transistor drive or a relay closure to the input return. The pin may be left open for normal operation and has a nominal open circuit voltage of 10.5V.

Design Methodology

The S-Series was developed using a proven conservative design methodology, which includes selecting radiation tolerant, and established reliability components and fully derating to the requirements of MIL-STD-975 and MIL-STD-1547 except for the CDR type of capacitors, a capacitor with 50V rating is used for in-circuit voltage stress of less than10V. Heavy derating of the radiationhardened power MOSFET virtually eliminates the possibility of SEGR and SEB. A magnetic feedback circuit is utilized instead of opto-couplers to minimize temperature, radiation and aging sensitivity. PSPICE was used extensively to predict and optimize circuit performance for both beginning and end-oflife. Thorough design analyses include Radiation Susceptibility (TREE), Worst Case, Stress, Thermal and Reliability (MTBF).

Specifications

| Absolute Maximum Ratings | | Recommended Operating Conditions | | |
|---------------------------------------|-----------------------|------------------------------------|------------------|--|
| Input voltage range -0.5Vdc to +60Vdc | | Input voltage range | +18Vdc to +40Vdc | |
| Output power | Internally limited | Output power | 0 to Max. Rated | |
| Lead temperature | +300°C for 10 seconds | | | |
| Operating temperature | -55°C to +135°C | Operating temperature | -55°C to +125°C | |
| Storage temperature | -55°C to +135°C | Operating temperature ¹ | -55°C to +70°C | |

¹ Meets derating per MIL-STD-975

Electrical Performance Characteristics

| | Group A | Conditions -55°C \leq T _C \leq +85°C | | Limits | | |
|--|--|--|--|--|---|------|
| Parameter | Subgroup | $V_{IN} = 28V DC \pm 5\%$, $C_L = 0$ unless otherwise specified | Min | Nom | Max | Unit |
| Input Voltage | | | 18 | 28 | 40 | V |
| Output Voltage (V _{OUT}) S2803R3S S2805S S2807S S2812S S2815S S2805D S2815D S2815D S2803R3S S2805S S2807S | 1 1 1 1 1 1 1 2,3 2,3 2,3 2,3 2,3 | I _{OUT} = 100% rated load Note 4 I _{OUT} = 100% rated load | $\begin{array}{c} 3.27\\ 4.95\\ 6.93\\ 11.88\\ 14.85\\ \pm 4.95\\ \pm 11.88\\ \pm 14.85\\ 3.230\\ 4.900\\ 6.895\\ 11.760\end{array}$ | 3.30 5.00 7.00 12.00 ±5.00 ±12.00 ±15.00 | $\begin{array}{r} 3.33\\ 5.05\\ 7.07\\ 12.12\\ 15.15\\ \pm 5.05\\ \pm 12.12\\ \pm 15.15\\ 3.370\\ 5.100\\ 7.105\\ 12.240\\ \end{array}$ | V |
| S2812S S2815S S2805D S2812D S2815D | 2,3 2,3 2,3 2,3 2,3 2,3 | Note 4 | 14.700 ±4.900 ±11.760 ±14.700 | | 12.240 15.300 ±5.100 ±12.240 ±15.300 | |
| Output power (P _{OUT}) All models | 1,2,3 | V _{IN} = 18, 28, 40 Volts, Note 2 | 0 | | 10 | w |
| Output current (I _{OUT}) S2803R3S S2805S S2807S S2812S S2815S S2815S S2805D S2812D S2815D | 1,2,3 | V _{IN} = 18, 28, 40 Volts, Note 2 Either Output, Note 3 Either Output, Note 3 Either Output, Note 3 | 0 0 0 0.16 0.06 0.05 | | 3.03 2.00 1.43 0.83 0.67 1.60 0.66 0.54 | A |
| Line regulation (VR _{LINE}) S28XXS S28XXD | 1,2,3 | V _{IN} = 18, 28, 40 Volts I _{OUT} = 0, 50%, 100% rated I _{OUT} = 10%, 50%, 100% rated, Note 4 | -0.5 -0.5 | | 0.5 0.5 | % |
| Load regulation (VR _{LOAD}) S28XXS S28XXD | 1,2,3 | V _{IN} = 18, 28, 40 Volts I _{OUT} = 0, 50%, 100% rated I _{OUT} = 10%, 50%, 100% rated, Note 4 | -1.0 -1.0 | | 1.0 1.0 | % |
| Cross regulation (VR _{CROSS}) S2805D S2812D S2815D | 1,2,3 | Duals only, Note 5 V _{IN} = 18, 28, 40 Volts | -5.0 -3.0 -3.0 | | 5.0 3.0 3.0 | % |

For Notes to Electrical Performance Characteristic Table, refer to page 6

| | Group A | Conditions -55°C \leq T _C \leq +85°C | | Limits | | |
|--|---|---|--|--|--|--------------|
| Parameter | Subgroup $V_{IN} = 28V DC \pm 5\%$, $C_L = 0$ unless otherwise specified | | Min | Nom | Max | Unit |
| Total Regulation S2803R3S S2805S S2807S S2812S S2815S S2805D S2812D S2815D | 1,2,3 | V _{IN} = 18, 28, 40 Volts Single Output: I _{OUT} = 0% ,50%,100% of rated current Note 14 Dual Output: I _{OUT} = 10% ,50%,100% of rated current Notes 4, 14 | 2.0 2.0 2.0 2.0 5.0 3.0 3.0 | | 2.0 2.0 2.0 2.0 2.0 5.0 3.0 3.0 | % |
| Input current (I _N) S2803R3S S2805S S2807S S2812S S2815S S2805D S2812D S2815D | 1,2,3 | I _{OUT} = 0, Pin 4 open | | | 60 70 70 70 70 70 70 70 | mA |
| | | Pin 4 shorted to pin 2 | | | 5.0 | mA |
| Switching frequency (F_S) | 1,2,3 | | 475 | 525 | 575 | KHz |
| Output ripple (V _{RIP}) S2803R3S S2805S S2807S S2812S S2815S S2805D S2812D S2815D | 1,2,3 | V _{IN} = 18, 28, 40 Volts I _{OUT} = 100% rated load Notes 4, 6 | | | 50 50 45 50 60 80 80 80 80 | mV p-p |
| Efficiency (E _{FF}) S2803R3S S2805S S2807S S2812S S2815S S2805D S2812D S2815D | 1,2,3 | I _{OUT} = 100% rated load Note 4 | 68 75 76 78 77 76 76 76 | 73 80 81 81 82 80 81 82 | | % |
| Enable Input (Inhibit Function) Open circuit voltage Drive current (sink) Voltage range | 1,2,3 | Note 1 | 9.5 -0.5 | | 11.5 500 50 | V μA V |
| Current Limit Point Expressed as a percentage of full rated load current S2803R3S S2805S S2805S S2812S S2812S S2815S S2805D S2812D S2815D | 1,2,3 | V _{OUT} = 90% of Nominal, Note 4, 13 | 105 105 110 105 110 105 105 105 | | 140 140 144 145 146 150 160 167 | % |

Electrical Performance Characteristics (continued)

For Notes to Electrical Performance Characteristic Table, refer to page 6

| | Group A | Conditions -55°C ≤ T _C ≤ +85°C | Limits | | | 11.5 |
|---|----------|--|-----------------------|-----|--|-------|
| Parameter | Subgroup | $V_{IN} = 28V DC \pm 5\%$, $C_L = 0$ unless otherwise specified | Min | Nom | Max | Unit |
| Power dissipation, load fault (P_D) | 1,2,3 | Short Circuit, Overload, Note 8 | | | 9.0 | w |
| Output response to step load changes (V _{TLD}) | 4,5,6 | Half Load to/ from Full Load, Notes 4,9 | -300 | | 300 | mV pk |
| Recovery time, step load changes (T _{TLD}) | 4,5,6 | Half Load to/from Full Load, Notes 4,9,10 | | | 200 | μs |
| Output response to step line changes (V _{TLN}) | 4,5,6 | 18V to/from 40V I _{OUT} = 100% rated load, Notes 1,4,11 | -300 | | 300 | mV pk |
| Recovery time, step line changes (T _{TLN}) | 4,5,6 | 18V to/from 40V I _{OUT} = 100% rated load, Notes 1,4,10,11 | | | 200 | μs |
| Turn-on Response Overshoot (V _{OS}) S2803R3S S2807S S2812S S2815S S2815D S2815D Turn-on Delay (T _{DLY}) | 4,5,6 | 10% Load, Full Load Notes 4,12 | 1.5 | | 500 600 750 1400 1650 600 1400 1500 10 | mV |
| Capacitive Load (C _L) S2803R3S S2805S S2807S S2812S S2815S S2805D S2812D S2815D | 1 | I _{OUT} = 100% rated load No effect on DC performance Notes 1, 4, 7 Each output on duals | | | 2200 1000 750 180 120 500 90 60 | μF |
| Line Rejection | 1 | I _{OUT} = 100% rated load DC to 50KHz, Notes 1, 4 | | 50 | | dB |
| Isolation | 1 | Input to Output or Any Pin to Case except pin 3, test @ 500VDC | 100 | | | MΩ |
| Device Weight | | | | | 50 | g |
| MTBF | | MIL-HDBK-217F2, SF, 35°C | 1.5 x 10 ⁶ | | | Hours |

Electrical Performance Characteristics (continued)

For Notes to Electrical Performance Characteristic Table, refer to page 6

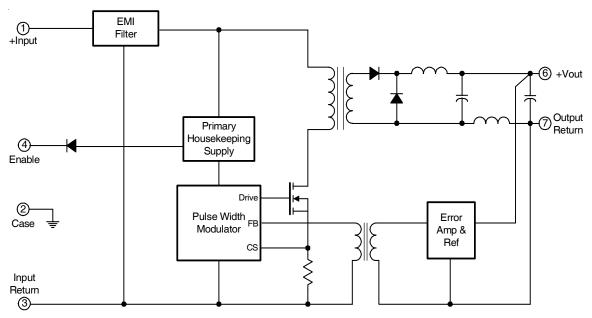
Notes: Electrical Performance Characteristics

- 1. Parameter is tested as part of design characterization or after design changes. Thereafter, parameter shall be guaranteed to the limits specified.
- 2. Parameter verified during line and load regulation tests.
- 3. Output load current must be distributed such that at least 20% of the total load current is being provided by one of the outputs.
- 4. Load currents split equally between outputs on dual output models. For dual output models, the output may not meet regulation limits but the converter will not be damaged under this condition.
- 5. Cross regulation is measured with 20% rated load on output under test while changing the load on the other output from 20% to 80% of rated.
- 6. Guaranteed for a D.C. to 20MHz bandwidth. Tested using a 20KHz to 10MHz bandwidth.
- 7. Capacitive load may be any value from 0 to the maximum limit without compromising dc performance. A capacitive load in excess of the maximum limit may interfere with the proper operation of the converter's overload protection, causing erratic behavior during turn-on.
- 8. Overload power dissipation is defined as the device power dissipation with the load set such that $V_{OUT} = 90\%$ of nominal.
- 9. Load step transition time $\ge 10 \ \mu s$.
- 10. Recovery time is measured from the initiation of the transient to where VouT has returned to within ±1% of its steady state value.
- 11. Line step transition time \geq 100 μ s.
- 12. Turn-on delay time from either a step application of input power or a logic low to a logic high transition on the inhibit pin (pin 4) to the point where Vout = 90% of nominal.
- 13. Current limit point expressed as a percentage of full rated load current.
- 14. Total Regulation includes all combinations of line and load compared to the nominal output voltage. For dual models all line, load, and cross regulation conditions are tested per Note 4 and Note 5.

| Test | Conditions | | Тур | Unit |
|--|---|------|-----|-------------------------|
| | MIL-PRF-883, Method 1019.5 | | | |
| Total Ionizing Dose (Gamma) | Operating bias applied during exposure, | | | |
| | Half Rated Load, V _{IN} = 28V | | | Krads(Si) |
| Heavy lons (LET) | | | | |
| Single Event Effects Operating bias applied during exposure, | | | | |
| SEU, SEL, SEGR, SEB | Full Rated Load, $V_{IN} = 18V$, 28V & 40V | 82 1 | | MeV•cm ² /mg |
| | Test lab: Cyclotron Institute, | | | _ |
| | Texas A & M University | | | |

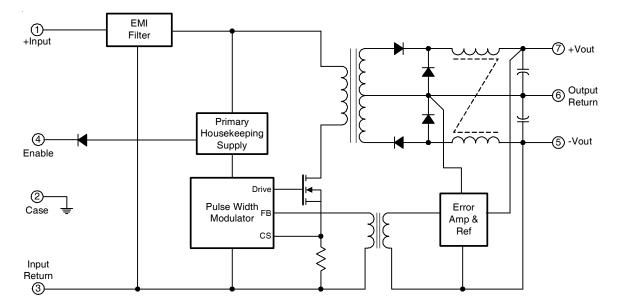
Radiation Performance Characteristics

International Rectifier currently does not have a DLA Land and Maritime certified Radiation Hardness Assurance Program.

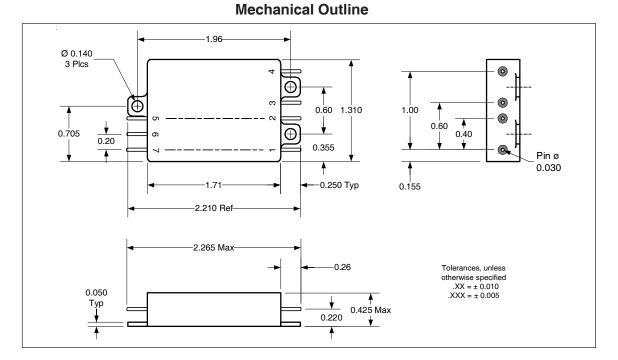








International **tor** Rectifier



Pin Designation

| Pin # | Single | Pin # | Dual |
|-------|---------------|-------|---------------|
| 1 | + Vin | 1 | + Vin |
| 2 | Case | 2 | Case |
| 3 | Input Return | 3 | Input Return |
| 4 | Enable | 4 | Enable |
| 5 | NC | 5 | - Vout |
| 6 | + Vout | 6 | Output Return |
| 7 | Output Return | 7 | + Vout |

Standard Microcircuit Drawing Equivalence Table

| Standard Microcircuit | IR Standard |
|-----------------------|-------------|
| Drawing Number | Part Number |
| 5962-04238 | S2803R3S |
| 5962-04239 | S2805S |
| 5962-13216 | S2807S |
| 5962-04240 | S2812S |
| 5962-04241 | S2815S |
| 5962-04242 | S2805D |
| 5962-04243 | S2812D |
| 5962-04244 | S2815D |

International **TOR** Rectifier

S-SERIES (28V Input, Single/Dual Output)

Device Screening

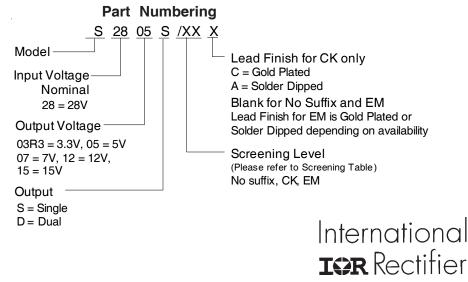
| Requirement | MIL-STD-883 Method | No Suffix ② | CK © | ЕМ |
|---------------------------|--------------------|-----------------|-----------------|----------------|
| Temperature Range | — | -55°C to +85°C | -55°C to +85°C | -55°C to +85°C |
| Element Evaluation | MIL-PRF-38534 | Class K | Class K | N/A |
| Non-Destructive Bond Pull | 2023 | Yes | Yes | N/A |
| Internal Visual | 2017 | Yes | Yes | 0 |
| Temperature Cycle | 1010 | Cond C | Cond C | Cond C |
| Constant Acceleration | 2001, Y1 Axis | 3000 Gs | 3000 Gs | 3000 Gs |
| PIND | 2020 | Cond A | Cond A | N/A |
| Burn-In | 1015 | 320 hrs @ 125°C | 320 hrs @ 125°C | 48 hrs @ 125°C |
| Duni-in | 1015 | (2 x 160 hrs) | (2 x 160 hrs) | |
| Final Electrical | MIL-PRF-38534 | -55°C, +25°C, | -55°C, +25°C, | -55°C, +25°C, |
| (Group A) | & Specification | +85°C | +85°C | +85°C |
| PDA | MIL-PRF-38534 | 2% | 2% | N/A |
| Seal, Fine and Gross | 1014 | Cond A, C | Cond A, C | Cond A |
| Radiographic | 2012 | Yes | Yes | N/A |
| External Visual | 2009 | Yes | Yes | 0 |

Notes:

① Best commercial practice.

② CK is a DLA Land and Maritime (formerly DSCC) part marking used to designate a Class K compliant hybrid. The CK marking does not indicate the hybrid is radiation certified.

No Suffix is a radiation rated device but not available as a DLA Land and Martime qualified SMD per MIL-PRF-38534. International Rectifier currently does not have a DLA Land and Maritime certified Radiation Hardness Assurance Program.



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