APPLICATION MANUAL

Auto Discharge, Ultra Small Package, High RR, Fast Response, Low Noise 200mA CMOS LDO Regulator IC TK634xxAMF

CONTENTS

Auto Discharge, Ultra Small Package, High RR, Fast Response, Low Noise 200mA CMOS LDO Regulator IC TK634xxAMF

1. DESCRIPTION

The TK634xxAMF is a CMOS LDO regulator with auto discharge function. The package is the very small and thin HSON1214-4.
The IC is designed for portable applications with space requirements.
The IC can supply 200mA output current.
The IC does not require a noise-bypass capacitor.
The output voltage is internally fixed from 1.5V to 4.2V.

2. FEATURES

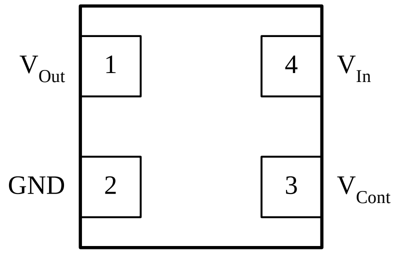
- Auto discharge function
- Package: HSON1214-4
- No noise bypass capacitor required
- High ripple rejection
- Fast transient response
- Low noise
- Thermal and over current protection
- High maximum load current
- On/Off control
- High accuracy

3. APPLICATIONS

- Mobile communication
- Battery powered system
- Any electronic equipment

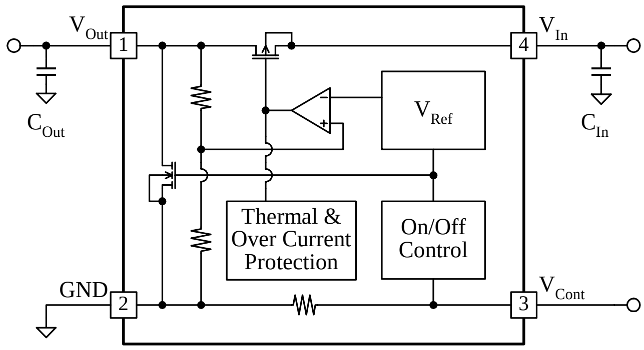
4. PIN CONFIGURATION

- HSON1214-4



(Top View)

5. BLOCK DIAGRAM



6. ORDERING INFORMATION

T K 6 3 4 □□ A □□ G 0 L - C

Voltage Code
(Refer to the following table)

Package Code
MF : HSON1214-4

Solder Composion Code
G0 : Lead Free

Tape/Reel Code
L : Left type

Operating Temp. Range Code
C : C Rank(standard)

Output Voltage	Voltage Code	Output Voltage	Voltage Code	Output Voltage	Voltage Code
1.5V	15	2.85V	01	3.5V	35
1.8V	18	2.9V	29	4.0V	40
2.5V	25	3.0V	30		
2.6V	26	3.1V	31		
2.7V	27	3.2V	32		
2.8V	28	3.3V	33		

*If you need a voltage other than the value listed in the above table, please contact TOKO.

7. ABSOLUTE MAXIMUM RATINGS

T_a=25°C

Parameter	Symbol	Rating	Units	Conditions
Absolute Maximum Ratings				
Input Voltage	$V_{In,MAX}$	-0.3 ~ 6.0	V	
Output pin Voltage	$V_{Out,MAX}$	-0.3 ~ V_{In}+0.3	V	
Control pin Voltage	$V_{Cont,MAX}$	-0.3 ~ 6.0	V	
Storage Temperature Range	T_{stg}	-55 ~ 150	°C	
Power Dissipation	P_D	400	mW	Internal Limited T_j=150°C *****, When mounted on PCB
Operating Condition				
Operational Temperature Range	T_{OP}	-40 ~ 85	°C	
Operational Voltage Range	V_{OP}	1.8 ~ 6.0	V	

* P_D must be decreased at the rate of 3.2mW/°C for operation above 25°C.
The maximum ratings are the absolute limitation values with the possibility of the IC being damaged.
When operation exceeds this standard quality can not be guaranteed.

8. ELECTRICAL CHARACTERISTICS

The parameters with min. or max. values will be guaranteed at $T_a=T_j=25°C$ with test when manufacturing or SQC(Statistical Quality Control) methods. The operation between -40 ~ 85°C is guaranteed by design.

$V_{In}=V_{Out,TYP}+1V$, $V_{Cont}=1.2V$, $T_a=T_j=25°C$

Parameter	Symbol	Value			Units	Conditions
		MIN	TYP	MAX		
Output Voltage	V_{Out}	**Refer to TABLE 1**			V	$I_{Out}=5mA$
Line Regulation	LinReg	-	0.0	4.0	mV	$\Delta V_{In}=1V$
Load Regulation	LoaReg	**Refer to TABLE 1**			mV	**Refer to TABLE 1**
Dropout Voltage ***1**	V_{Drop}	**Refer to TABLE 1**			mV	**Refer to TABLE 1**
Maximum Load Current ***2**	$I_{Out,MAX}$	210	350	-	mA	$V_{Out}=V_{Out,TYP}\times0.9$
Quiescent Current	I_Q	-	35	70	μA	$I_{Out}=0mA$, $V_{Cont}=V_{In}$
Standby Current	$I_{Standby}$	-	0.01	0.1	μA	$V_{Cont}=0V$
GND Pin Current	I_{GND}	-	55	110	μA	$I_{Out}=50mA$, $V_{Cont}=V_{In}$
Discharge Resistance	R_{Dis}	-	25	-	Ω	$V_{In}=5V$, $V_{Out}=0.1V$, $V_{Cont}=0V$
Control Terminal						
Control Current	I_{Cont}	-	0.7	1.4	μA	$V_{Cont}=1.2V$
Control Voltage	V_{Cont}	1.2	-	-	V	V_{Out} On state
		-	-	0.2	V	V_{Out} Off state

Reference Value						
Output Voltage / Temp.	$\Delta V_{Out}/\Delta T_a$	-	100	-	ppm/°C	$I_{Out}=5mA$
Output Noise Voltage (TK63428A)	V_{Noise}	-	35	-	μVrms	$C_{Out}=1.0\mu F$, $I_{Out}=30mA$, BPF=400Hz~80kHz
Ripple Rejection (TK63428A)	RR	-	72	-	dB	$C_{Out}=1.0\mu F$, $I_{Out}=10mA$, f=1kHz
Rise Time (TK63428A)	t_r	-	85	-	μs	$C_{Out}=1.0\mu F$, $I_{Out}=30mA$ V_{Cont}: Pulse Wave (100Hz), V_{Cont} On → $V_{Out}\times95\%$ point

***1**: For $V_{Out} \leq 1.8V$, no regulations.
***2**: The maximum output current is limited by power dissipation.
The maximum load current is the current where the output voltage decreases to 90% by increasing the output current at $T_j=25°C$, compared to the output voltage specified at $V_{In}=V_{Out,TYP}+1V$. The maximum load current indicates the current at which over current protection turn on.
For all output voltage products, the maximum output current for normal operation without operating any protection is 200mA. Accordingly, LoaReg and V_{Drop} are specified on the condition that I_{Out} is less than 200mA.

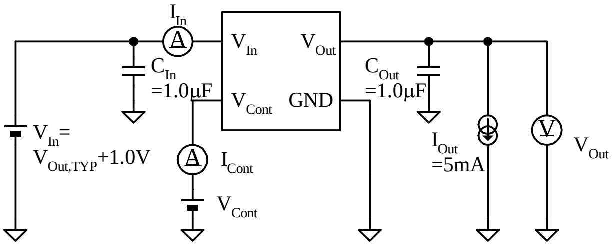
General Note
Parameters with only typical values are just reference. (Not guaranteed)
The noise level is dependent on the output voltage, the capacitance and capacitor characteristics.

TABLE 1. Preferred Product (TK634xxAMF)

Part Number	Output Voltage			Load Regulation				Dropout Voltage			
				I_{Out}=5 ~ 100mA		I_{Out}=5 ~ 200mA		I_{Out}=100mA		I_{Out}=200mA	
	MIN	TYP	MAX	TYP	MAX	TYP	MAX	TYP	MAX	TYP	MAX
	V	V	V	mV	mV	mV	mV	mV	mV	mV	mV
TK63415AMF	1.475	1.500	1.525	10	40	19	76	-	-	-	-
TK63418AMF	1.775	1.800	1.825	10	40	20	80	-	-	-	-
TK63425AMF	2.475	2.500	2.525	10	40	20	80	95	145	185	310
TK63426AMF	2.574	2.600	2.626	10	40	20	80	90	145	180	300
TK63427AMF	2.673	2.700	2.727	10	40	20	80	90	140	175	295
TK63428AMF	2.772	2.800	2.828	10	40	20	80	85	135	170	290
TK63401AMF	2.821	2.850	2.879	10	40	20	80	85	135	170	285
TK63429AMF	2.871	2.900	2.929	10	40	20	80	85	135	165	285
TK63430AMF	2.970	3.000	3.030	10	40	20	80	85	130	165	280
TK63431AMF	3.069	3.100	3.131	11	44	21	84	80	130	160	275
TK63432AMF	3.168	3.200	3.232	11	44	21	84	80	125	160	270
TK63433AMF	3.267	3.300	3.333	11	44	21	84	80	125	155	265
TK63435AMF	3.465	3.500	3.535	11	44	21	84	75	120	150	255
TK63440AMF	3.960	4.000	4.040	11	44	21	84	70	115	140	240

Notice.
Please contact your authorized TOKO representative for voltage availability.

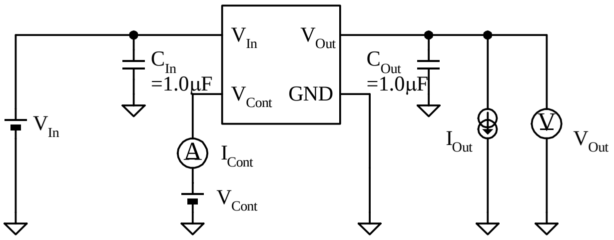
9. TEST CIRCUIT



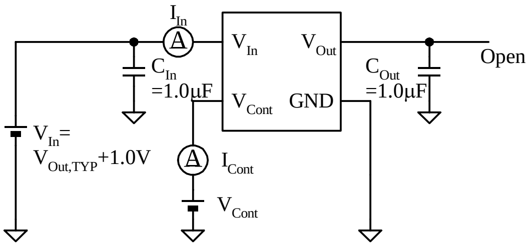
■ Test circuit for electrical characteristic

Notice.

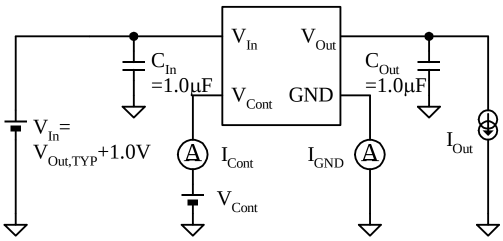
The limit value of electrical characteristics is applied when C_{In}=1.0µF(Ceramic), C_{Out}=1.0µF(Ceramic).
But C_{In}, and C_{Out} can be used with both ceramic and tantalum capacitors.



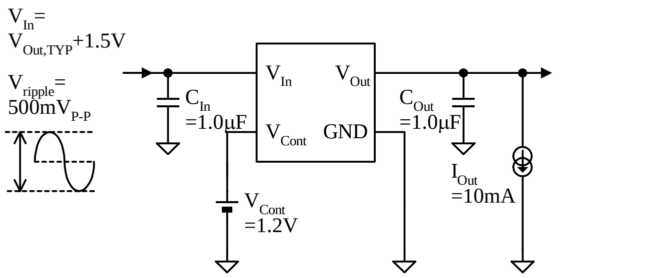
- ■ ΔV_{Out} vs V_{In}
- ■ V_{Drop} vs I_{Out}
- ■ V_{Out} vs I_{Out}
- ■ ΔV_{Out} vs I_{Out}
- ■ ΔV_{Out} vs T_a
- ■ V_{Drop} vs T_a
- ■ $I_{Out,MAX}$ vs T_a
- ■ I_{Cont} vs V_{Cont} , V_{Out} vs V_{Cont}
- ■ I_{Cont} vs T_a
- ■ V_{Cont} vs T_a
- ■ V_{Noise} vs V_{In}
- ■ V_{Noise} vs I_{Out}
- ■ V_{Noise} vs V_{Out}
- ■ V_{Noise} vs Frequency



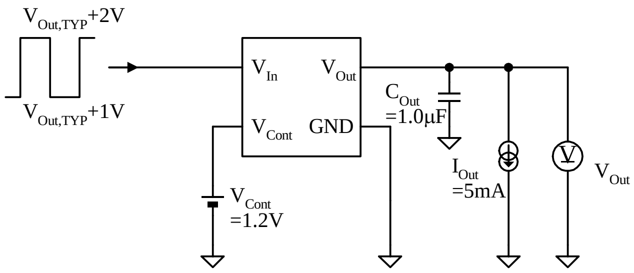
- ■ I_Q vs V_{In}
- ■ $I_{Standby}$ vs V_{In}
- ■ I_Q vs T_a



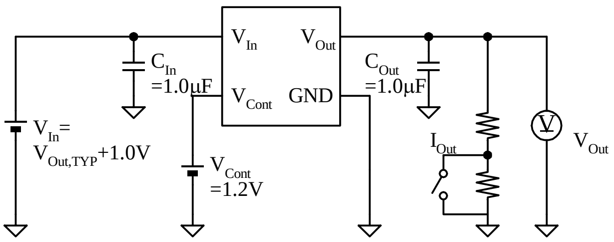
- ■ I_{GND} vs I_{Out}
- ■ I_{GND} vs T_a



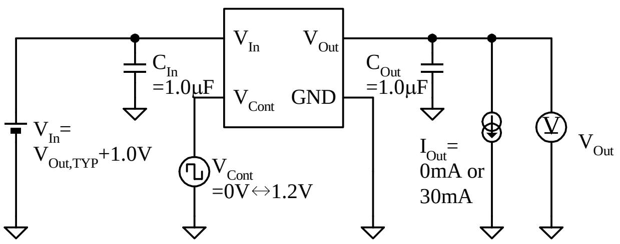
- RR vs V_{In}
- RR vs Frequency
- RR vs Frequency



- Line Transient



- Load Transient

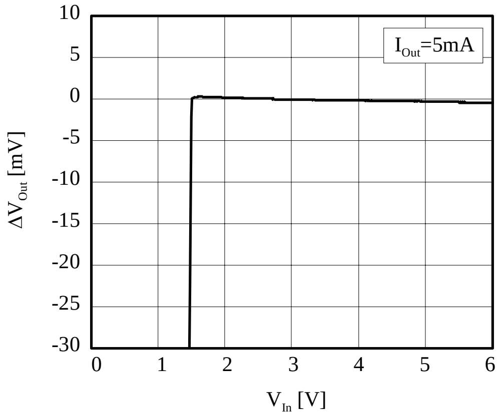


- On/Off Transient

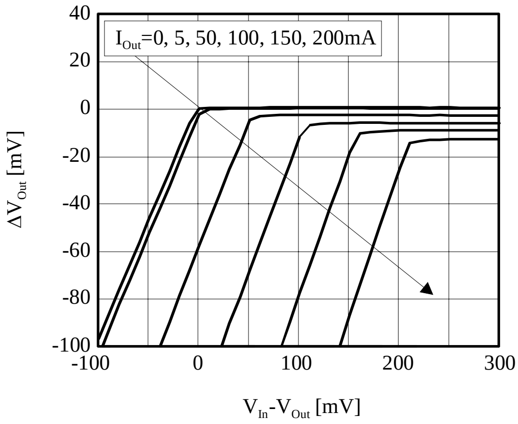
10. TYPICAL CHARACTERISTICS

10-1. DC CHARACTERISTICS

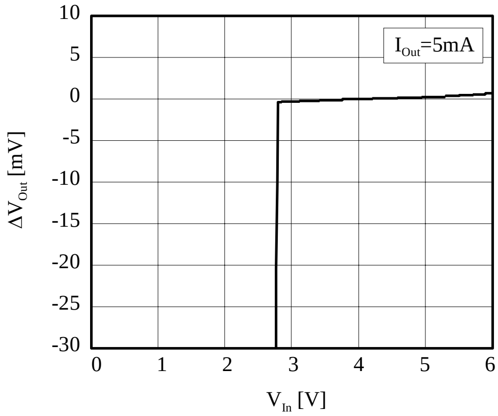
■ ΔV_{Out} vs V_{In} (TK63415AMF)



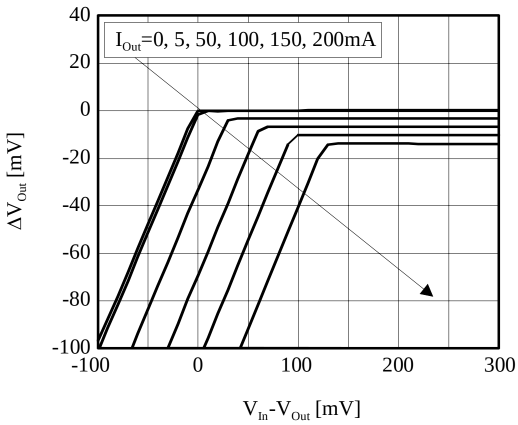
■ ΔV_{Out} vs V_{In} (TK63415AMF)



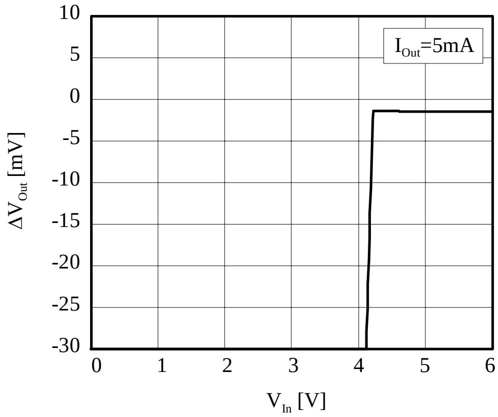
■ ΔV_{Out} vs V_{In} (TK63428AMF)



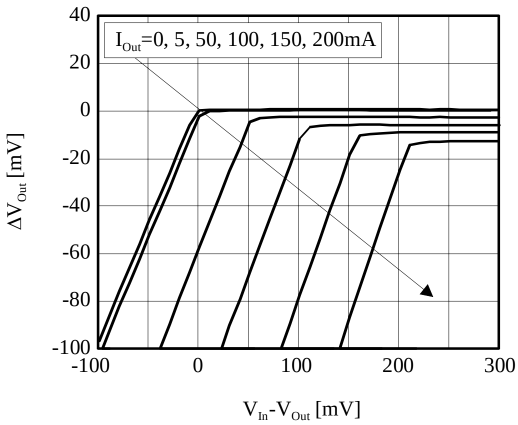
■ ΔV_{Out} vs V_{In} (TK63428AMF)



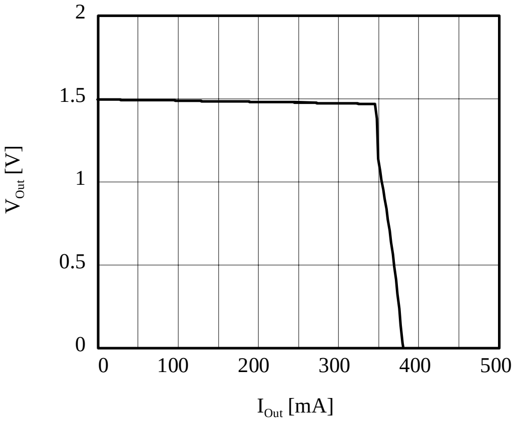
■ ΔV_{Out} vs V_{In} (TK63442AMF)



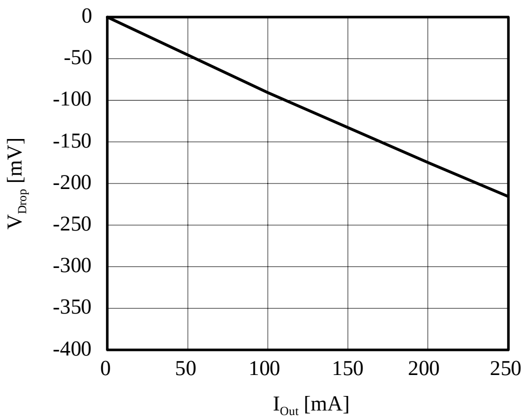
■ ΔV_{Out} vs V_{In} (TK63442AMF)



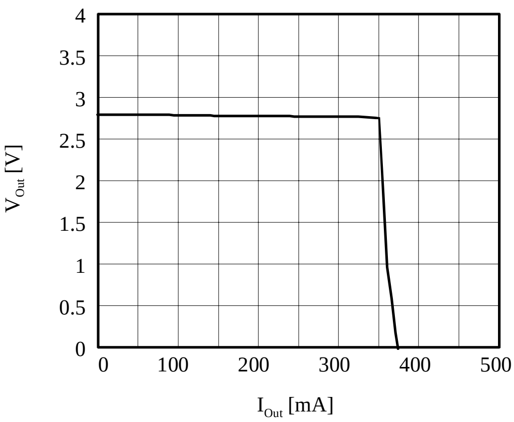
■ V_{Out} vs I_{Out} (TK63415AMF)



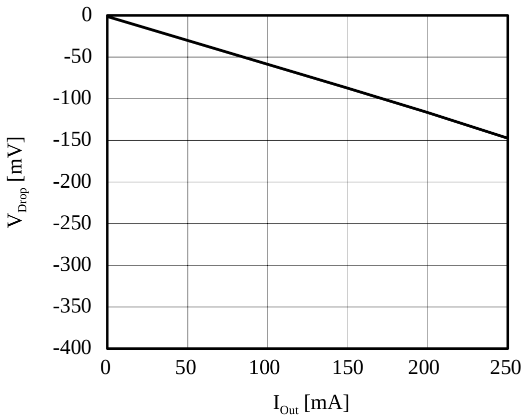
■ V_{Drop} vs I_{Out} (TK63428AMF)



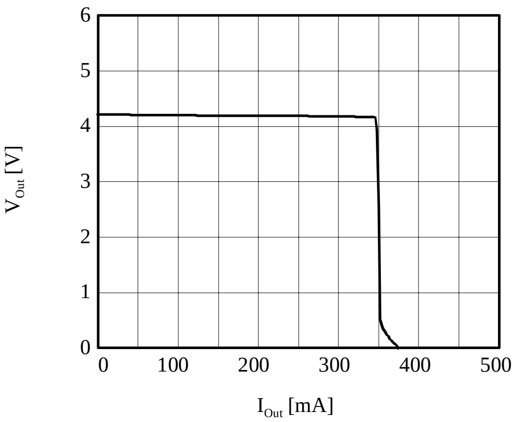
■ V_{Out} vs I_{Out} (TK63428AMF)



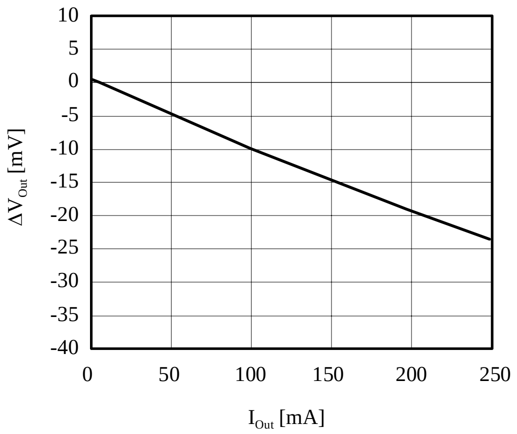
■ V_{Drop} vs I_{Out} (TK63442AMF)



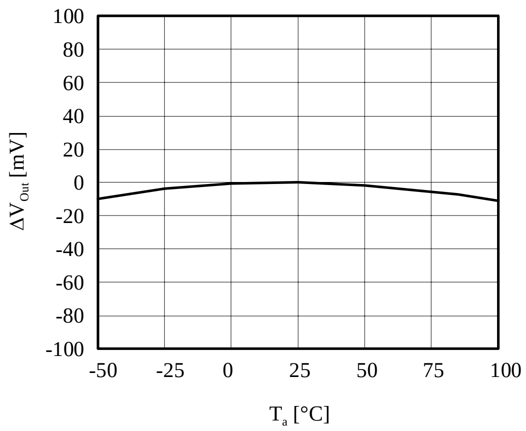
■ V_{Out} vs I_{Out} (TK63442AMF)



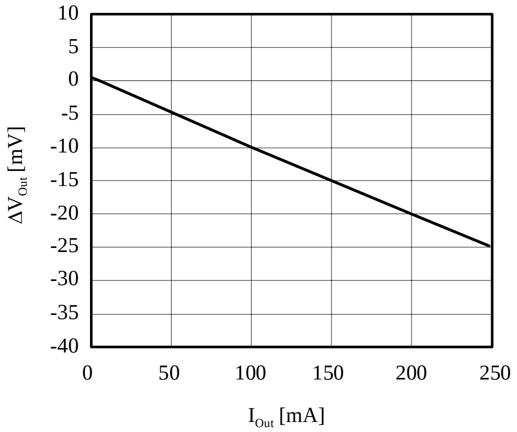
■ ΔV_{Out} vs I_{Out} (TK63415AMF)



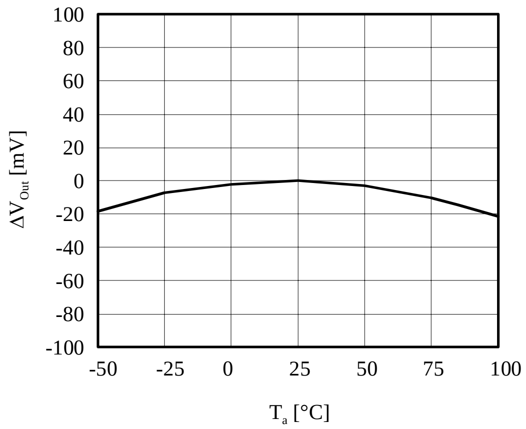
■ ΔV_{Out} vs T_a (TK63415AMF)



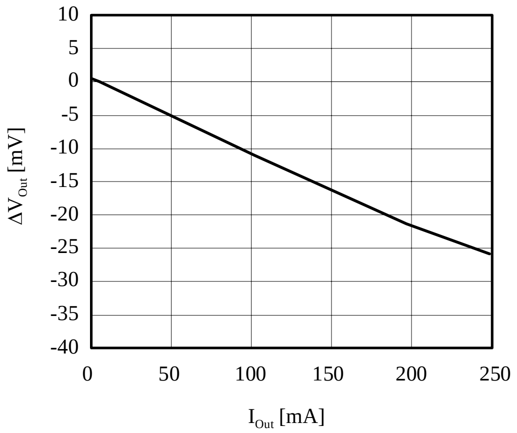
■ ΔV_{Out} vs I_{Out} (TK63428AMF)



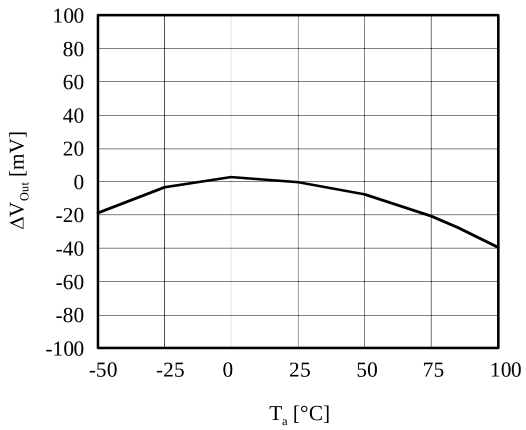
■ ΔV_{Out} vs T_a (TK63428AMF)



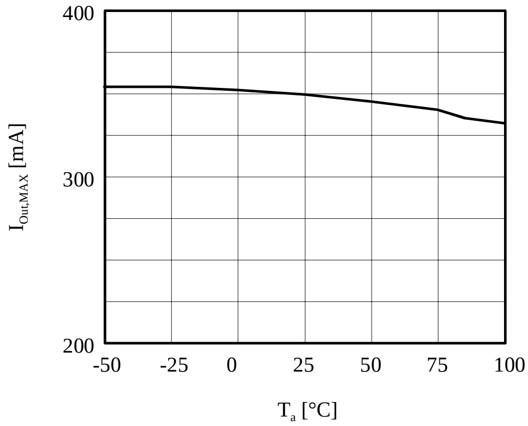
■ ΔV_{Out} vs I_{Out} (TK63442AMF)



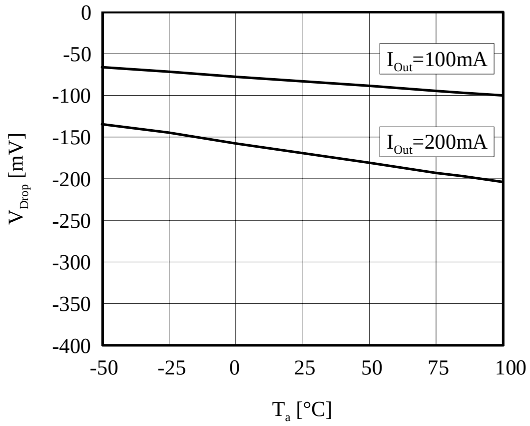
■ ΔV_{Out} vs T_a (TK63442AMF)



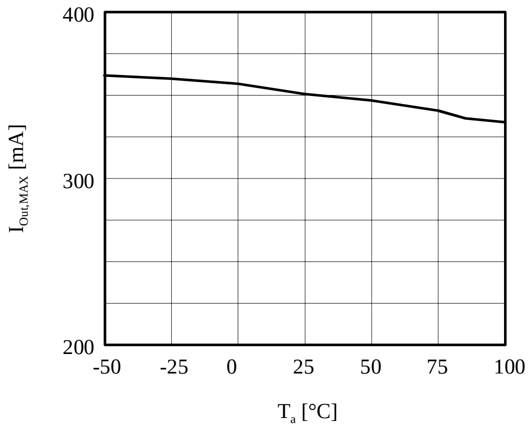
■ $I_{Out,MAX}$ vs T_a (TK63415AMF)



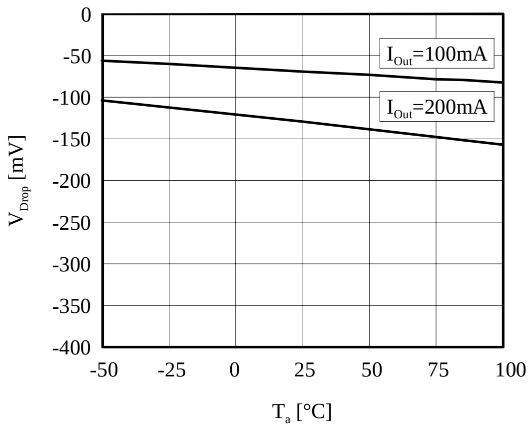
■ V_{Drop} vs T_a (TK63428AMF)



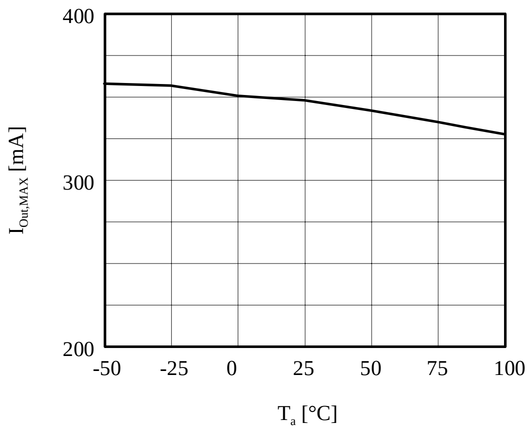
■ $I_{Out,MAX}$ vs T_a (TK63428AMF)



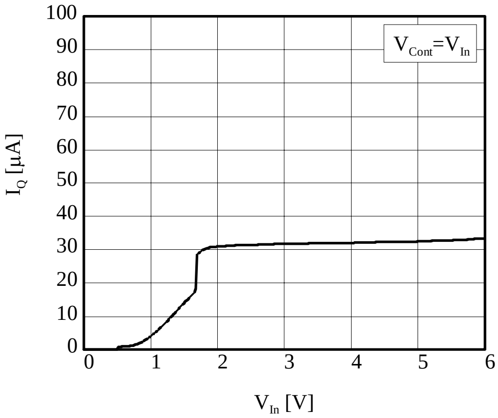
■ V_{Drop} vs T_a (TK63442AMF)



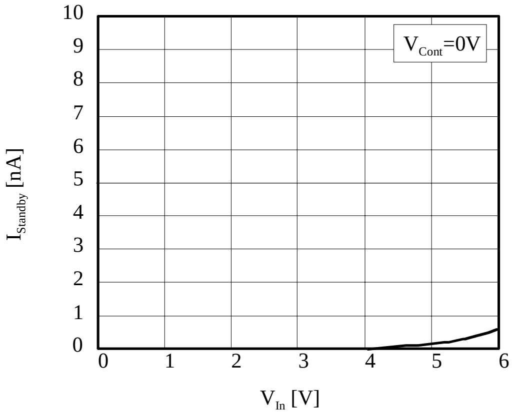
■ $I_{Out,MAX}$ vs T_a (TK63442AMF)



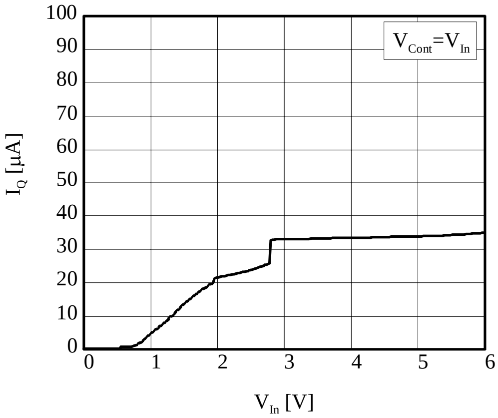
■ I_Q vs V_{In} (TK63415AMF)



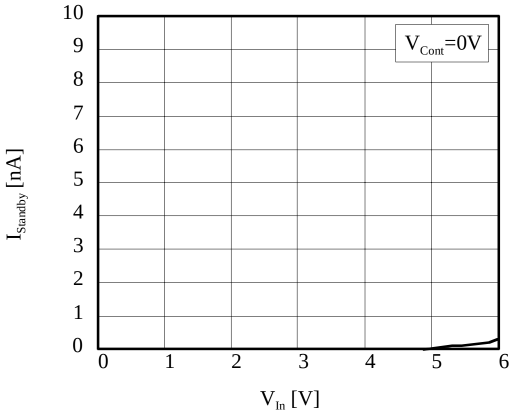
■ $I_{Standby}$ vs V_{In} (TK63415AMF)



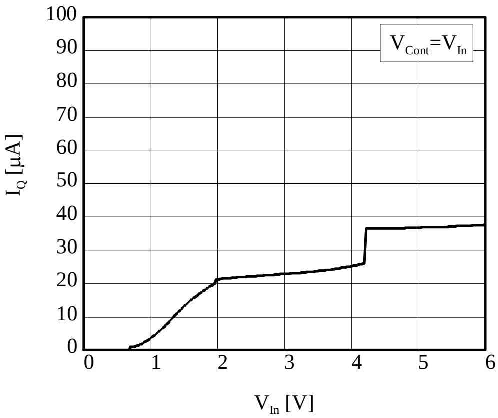
■ I_Q vs V_{In} (TK63428AMF)



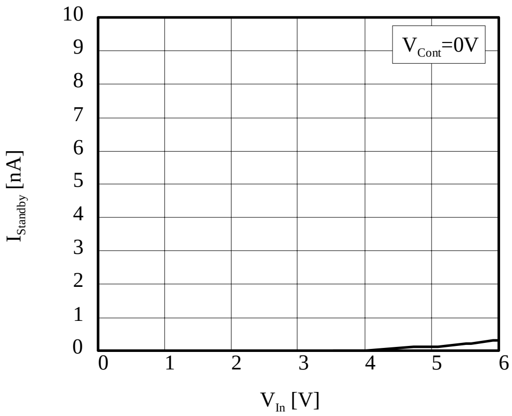
■ $I_{Standby}$ vs V_{In} (TK63428AMF)



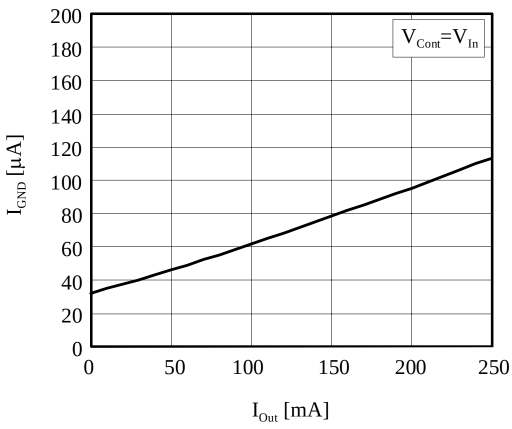
■ I_Q vs V_{In} (TK63442AMF)



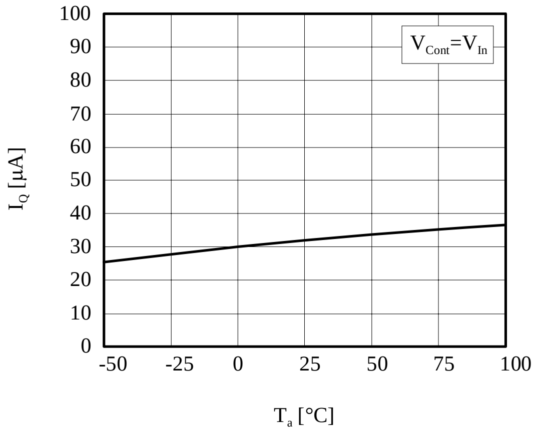
■ $I_{Standby}$ vs V_{In} (TK63442AMF)



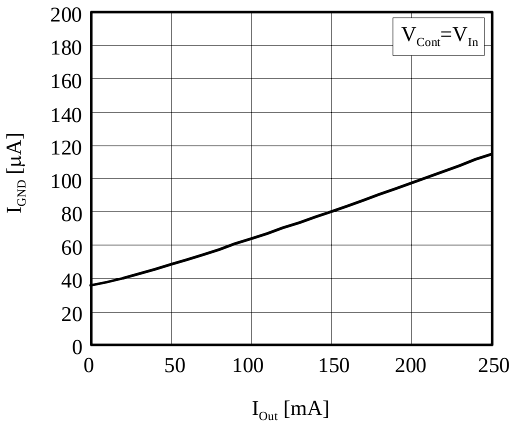
■ I_{GND} vs I_{Out} (TK63415AMF)



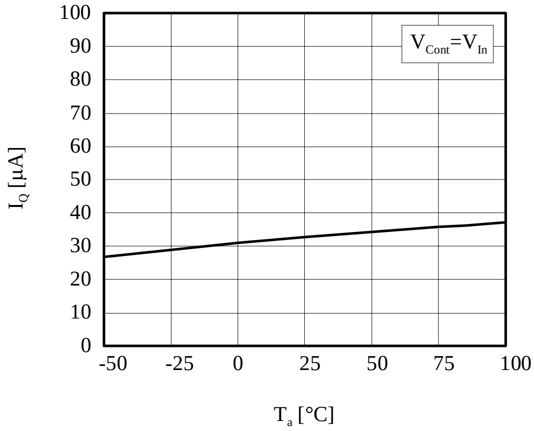
■ I_Q vs T_a (TK63415AMF)



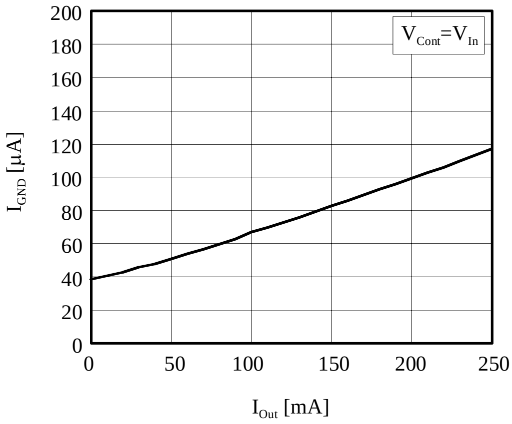
■ I_{GND} vs I_{Out} (TK63428AMF)



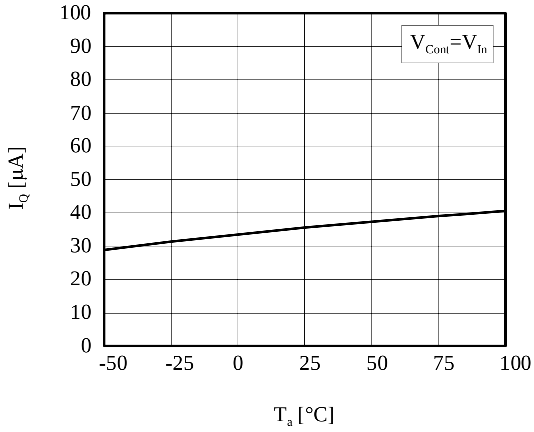
■ I_Q vs T_a (TK63428AMF)



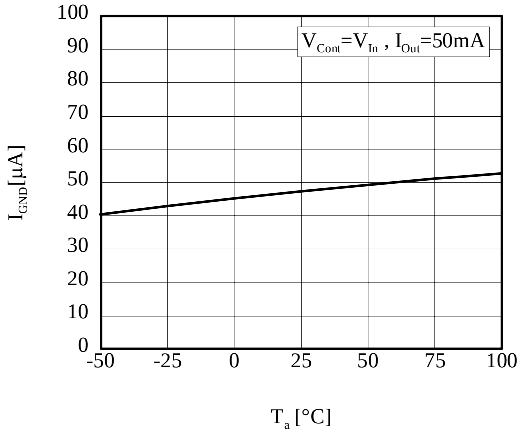
■ I_{GND} vs I_{Out} (TK63442AMF)



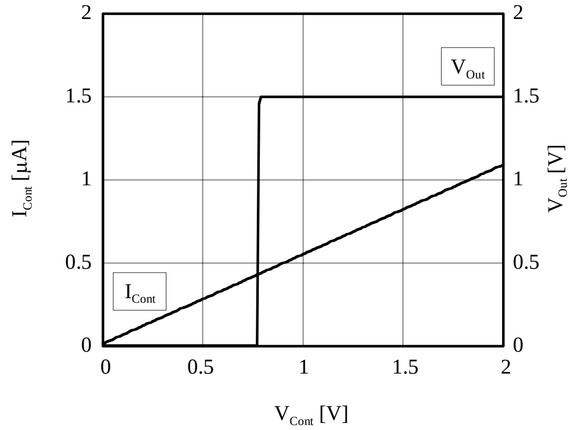
■ I_Q vs T_a (TK63442AMF)



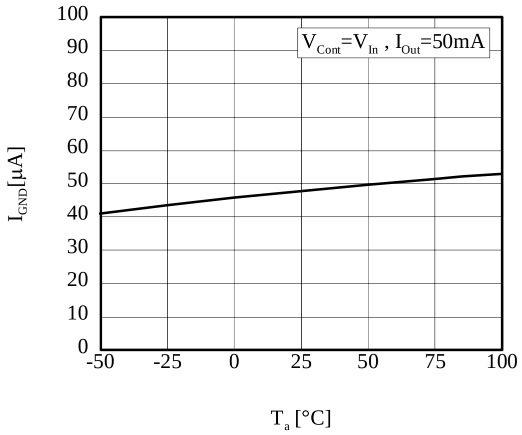
■ I_{GND} vs T_a (TK63415AMF)



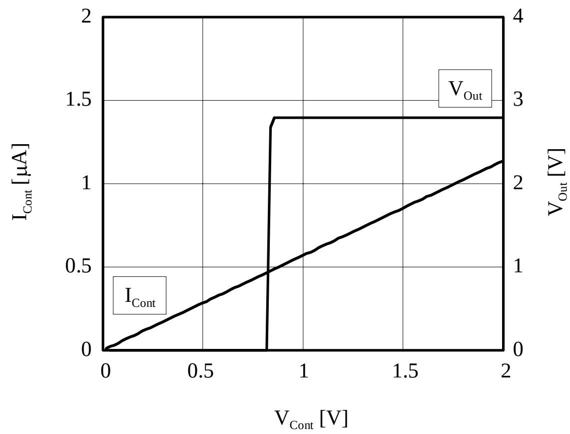
■ I_{Cont} vs V_{Cont}, V_{Out} vs V_{Cont} (TK63415AMF)



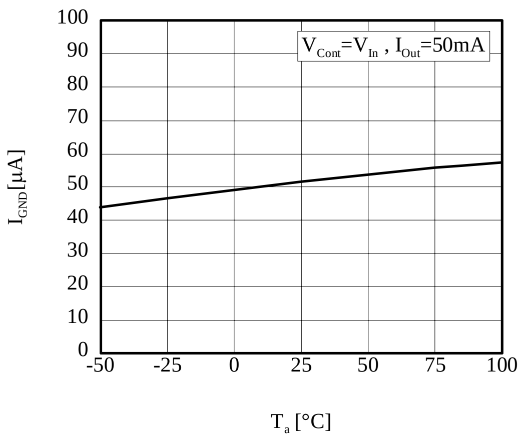
■ I_{GND} vs T_a (TK63428AMF)



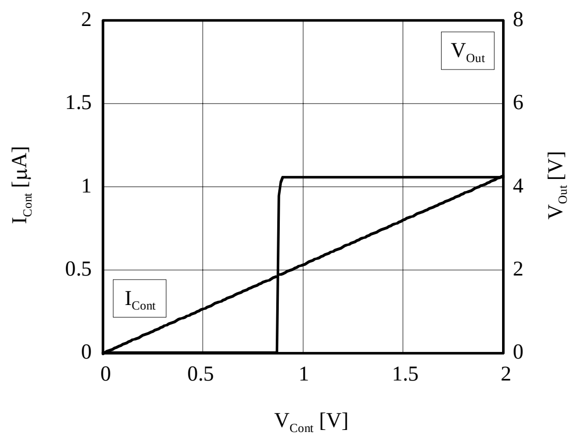
■ I_{Cont} vs V_{Cont}, V_{Out} vs V_{Cont} (TK63428AMF)



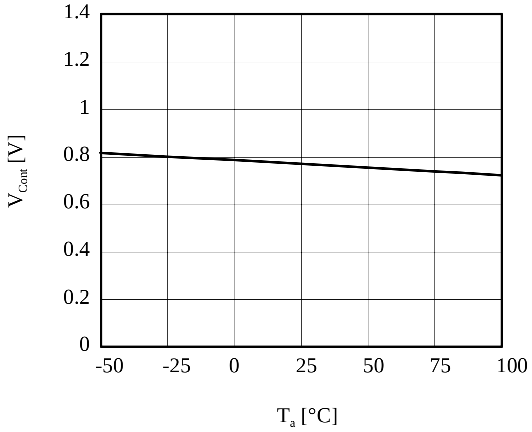
■ I_{GND} vs T_a (TK63442AMF)



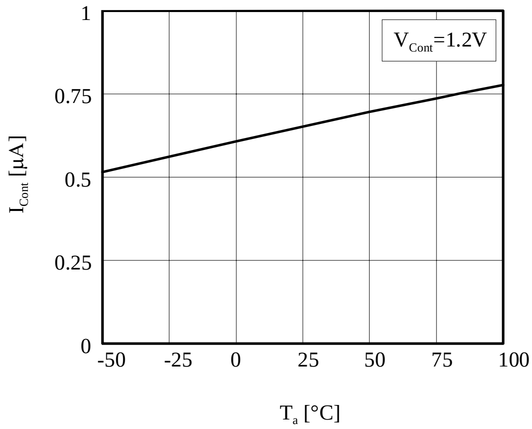
■ I_{Cont} vs V_{Cont}, V_{Out} vs V_{Cont} (TK63442AMF)



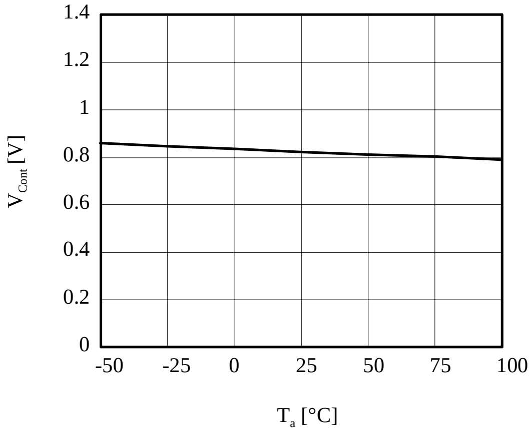
■ V_{Cont} vs T_a (TK63415AMF)



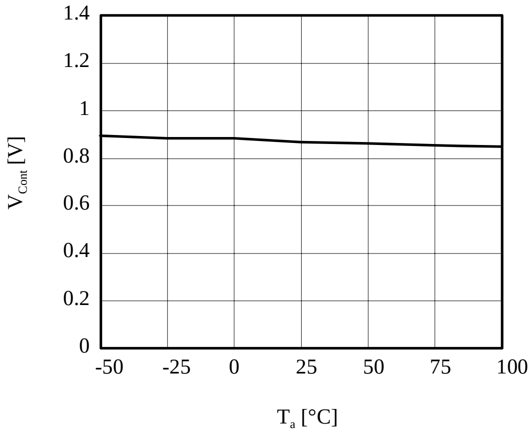
■ I_{Cont} vs T_a (TK634xxAMF)



■ V_{Cont} vs T_a (TK63428AMF)

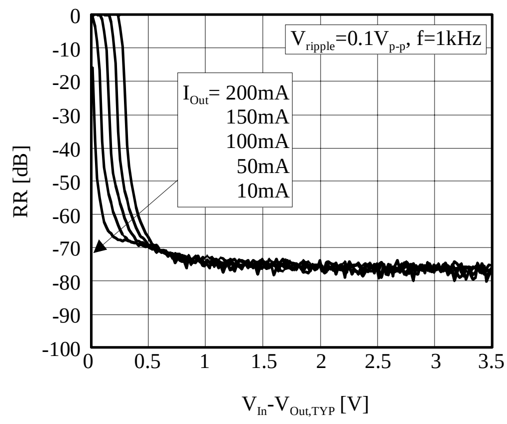


■ V_{Cont} vs T_a (TK63442AMF)

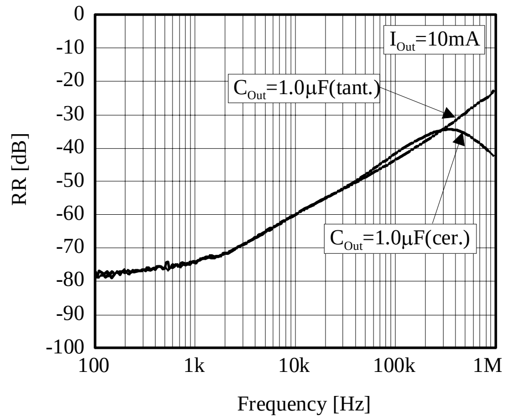


10-2. AC CHARACTERISTICS

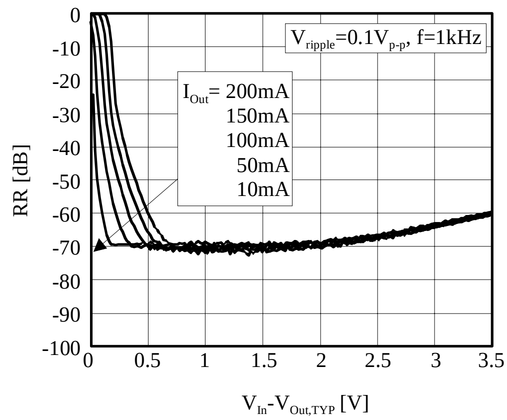
■ RR vs V_{In} (TK63415AMF)



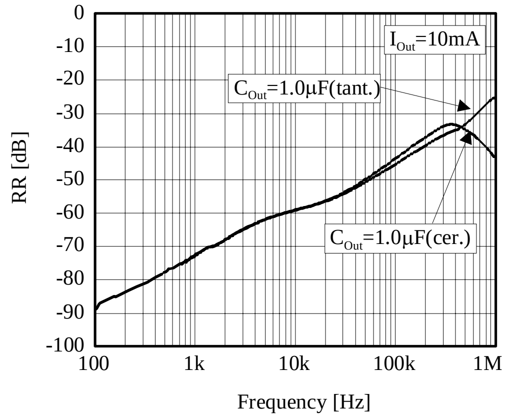
■ RR vs Frequency (TK63415AMF)



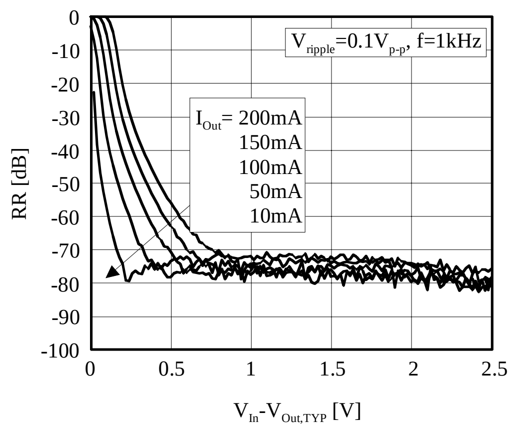
■ RR vs V_{In} (TK63428AMF)



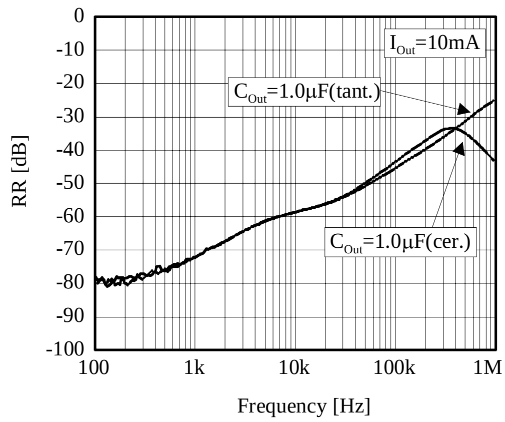
■ RR vs Frequency (TK63428AMF)



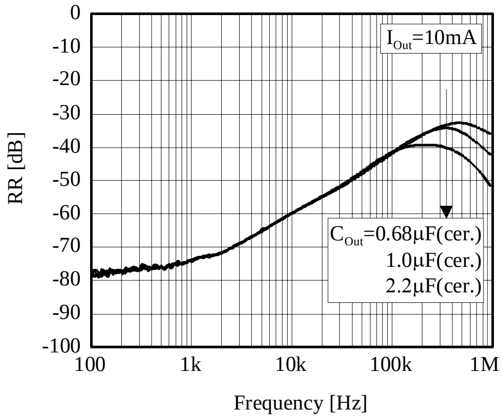
■ RR vs V_{In} (TK63442AMF)



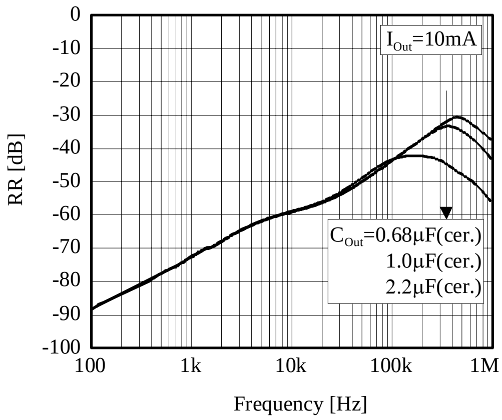
■ RR vs Frequency (TK63442AMF)



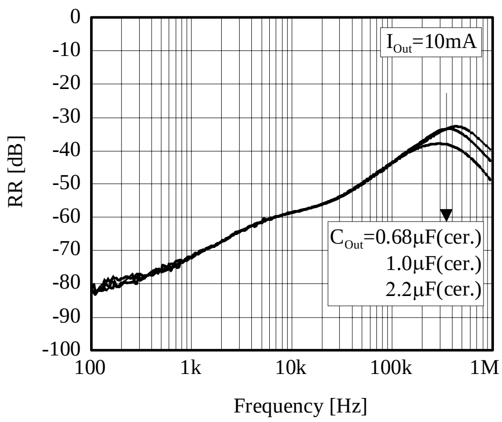
■ RR vs Frequency (TK63415AMF)



■ RR vs Frequency (TK63428AMF)

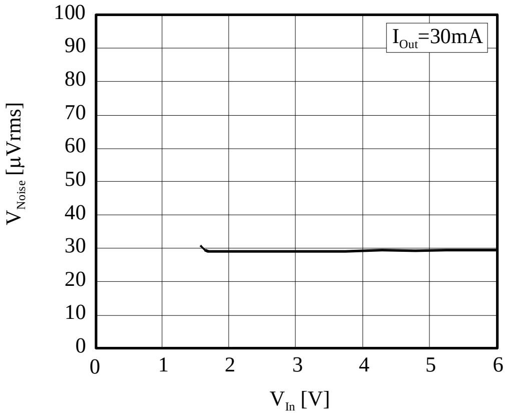


■ RR vs Frequency (TK63442AMF)

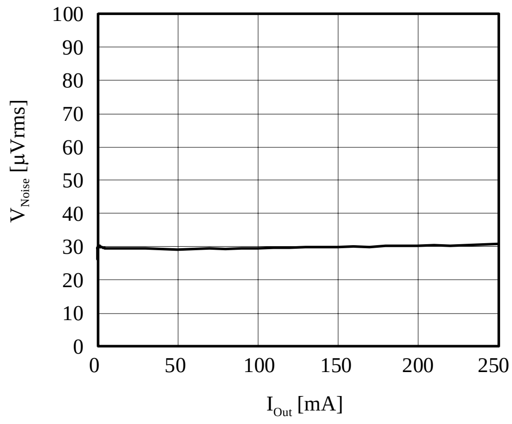


The ripple rejection (RR) characteristic depends on the characteristic and the capacitance value of the capacitor connected to the output side. The RR characteristic of 50kHz or more varies greatly with the capacitor on the output side and PCB pattern. If necessary, please confirm stability of your design.

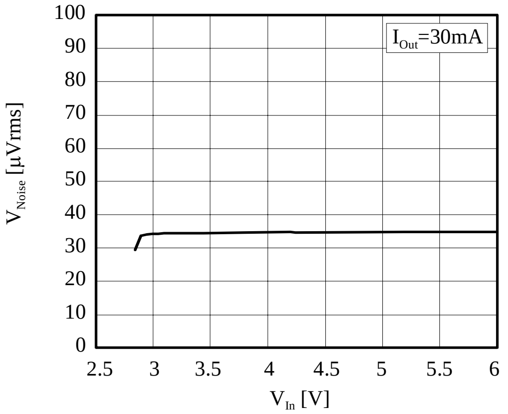
■ V_{Noise} vs V_{In} (TK63415AMF)



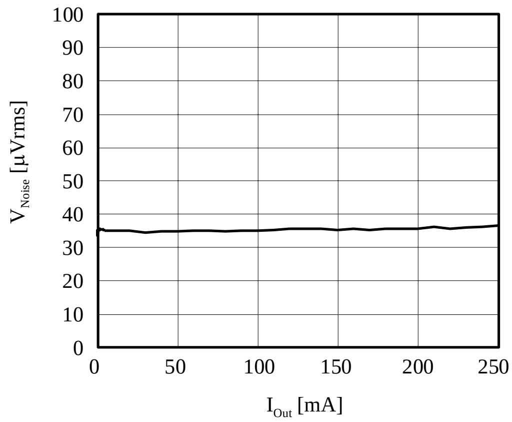
■ V_{Noise} vs I_{Out} (TK63415AMF)



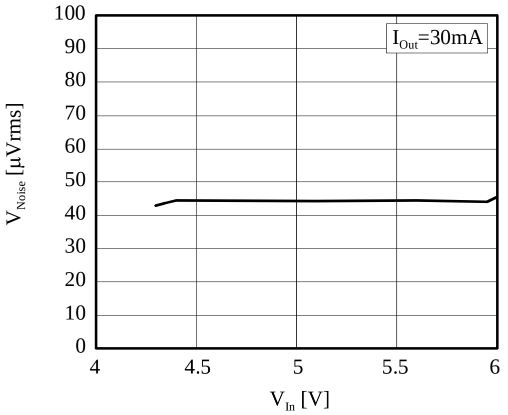
■ V_{Noise} vs V_{In} (TK63428AMF)



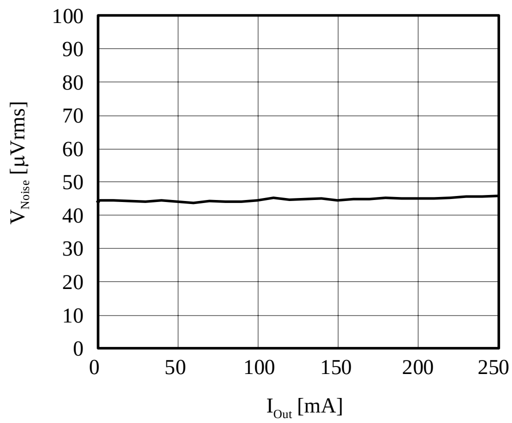
■ V_{Noise} vs I_{Out} (TK63428AMF)



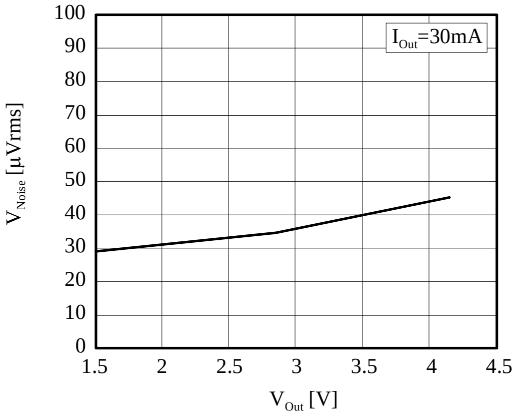
■ V_{Noise} vs V_{In} (TK63442AMF)



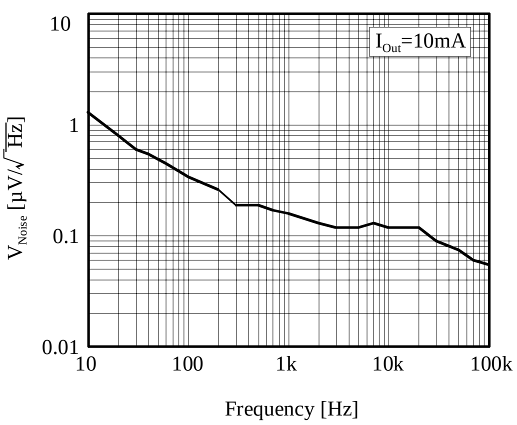
■ V_{Noise} vs I_{Out} (TK63442AMF)



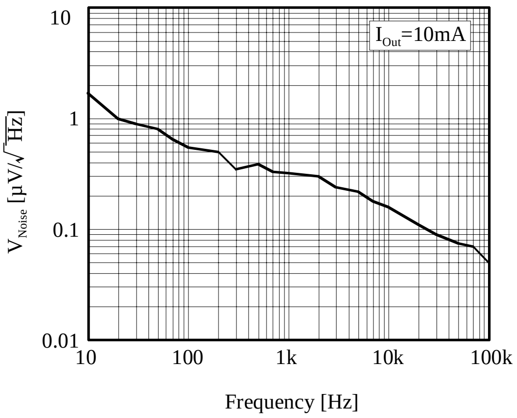
■ V_{Noise} vs V_{Out} (TK634xxAMF)



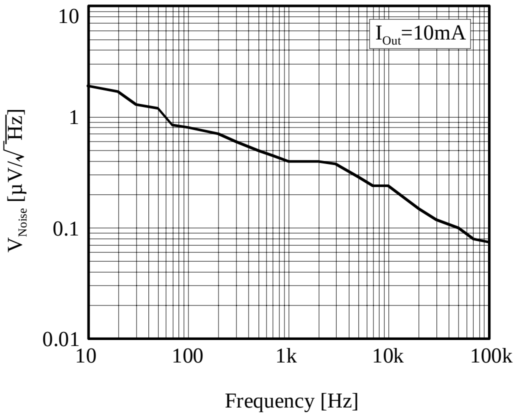
■ V_{Noise} vs Frequency (TK63415AMF)



■ V_{Noise} vs Frequency (TK63428AMF)

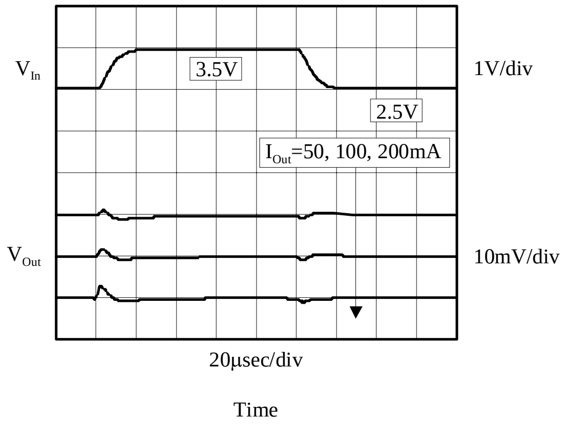


■ V_{Noise} vs Frequency (TK63442AMF)

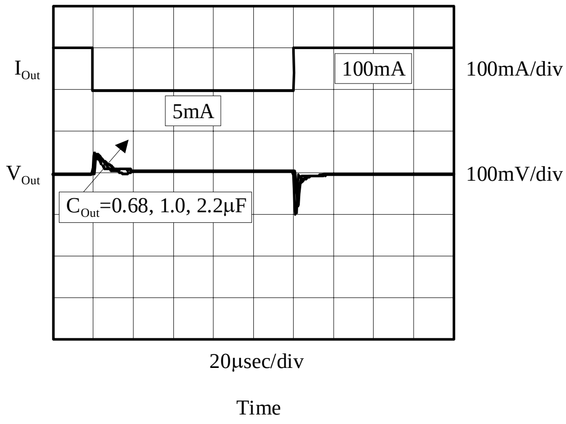


10-3. TRANSIENT CHARACTERISTICS

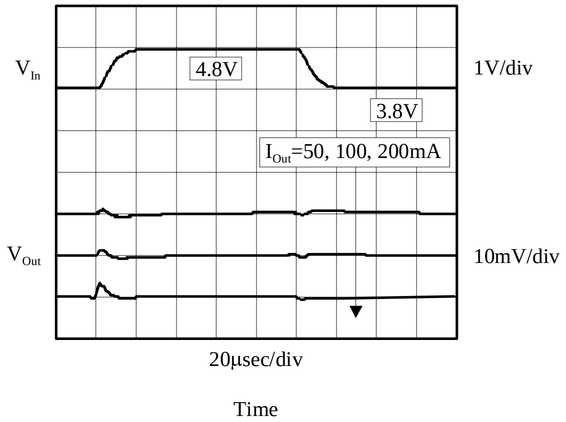
■ Line Transient (TK63415AMF)



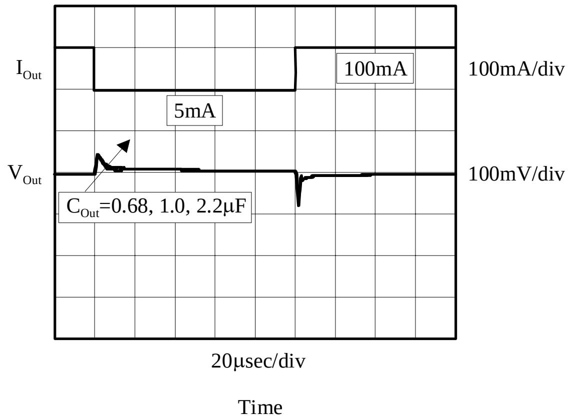
■ Load Transient (I_{Out}=5↔100mA) (TK63415AMF)



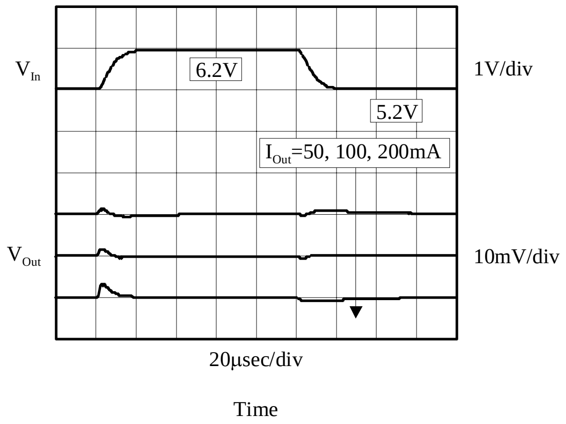
■ Line Transient (TK63428AMF)



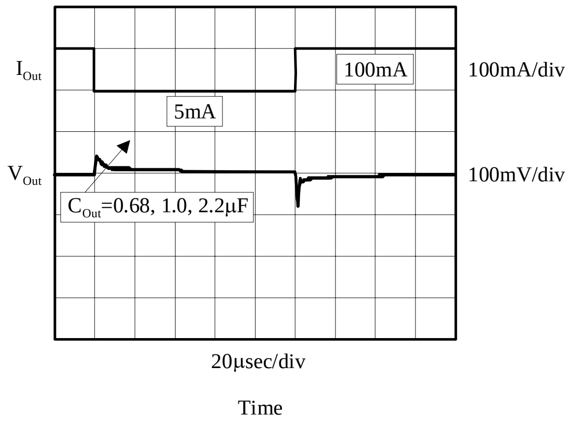
■ Load Transient (I_{Out}=5↔100mA) (TK63428AMF)



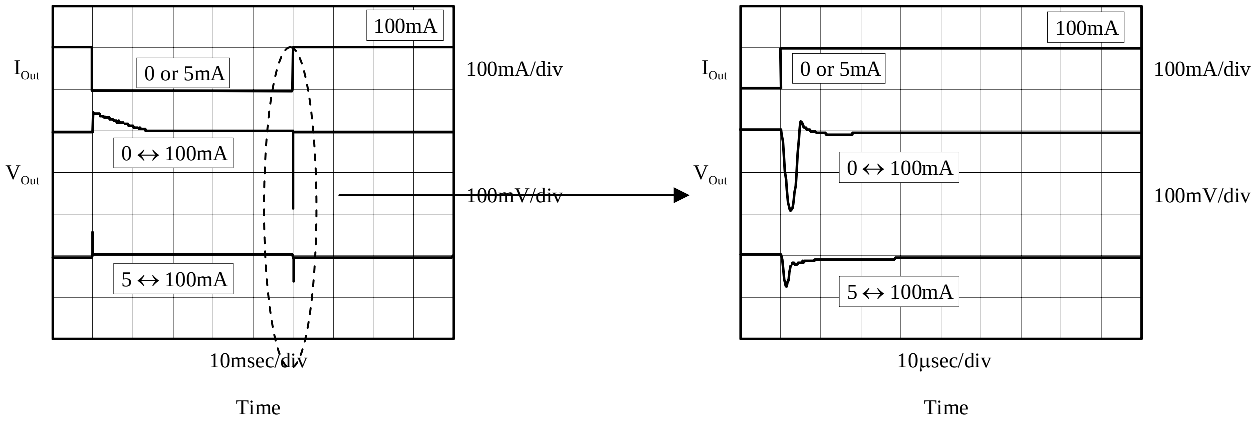
■ Line Transient (TK63442AMF)



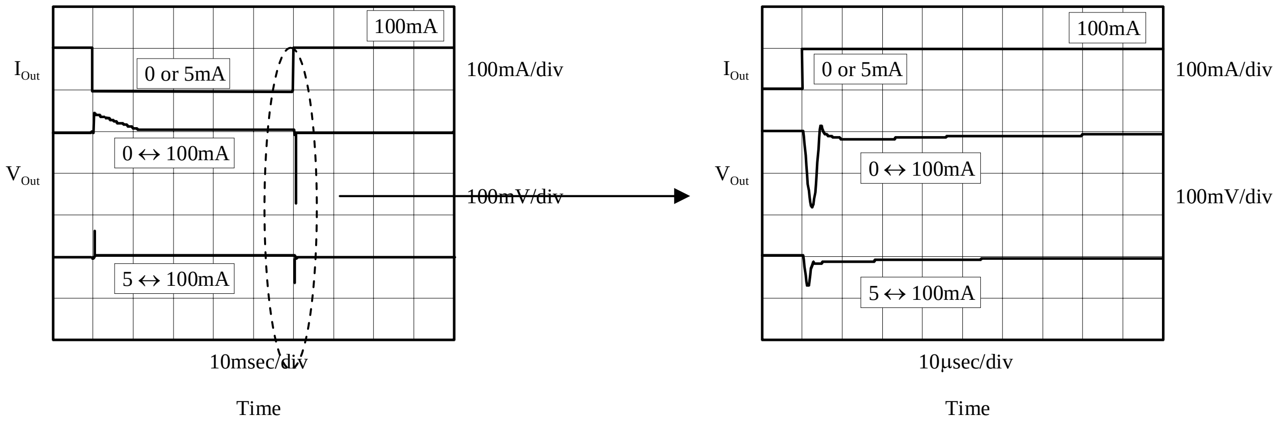
■ Load Transient (I_{Out}=5↔100mA) (TK63442AMF)



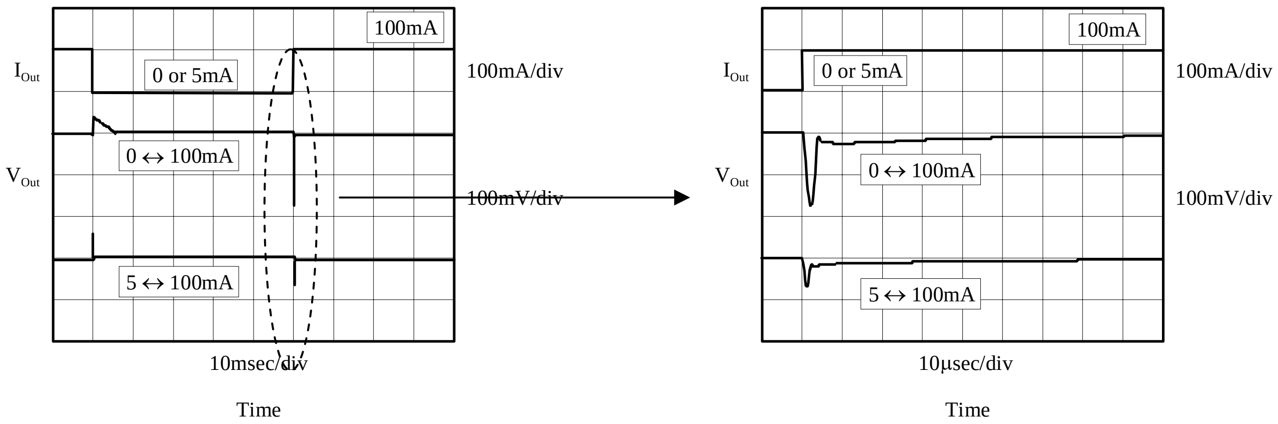
■ Load Transient (I_{Out}=0 or 5↔100mA) (TK63415AMF)



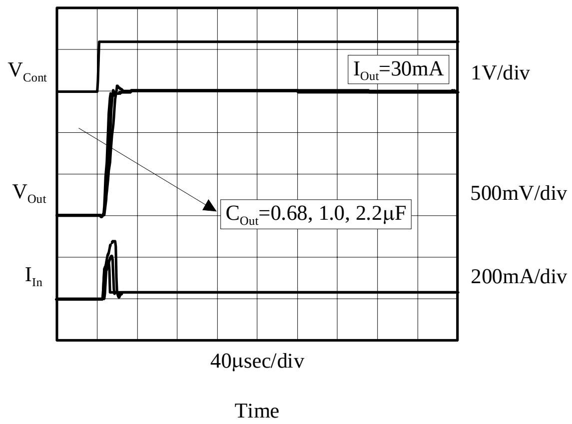
■ Load Transient (I_{Out}=0 or 5↔100mA) (TK63428AMF)



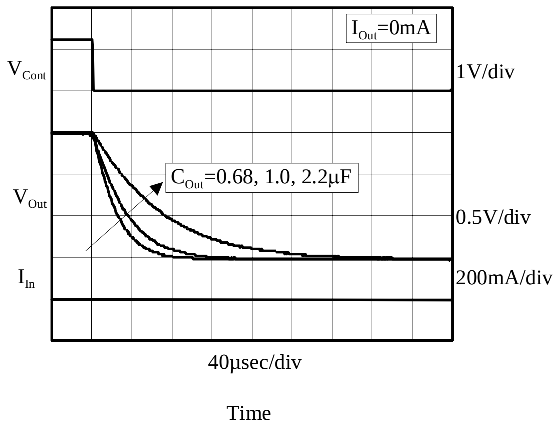
■ Load Transient (I_{Out}=0 or 5↔100mA) (TK63442AMF)



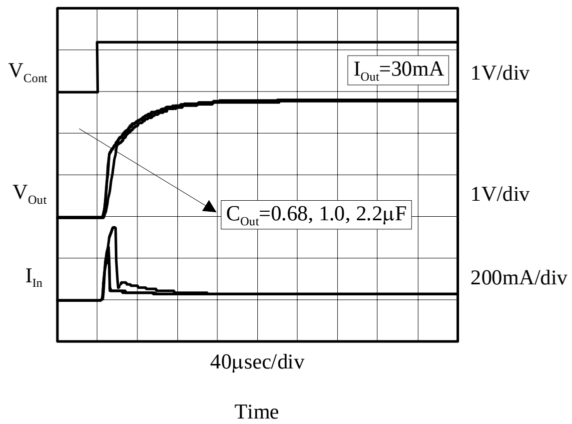
■ On/Off Transient (V_{Cont}=0→1.2V) (TK63415AMF)



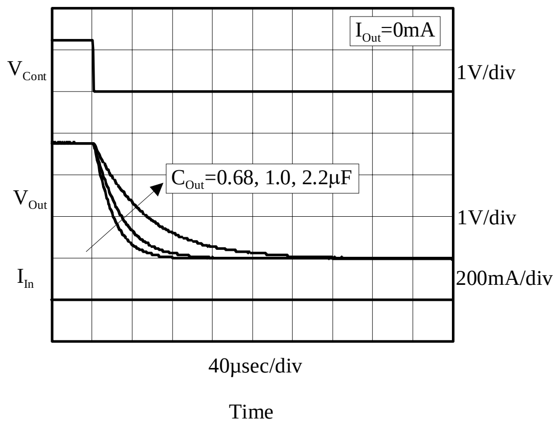
■ On/Off Transient (V_{Cont}=1.2→0V) (TK63415AMF)



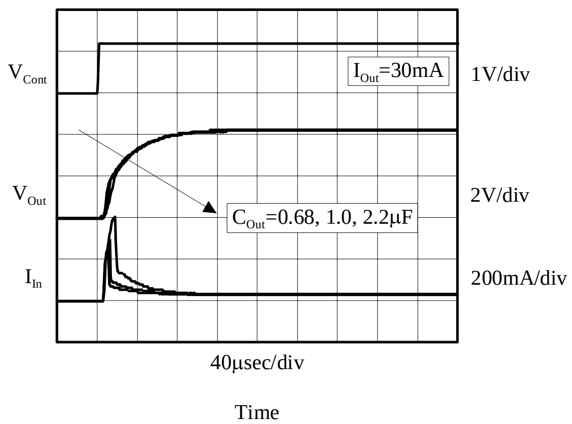
■ On/Off Transient (V_{Cont}=0→1.2V) (TK63428AMF)



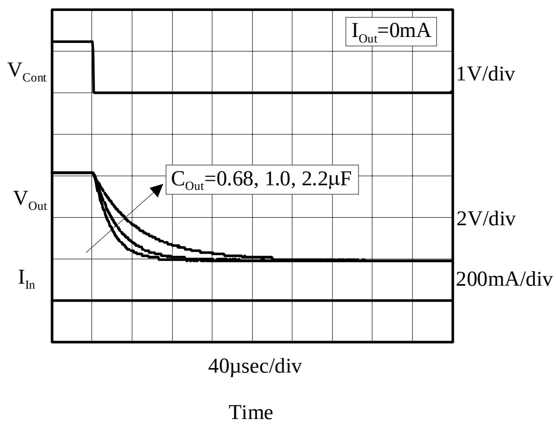
■ On/Off Transient (V_{Cont}=1.2→0V) (TK63428AMF)



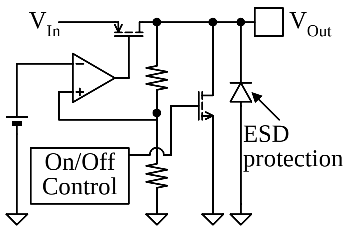
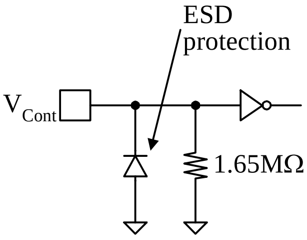
■ On/Off Transient (V_{Cont}=0→1.2V) (TK63442AMF)



■ On/Off Transient (V_{Cont}=1.2→0V) (TK63442AMF)



11. PIN DESCRIPTION

Pin No.	Pin Description	Internal Equivalent Circuit	Description
1	V_{Out}		Output Terminal
2	GND		GND Terminal
3	V_{Cont}		Control Terminal V_{Cont} > 1.2V : On V_{Cont} < 0.2V : Off The pull-down resistor (about 1.65MΩ) is built-in.
4	V_{In}		Input Terminal

12. APPLICATIONS INFORMATION

12-1. Stability

Linear regulators require input and output capacitors in order to maintain the regulator's loop stability. If 0.68µF capacitors are connected to the input side and the output side, the IC provides stable operation. However, it is recommended to use as large a value capacitor as is practical. The output noise and the ripple noise decrease as the value of the capacitor increases.

A recommended value of the application is as follows.

$C_{In} \geq 0.68\mu F$, $C_{Out} \geq 0.68\mu F$

It is not possible to determine this indiscriminately. Please confirm the stability in your design.

Fig12-1: Capacitor in the application

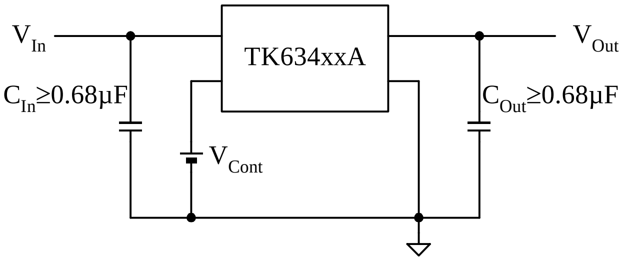


Fig12-2: Output Current vs Stable Operation Area

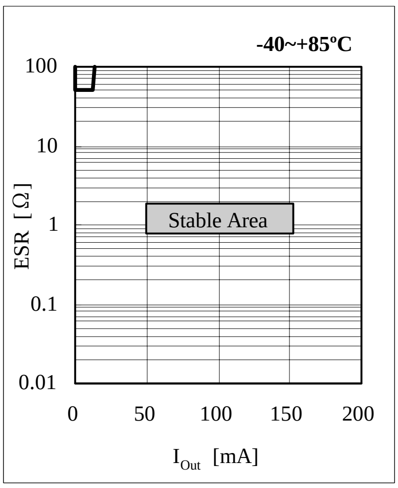
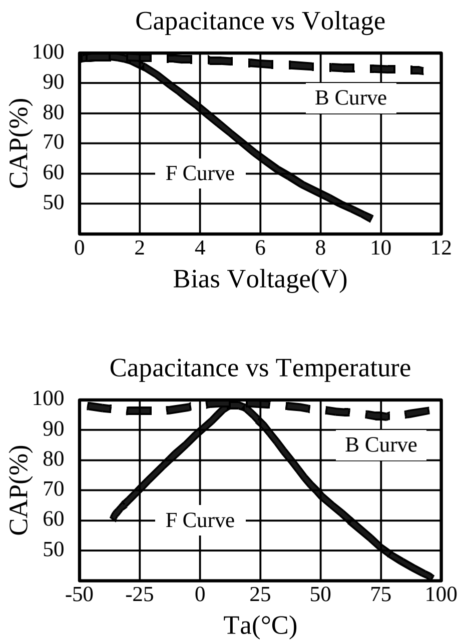


Fig.12-2 shows the stable operation area of output current and the equivalent series resistance (ESR) with a ceramic capacitor of 0.68µF. ESR of the output capacitor must be in the stable operation area. Please select the best output capacitor according to the voltage and current used. The stability of the regulator improves as the value of the output side capacitor increases (the stable operation area extends.) Please use as large a value capacitor as is practical.

For evaluation

Kyocera : CM05X5R105K10AB

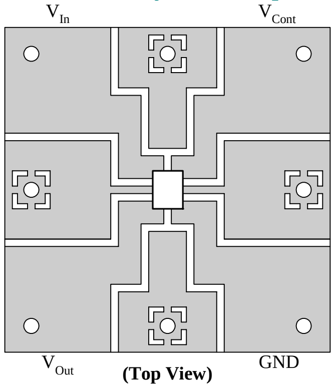
Fig12-3: ex. Ceramic Capacitance vs Voltage, Temperature



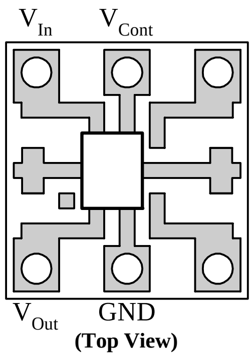
Generally, a ceramic capacitor has both a temperature characteristic and a voltage characteristic. Please consider both characteristics when selecting the part. The B curves are the recommended characteristics.

12-2. Layout

図12-4: Layout example



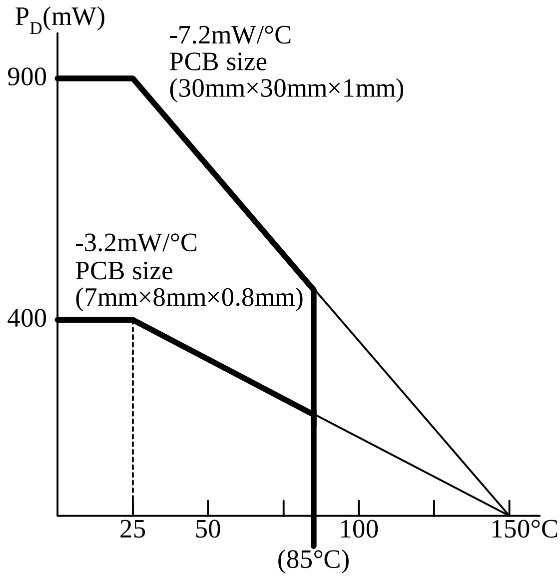
PCB Material: Glass epoxy
Size: 30mm×30mm×1mm



PCB Material: Glass epoxy
Size: 7mm×8mm×0.8mm

Please do derating with 3.2mW/°C(PCB size: 7mm×8mm×0.8mm), 7.2mW/°C(PCB size: 30mm×30mm×1mm), at P_D=400mW(PCB size: 7mm×8mm×0.8m), 900mW(PCB size: 30mm×30mm×1mm), and 25°C or more.

Fig12-5: Derating Curve



The package loss is limited at the temperature that the internal temperature sensor works (about 150°C). Therefore, the package loss is assumed to be an internal limitation. There is no heat radiation characteristic of the package unit assumed because of its small size. Heat is carried away from the device by being mounted on the PCB. This value is directly effected by the material and the copper pattern etc. of the PCB. The losses are approximately 400mW. Enduring these losses becomes possible in a lot of applications operating at 25°C.

The overheating protection circuit operates when the junction temperature reaches 150°C (this happens when the regulator is dissipating excessive power, outside temperature is high, or heat radiation is bad). The output current and the output voltage will drop when the protection circuit operates. However, operation begins again as soon as the output voltage drops and the temperature of the chip decreases.

How to determine the thermal resistance when mounted on PCB

The thermal resistance when mounted is expressed as follows:

$T_j = \theta_{ja} \times P_D + T_a$

T_j of IC is set around 150°C. P_D is the value when the thermal sensor is activated.

If the ambient temperature is 25°C, then:

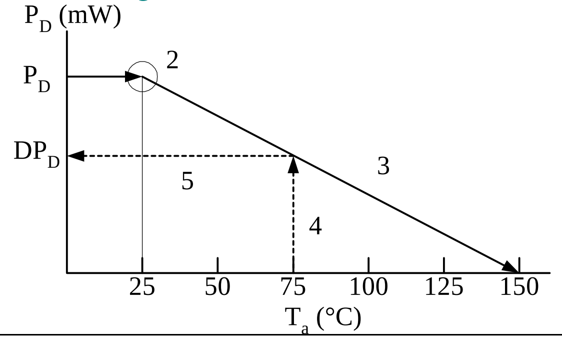
$150 = \theta_{ja} \times P_D + 25$

$\theta_{ja} = 125/P_D$ (°C /mW)

P_D is easily calculated.

A simple way to determine P_D is to calculate $V_{In}\times I_{In}$ when the output side is shorted. Input current gradually falls as output voltage rises after working thermal shutdown. You should use the value when thermal equilibrium is reached.

Fig12-6: How to determine DP_D



Procedure (When mounted on PCB.)

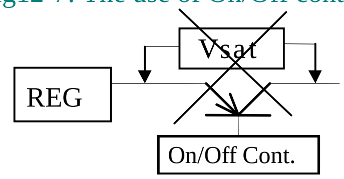
1. Find P_D ($V_{In}\times I_{In}$ when the output side is short-circuited).
2. Plot P_D against 25°C.
3. Connect P_D to the point corresponding to the 150°C with a straight line.
4. In design, take a vertical line from the maximum operating temperature (e.g., 75°C) to the derating curve.
5. Read off the value of P_D against the point at which the vertical line intersects the derating curve. This is taken as the maximum power dissipation DPd.
6. $DP_D \div (V_{In,MAX} - V_{Out}) = I_{Out}$ (at 75°C)

The maximum output current at the highest operating temperature will be $I_{Out} \cong DP_D \div (V_{In,MAX} - V_{Out})$.
Please use the device at low temperature with better radiation. The lower temperature provides better quality.

12-3. On/Off Control

It is recommended to turn the regulator Off when the circuit following the regulator is not operating. A design with little electric power loss can be implemented. We recommend the use of the On/Off control of the regulator without using a high side switch to provide an output from the regulator. A highly accurate output voltage with low voltage drop is obtained.
Because the control current is small, it is possible to control it directly by CMOS logic.

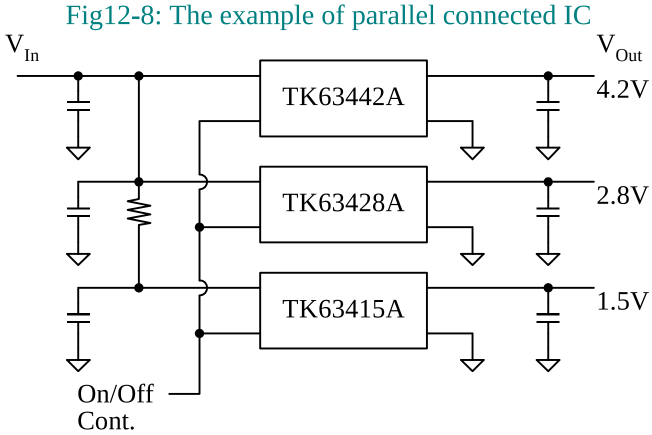
Fig12-7: The use of On/Off control



Control Terminal Voltage ((V_{Cont})	On/Off State
$V_{Cont} > 1.2V$	On
$V_{Cont} < 0.2V$	Off

Parallel Connected On/Off Control

Fig12-8: The example of parallel connected IC



The above figure is multiple regulators being controlled by a single On/Off control signal. There is concern of overheating, because the power loss of the low voltage side IC (TK63415A) is large. The series resistor (R) is put in the input line of the low output voltage regulator in order to prevent over-dissipation. The voltage dropped across the resistor reduces the large input-to-output voltage across the regulator, reducing the power dissipation in the device. When the thermal sensor works, a decrease of the output voltage, oscillation, etc. may be observed.

12-4. Definition of term

Characteristics

♦ Output Voltage (V_{Out})
The output voltage is specified with V_{In}=(V_{OutTYP}+1V) and I_{Out}=5mA.

♦ Maximum Output Current ($I_{Out, MAX}$)
The rated output current is specified under the condition where the output voltage drops to 90% of the value specified with I_{Out}=5mA. The input voltage is set to V_{OutTYP}+1V and the current is pulsed to minimize temperature effect.

♦ Dropout Voltage (V_{Drop})
The dropout voltage is the difference between the input voltage and the output voltage at which point the regulator starts to fall out of regulation. Below this value, the output voltage will fall as the input voltage is reduced. It is dependent upon the output voltage, the load current, and the junction temperature.

♦ Line Regulation (LinReg)
Line regulation is the ability of the regulator to maintain a constant output voltage as the input voltage changes. The line regulation is specified as the input voltage is changed from V_{In}=$V_{Out,TYP}$+1V to V_{In}=6V. It is a pulse measurement to minimize temperature effect.

♦ Load Regulation (LoaReg)
Load regulation is the ability of the regulator to maintain a constant output voltage as the load current changes. It is a pulsed measurement to minimize temperature effects with the input voltage set to V_{In}=$V_{Out,TYP}$+1V. The load regulation is specified under an output current step condition of 1mA to 50mA.

♦ Ripple Rejection (RR)
Ripple rejection is the ability of the regulator to attenuate the ripple content of the input voltage at the output. It is specified with 500mV$_{P\text{-}P}$, 1kHz super-imposed on the input voltage, where V_{In}=$V_{Out,TYP}$+1.5V. Ripple rejection is the ratio of the ripple content of the output vs. input and is expressed in dB.

♦Standby Current ($I_{Standby}$)
Standby current is the current which flows into the regulator when the output is turned off by the control function (V_{Cont}=0V).

Protections

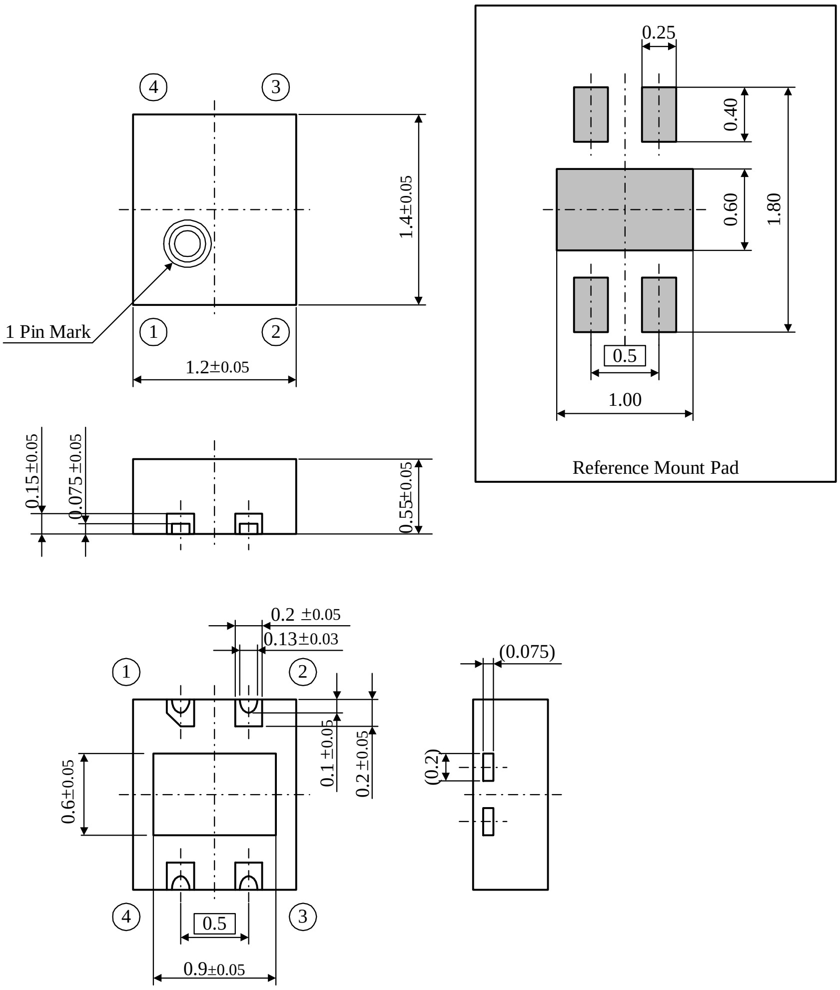
♦ Over Current Sensor
The over current sensor protects the device when there is excessive output current. It also protects the device if the output is accidentally connected to ground.

♦ Thermal Sensor
The thermal sensor protects the device in case the junction temperature exceeds the safe value (T_j=150°C). This temperature rise can be caused by external heat, excessive power dissipation caused by large input to output voltage drops, or excessive output current. The regulator will shut off when the temperature exceeds the safe value. As the junction temperatures decrease, the regulator will begin to operate again. Under sustained fault conditions, the regulator output will oscillate as the device turns off then resets. Damage may occur to the device under extreme fault.
Please prevent the loss of the regulator when this protection operates, by reducing the input voltage or providing better heat efficiency.

♦ ESD
MM : 200pF 0Ω 150V or more
HBM : 100pF 1.5kΩ 2000V or more

13. PACKAGE OUTLINE

■ 4-Lead-Small Outline Non-Leaded Package with Heat Sink : HSON1214-4



Unit : mm

Package Structure and Others

Package Material	:	Epoxy Resin
Terminal Material	:	Copper Alloy
Terminal Finish	:	Ni/Pd/Au

Caution in Printed Circuit Board Layout

In addition to the normal pins, this plastic package has exposed metal tabs.
This tab is electrically connected to the GND of internal chip.
Avoid electrical contact with this tab from external print traces, adjacent components other than GND, etc.
This tab is recommended to be solder-mounted so as to enhance heat release.

Marking

Part Number	Marking Code	Part Number	Marking Code	Part Number	Marking Code
TK63415AMF	D15	TK63401AMF	D01	TK63435AMF	D35
TK63418AMF	D18	TK63429AMF	D29	TK63440AMF	D40
TK63425AMF	D25	TK63430AMF	D30		
TK63426AMF	D26	TK63431AMF	D31		
TK63427AMF	D27	TK63432AMF	D32		
TK63428AMF	D28	TK63433AMF	D33		

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■ None of the ozone depleting substances(ODS) under the Montreal Protocol are used in our manufacturing process.

15. OFFICES

If you need more information on this product and other TOKO products, please contact us.

■ TOKO Inc. Headquarters
1-17, Higashi-yukigaya 2-chome, Ohta-ku, Tokyo,
145-8585, Japan
TEL: +81.3.3727.1161
FAX: +81.3.3727.1176 or +81.3.3727.1169
Web site: http://www.toko.co.jp/

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