- Designed for Complementary Use with BD895, BD897, BD899 and BD901
- 70 W at 25°C Case Temperature
- 8 A Continuous Collector Current
- Minimum h<sub>FE</sub> of 750 at 3V, 3A

# 

Pin 2 is in electrical contact with the mounting base.

MDTRACA

#### absolute maximum ratings at 25°C case temperature (unless otherwise noted)

RATING	SYMBOL	VALUE	UNIT		
	BD896		-45		
Collector-base voltage (I <sub>E</sub> = 0)	BD898		-60	v	
	BD900	V <sub>CBO</sub>	-80	\ \	
	BD902		-100		
	BD896		-45		
Calleston amittani valtara (I O)	BD898		-60	v	
Collector-emitter voltage (I <sub>B</sub> = 0)	BD900	V <sub>CEO</sub>	-80		
	BD902		-100		
Emitter-base voltage	V <sub>EBO</sub>	-5	٧		
Continuous collector current	Ic	-8	Α		
Continuous base current	I <sub>B</sub>	-0.3	Α		
Continuous device dissipation at (or below) 25°C case temperature (see Note	P <sub>tot</sub>	70	W		
Continuous device dissipation at (or below) 25°C free air temperature (see No	P <sub>tot</sub>	2	W		
Operating free-air temperature range	T <sub>A</sub>	-65 to +150	°C		
Operating junction temperature range	T <sub>j</sub>	-65 to +150	°C		
Storage temperature range	T <sub>stg</sub>	-65 to +150	°C		

NOTES: 1. Derate linearly to 150°C case temperature at the rate of 0.56 W/°C.

2. Derate linearly to  $150^{\circ}$ C free air temperature at the rate of 16 mW/°C.

#### BD896, BD898, BD900, BD902 PNP SILICON POWER DARLINGTONS

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#### electrical characteristics at 25°C case temperature (unless otherwise noted)

	PARAMETER	TEST CONDITIONS				MIN	TYP	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-emitter breakdown voltage	I <sub>C</sub> = -100 mA	I <sub>B</sub> = 0	(see Note 3)	BD896 BD898 BD900 BD902	-45 -60 -80 -100			٧
I <sub>CEO</sub>	Collector-emitter cut-off current	$V_{CE} = -30 \text{ V}$ $V_{CE} = -30 \text{ V}$ $V_{CE} = -40 \text{ V}$ $V_{CE} = -50 \text{ V}$	$I_{B} = 0$ $I_{B} = 0$ $I_{B} = 0$ $I_{B} = 0$		BD896 BD898 BD900 BD902			-0.5 -0.5 -0.5 -0.5	mA
Ісво	Collector cut-off current	$V_{CB} = -45 \text{ V}$ $V_{CB} = -60 \text{ V}$ $V_{CB} = -80 \text{ V}$	I <sub>E</sub> = 0 I <sub>E</sub> = 0	$T_{C} = 100^{\circ}C$ $T_{C} = 100^{\circ}C$ $T_{C} = 100^{\circ}C$ $T_{C} = 100^{\circ}C$	BD896 BD898 BD900 BD902 BD896 BD898 BD900			-0.2 -0.2 -0.2 -0.2 -2 -2 -2	mA
I <sub>EBO</sub>	Emitter cut-off current	V <sub>EB</sub> = -5 V	I <sub>C</sub> = 0	(see Notes 3 and 4)				-2	mA
h <sub>FE</sub>	Forward current transfer ratio	V <sub>CE</sub> = -3 V	I <sub>C</sub> = -3 A	(see Notes 3 and 4)		750			
V <sub>CE(sat)</sub>	Collector-emitter saturation voltage	I <sub>B</sub> = -12 mA	I <sub>C</sub> = -3 A	(see Notes 3 and 4)				-2.5	٧
V <sub>BE(on)</sub>	Base-emitter voltage	V <sub>CE</sub> = -3 V	I <sub>C</sub> = -3 A	(see Notes 3 and	i 4)			-2.5	٧
V <sub>EC</sub>	Parallel diode forward voltage	I <sub>E</sub> = -8 A	I <sub>B</sub> = 0					-3.5	٧

NOTES: 3. These parameters must be measured using pulse techniques,  $t_p = 300 \, \mu s$ , duty cycle  $\leq 2\%$ .

#### thermal characteristics

	PARAMETER			MAX	UNIT
$R_{\theta JC}$	Junction to case thermal resistance			1.79	°C/W
$R_{\theta JA}$	Junction to free air thermal resistance			62.5	°C/W

#### resistive-load-switching characteristics at 25°C case temperature

	PARAMETER	TEST CONDITIONS †			MIN	TYP	MAX	UNIT
t <sub>on</sub>	Turn-on time	I <sub>C</sub> = -3 A	$I_{B(on)} = -12 \text{ mA}$	$I_{B(off)} = 12 \text{ mA}$		1		μs
t <sub>off</sub>	Turn-off time	$V_{BE(off)} = 3.5 \text{ V}$	$R_L = 10 \Omega$	$t_p = 20 \ \mu s, \ dc \le 2\%$		5		μs

<sup>&</sup>lt;sup>†</sup> Voltage and current values shown are nominal; exact values vary slightly with transistor parameters.

<sup>4.</sup> These parameters must be measured using voltage-sensing contacts, separate from the current carrying contacts.

#### TYPICAL CHARACTERISTICS

### **TYPICAL DC CURRENT GAIN** vs **COLLECTOR CURRENT** TCS135AD 50000 $T_c = -40^{\circ}C$ T<sub>c</sub> = 25°C T<sub>c</sub> = 100°C h<sub>FE</sub> - Typical DC Current Gain 10000 1000 -3 V = 300 µs, duty cycle < 2% 100 -1·0 -0.5 -10 Ic - Collector Current - A Figure 1.

## COLLECTOR-EMITTER SATURATION VOLTAGE vs

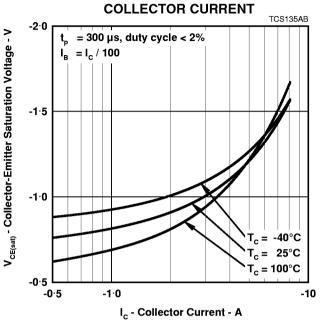
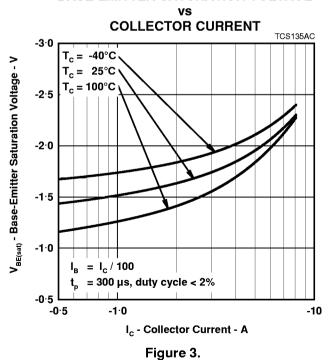


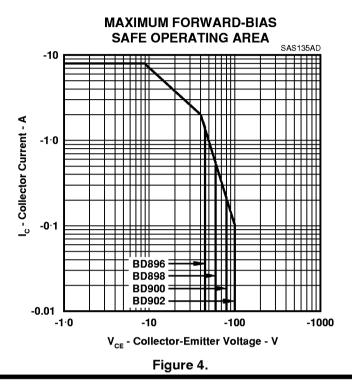
Figure 2.

#### **BASE-EMITTER SATURATION VOLTAGE**



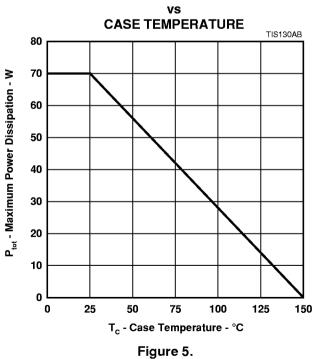


#### **MAXIMUM SAFE OPERATING REGIONS**



#### THERMAL INFORMATION

#### **MAXIMUM POWER DISSIPATION**



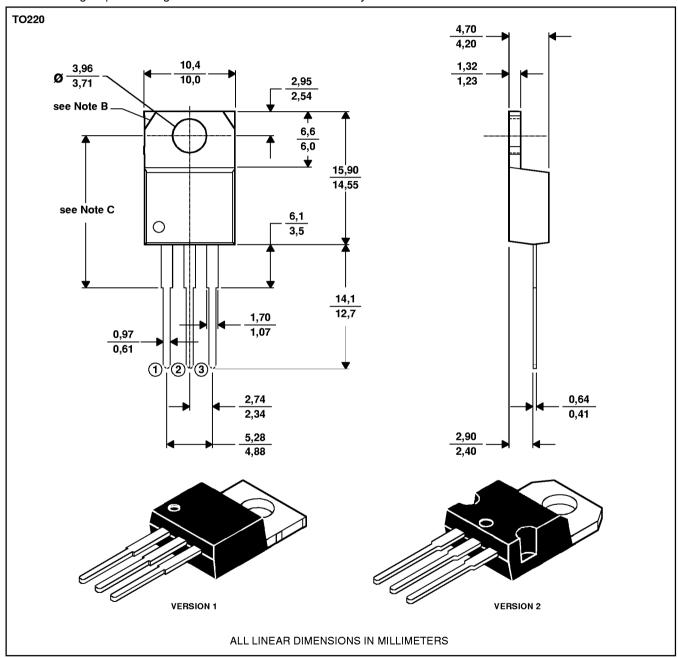
PRODUCT INFORMATION

#### **MECHANICAL DATA**

#### TO-220

#### 3-pin plastic flange-mount package

This single-in-line package consists of a circuit mounted on a lead frame and encapsulated within a plastic compound. The compound will withstand soldering temperature with no deformation, and circuit performance characteristics will remain stable when operated in high humidity conditions. Leads require no additional cleaning or processing when used in soldered assembly.



NOTES: A. The centre pin is in electrical contact with the mounting tab.

B. Mounting tab corner profile according to package version.

C. Typical fixing hole centre stand off height according to package version. Version 1, 18.0 mm. Version 2, 17.6 mm. MDXXBE



#### BD896, BD898, BD900, BD902 PNP SILICON POWER DARLINGTONS

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