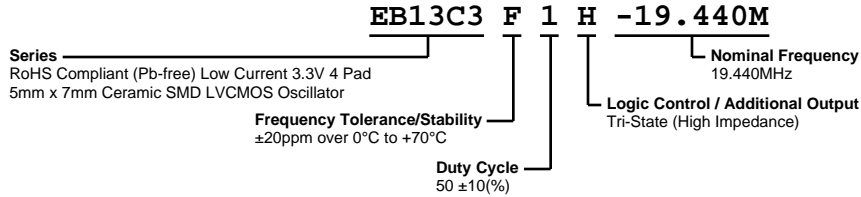


# EB13C3F1H-19.440M



**ECLIPTEK**<sup>®</sup>  
CORPORATION



## ELECTRICAL SPECIFICATIONS

<b>Nominal Frequency</b>	19.440MHz
<b>Frequency Tolerance/Stability</b>	±20ppm over 0°C to +70°C (Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the Operating Temperature Range, Supply Voltage Change, Output Load Change, First Year Aging at 25°C, Shock, and Vibration)
<b>Supply Voltage</b>	3.3Vdc ±10%
<b>Input Current</b>	3mA Maximum
<b>Output Voltage Logic High (Voh)</b>	90% of Vdd Minimum
<b>Input Current Logic High (Ioh)</b>	-1.6mA
<b>Output Voltage Logic Low (Vol)</b>	10% of Vdd Maximum
<b>Input Current Logic Low (Iol)</b>	+1.6mA
<b>Rise/Fall Time</b>	6nSec Maximum (Measured at 20% to 80% of waveform)
<b>Duty Cycle</b>	50 ±10(%) (Measured at 50% of waveform)
<b>Load Drive Capability</b>	15pF Maximum
<b>Output Logic Type</b>	CMOS
<b>Logic Control / Additional Output</b>	Tri-State (High Impedance)
<b>Tri-State Input Voltage (Vih and Vil)</b>	90% of Vdd Minimum or No Connect to Enable Output, 10% of Vdd Maximum to Disable Output (High Impedance)
<b>Standby Current</b>	10µA Maximum (Disabled Output: High Impedance)
<b>One Sigma Clock Period Jitter</b>	25pSec Maximum
<b>Start Up Time</b>	10mSec Maximum
<b>Storage Temperature Range</b>	-55°C to +125°C

## ENVIRONMENTAL & MECHANICAL SPECIFICATIONS

<b>Fine Leak Test</b>	MIL-STD-883, Method 1014, Condition A
<b>Gross Leak Test</b>	MIL-STD-883, Method 1014, Condition C
<b>Mechanical Shock</b>	MIL-STD-202, Method 213, Condition C
<b>Resistance to Soldering Heat</b>	MIL-STD-202, Method 210
<b>Resistance to Solvents</b>	MIL-STD-202, Method 215
<b>Solderability</b>	MIL-STD-883, Method 2003
<b>Temperature Cycling</b>	MIL-STD-883, Method 1010
<b>Vibration</b>	MIL-STD-883, Method 2007, Condition A

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## MECHANICAL DIMENSIONS (all dimensions in millimeters)



PIN	CONNECTION
1	Tri-State
2	Ground
3	Output
4	Supply Voltage

LINE	MARKING
1	<b>ECLIPTEK</b>
2	<b>19.440M</b>
3	<b>XXYYZZ</b> <i>XX=Ecliptek Manufacturing Code</i> <i>Y=Last Digit of the Year</i> <i>ZZ=Week of the Year</i>

## Suggested Solder Pad Layout

All Dimensions in Millimeters



All Tolerances are  $\pm 0.1$

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## OUTPUT WAVEFORM & TIMING DIAGRAM



### Test Circuit for CMOS Output



Note 1: An external 0.1µF low frequency tantalum bypass capacitor in parallel with a 0.01µF high frequency ceramic bypass capacitor close to the package ground and  $V_{DD}$  pin is required.

Note 2: A low capacitance (<12pF), 10X attenuation factor, high impedance (>10Mohms), and high bandwidth (>300MHz) passive probe is recommended.

Note 3: Capacitance value  $C_L$  includes sum of all probe and fixture capacitance.

## Recommended Solder Reflow Methods



### High Temperature Infrared/Convection

<b>T<sub>s</sub> MAX to T<sub>L</sub> (Ramp-up Rate)</b>	3°C/second Maximum
<b>Preheat</b>	
- Temperature Minimum (T <sub>s</sub> MIN)	150°C
- Temperature Typical (T <sub>s</sub> TYP)	175°C
- Temperature Maximum (T <sub>s</sub> MAX)	200°C
- Time (t <sub>s</sub> MIN)	60 - 180 Seconds
<b>Ramp-up Rate (T<sub>L</sub> to T<sub>p</sub>)</b>	3°C/second Maximum
<b>Time Maintained Above:</b>	
- Temperature (T <sub>L</sub> )	217°C
- Time (t <sub>L</sub> )	60 - 150 Seconds
<b>Peak Temperature (T<sub>p</sub>)</b>	260°C Maximum for 10 Seconds Maximum
<b>Target Peak Temperature (T<sub>p</sub> Target)</b>	250°C +0/-5°C
<b>Time within 5°C of actual peak (t<sub>p</sub>)</b>	20 - 40 seconds
<b>Ramp-down Rate</b>	6°C/second Maximum
<b>Time 25°C to Peak Temperature (t)</b>	8 minutes Maximum
<b>Moisture Sensitivity Level</b>	Level 1
<b>Additional Notes</b>	Temperatures shown are applied to body of device.

## Recommended Solder Reflow Methods



### Low Temperature Infrared/Convection 240°C

<b>T<sub>s</sub> MAX to T<sub>L</sub> (Ramp-up Rate)</b>	5°C/second Maximum
<b>Preheat</b>	
- Temperature Minimum (T <sub>s</sub> MIN)	N/A
- Temperature Typical (T <sub>s</sub> TYP)	150°C
- Temperature Maximum (T <sub>s</sub> MAX)	N/A
- Time (t <sub>s</sub> MIN)	60 - 120 Seconds
<b>Ramp-up Rate (T<sub>L</sub> to T<sub>P</sub>)</b>	5°C/second Maximum
<b>Time Maintained Above:</b>	
- Temperature (T <sub>L</sub> )	150°C
- Time (t <sub>L</sub> )	200 Seconds Maximum
<b>Peak Temperature (T<sub>P</sub>)</b>	240°C Maximum
<b>Target Peak Temperature (T<sub>P</sub> Target)</b>	240°C Maximum 1 Time / 230°C Maximum 2 Times
<b>Time within 5°C of actual peak (t<sub>p</sub>)</b>	10 seconds Maximum 2 Times / 80 seconds Maximum 1 Time
<b>Ramp-down Rate</b>	5°C/second Maximum
<b>Time 25°C to Peak Temperature (t)</b>	N/A
<b>Moisture Sensitivity Level</b>	Level 1
<b>Additional Notes</b>	Temperatures shown are applied to body of device.

### Low Temperature Manual Soldering

185°C Maximum for 10 seconds Maximum, 2 times Maximum. (Temperatures shown are applied to body of device.)

### High Temperature Manual Soldering

260°C Maximum for 5 seconds Maximum, 2 times Maximum. (Temperatures shown are applied to body of device.)