Low Power, 12 -Bit CMOS DACs

## FEATURES

- 12-bits binary or 3-decades BDC
- Low power: 300 mW
- Commercial, industrial and MIL-STD-883 models
- 5 pin selectable output ranges


## DESCRIP TION

The DAC349 Series are versatile, low power 12-bit D/A converters that are intended for fixed reference applications. These units combine a novel CMOS switching technique with a precision, ultra-stable thin-film ladder to achieve accurate conversion with low power drain; improved reliability and nearinstantaneous warmup are major advantages of reduced power consumption. All models include an internal precision reference and a gain-selectable output amplifier.
The DAC349 Series offers a choice of either 12-bit binary coding (-12 models) or 3 decade BCD coding (-3D models).
By external pin jumpering, the binary models can be connected for unipolar output ranges of 0 to

-5 V or 0 to -10 V and for offset binary coded bipolar outputs of $\pm 2.5 \mathrm{~V}, \pm 5 \mathrm{~V}$ and $\pm 10 \mathrm{~V}$. For ratiometric applications, the units operate from an external fixed DC reference of $-10 \mathrm{~V} \pm 10 \%$.
Both binary and BCD models are available in commercial/industrial and MIL-STD-883 Rev. C, Level B versions. The DAC349B is specified over a temperature range of $-55^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$. Both versions operate $-55^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$.

## FUNCTIONAL DIAGRAM



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## SPECIFICATIONS

(Typical @ $+25^{\circ} \mathrm{C}$ and nominal power supplies unless otherwise noted)

| SERIES | DAC349 |
| :---: | :---: |
| TYPE | Fixed Reference, Voltage Output |
| DIGITAL INPUT |  |
| Resolution | 12 Bits Binary (-12) or 3 Decades BCD (-3D) |
| Unipolar Coding | Binary or BCD |
| Bipolar Coding | Offset Binary |
| Logic Levels |  |
| Binary | $\begin{aligned} & \mathrm{V}_{\mathrm{IH}}=2.4 \mathrm{~V} \mathrm{~min}, \\ & \mathrm{~V}_{\mathrm{IL}}=0.8 \mathrm{~V} \mathrm{max}, \end{aligned}$ |
| $B C D$ | $\begin{aligned} & \mathrm{V}_{\mathrm{IH}}=3.5 \mathrm{~V} \text { min }, \\ & \mathrm{V}_{\mathrm{IL}}=1.5 \mathrm{~V} \mathrm{max}, \end{aligned}$ |
| Input Current | $\pm 1 \mu \mathrm{~A}$ max |
| ANALOG OUTPUT |  |
| Voltage Range | 0 to -5V, 0 to -10 |
|  | $\pm 2.5 \mathrm{~V} . \pm 5 \mathrm{~V}, \pm 10 \mathrm{~V}$ |
| Current Compliance | $\pm 5 \mathrm{~mA}$ ( min ) |
| Impedance | 0.1 |
| Scale Factor ${ }^{1}$ | $\pm 0.1 \%$ of F.S.R. (max) |
| Unipolar Offset ${ }^{1}$ | $\pm 0.2 \%$ of F.S.R. (max) |
| Bipolar Offset ${ }^{1}$ | $\pm 0.1 \%$ of F.S.R. (max) |
| REFERENCE |  |
| Internal ${ }^{2}$ | -10V DC |
| External (D.C. only) | -10V DC $\pm 10 \%$ @ 1 mA |
| STATIC PERFORMANCE |  |
| Linearity | $\pm 1 / 2$ LSB (max) |
| Differential Linearity | $\pm 1$ LSB (max) |
| DYNAMIC PERFORMANCE |  |
| Settling Time | $15 \mu \mathrm{~S}$ (max) |
| Slew Rate | 1V/us |
| STABILITY |  |
| Accuracy vs Temp. ${ }^{3}$ | $30 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$ (max) |
| Linearity vs Temp. | $5 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$ (max) |
| Differential Linearity vs Temp. | 2ppm/ $/{ }^{\circ} \mathrm{C}$ (max) |
| POWER SUPPLY |  |
| Voltage @ Current | +15V @ 5mA (nom): 10mA (max) |
|  | +13V to +18V; |
|  | -15V @ 15mA (nom): 20mA (max) |
|  | -13V to -18V; |
| Rejection Ratio | 0.005\% /\% |
| Power Consumption | 375 mW (max) |
| TEMPERATURE RANGE |  |

## Specified

| DAC349C | $-25^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |
| :--- | :--- |
| DAC349B | $-55^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ |
| Operating, All models | $-55^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ |
| Storage, All models | $-65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |

DAC349C
$-25^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$
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Storage, All models $-65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$

Case Style

## Ceramic

Consult factory for application information.


Pin 1 is marked by the dot on the top of the package

| PIN | FUNCTION | PIN | FUNCTION |  |
| :---: | :--- | :--- | :--- | ---: |
| NO. | NO. |  | BINARY | BCD |
| 1 | OUTPUT | 24 | BIT 1 (MSB) | 800 |
| 2 | SUMMING JCT | 23 | BIT 2 | 400 |
| 3 | 10V RANGE | 22 | BIT 3 | 200 |
| 4 | 20 VOLT RANGE | 21 | BIT 4 | 100 |
| 5 | BIPOLAR OFFSET | 20 | BIT 5 | 80 |
| 6 | GAIN ADJ | 19 | BIT 6 | 40 |
| 7 | REF IN | 18 | BIT 7 | 20 |
| 8 | REF OUT | 17 | BIT 8 | 10 |
| 9 | -15 | 16 | BIT 9 | 8 |
| 10 | +15V | 15 | BIT 10 | 4 |
| 11 | GND | 14 | BIT 11 | 2 |
| 12 | GND | 13 | BIT 1 (LSB) | 1 |

NOTES:

1. Offset and gain are externally adjustable. See APPLICATIONS INFORMATION.
2. For specified overall performance, external loading of the reference output (Pin 8) must not exceed 1.0 mA .
3. Total effect of linearity, offset and gain tempco's on the transfer characteristics of the unit.
4. In case of discrepancy between package shown in photograph and package outline dimension, the mechanical outline is correct. Specifications subject to change without notice.

## ORDERING INFORMATION

MODEL
DAC349C-12
DAC349C-3D
DAC349B-12
DAC349B-3D

## DESCRIPTION

Comm. 12-Bit, $-25^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ Comm, 3-Decade, $-25^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ MIL, 12 -Bit, $-55^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ MIL,3-Decade, $-55^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$

CAUTION: ESD (Electro-Static Discharge) sensitive device. Permanent damage may occur when unconnected devices are subjected to high energy electrostatic fields. Unless otherwise noted, the voltage at any digital input should never exceed the supply voltage by more than 0.5 volts or go below -0.5 volts.

