

CRD5AS-12B

Reverse Conducting Thyristor
Medium Power Use

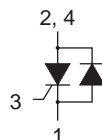
R07DS0503EJ0100
Rev.1.00
Jul 07, 2011

Features

- $I_{T(AV)}$: 5 A
- V_{DRM} : 600 V
- I_{GT} : 100 μ A
- The Product guaranteed maximum junction temperature 150°C
- Built-in reverse conducting diode
- Planar Type

Outline

RENESAS Package code: PRSS0004ZG-A
(Package name: MP-3A)



1. Cathode
2. Anode
3. Gate
4. Anode

Applications

Switching mode power supply, Regulator for motorcycle

Maximum Ratings

Parameter	Symbol	Voltage class	Unit
		12	
Repetitive peak off-state voltage ^{Note1}	V_{DRM}	600	V

Notes: 1. With gate to cathode resistance $R_{GK}=220\Omega$

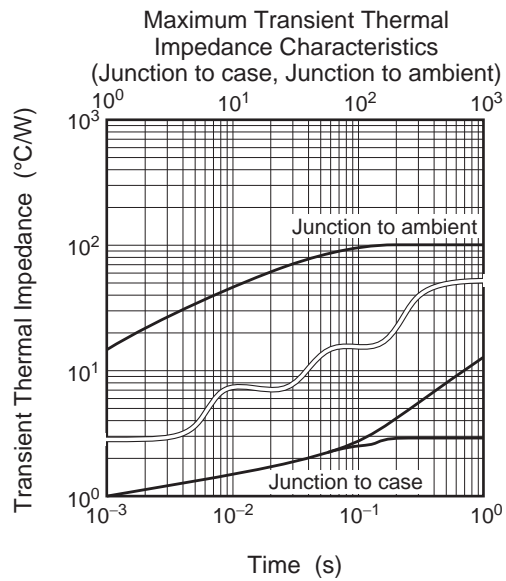
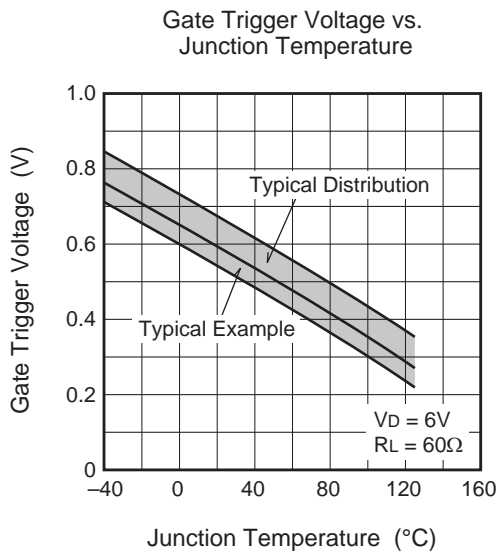
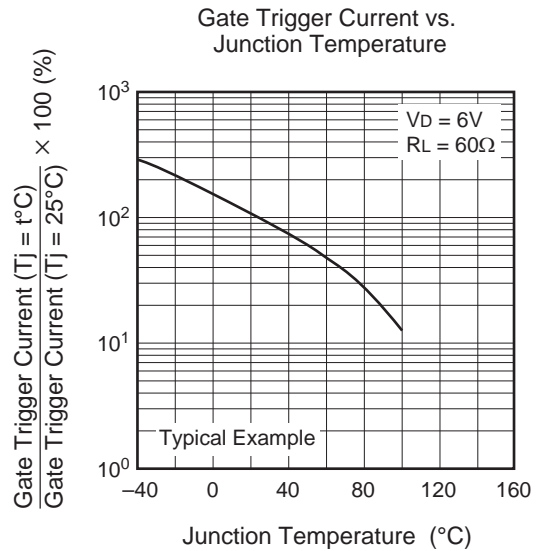
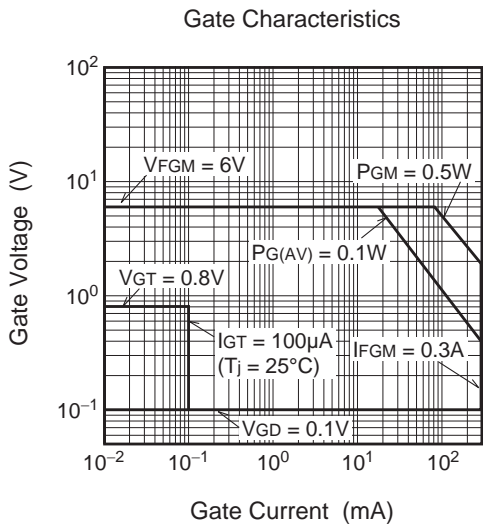
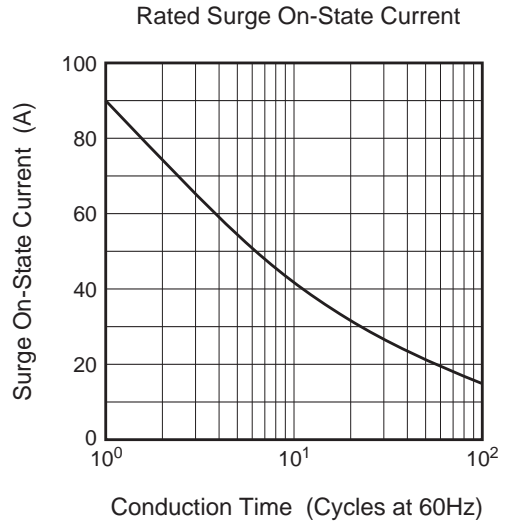
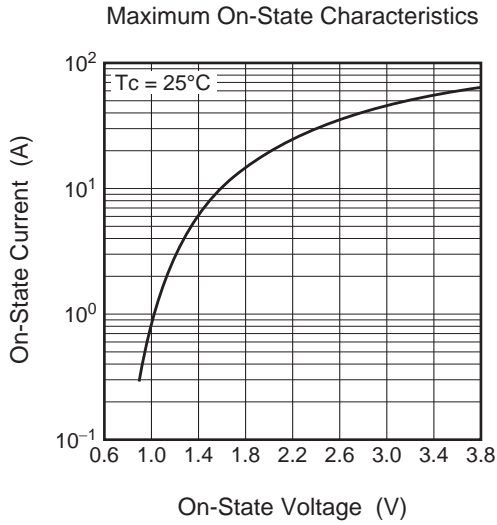
Parameter	Symbol	Ratings	Unit	Conditions
RMS on-state current	$I_{T(RMS)}$	7.8	A	
Average on-state current	$I_{T(AV)}$	5	A	Commercial frequency, sine half wave 180° conduction, $T_c=113^\circ\text{C}$
Surge on-state current	I_{TSM}	90	A	60Hz sine half wave, 1full cycle, peak value, non-repetitive
I^2t for fusing	I^2t	33	A^2s	Value corresponding to 1cycle of half wave 60Hz, surge on-state current
Surge reverse-conducting current	I_{RCSM}	3	A	sine half wave, pulse width 10ms peak value, non-repetitive, $R_{GK}=0\Omega$
Peak gate power dissipation	P_{GM}	0.5	W	
Average gate power dissipation	$P_{G(AV)}$	0.1	W	
Peak gate forward voltage	V_{FGM}	6	V	
Peak gate reverse voltage	V_{RGM}	6	V	
Peak gate forward current	I_{FGM}	0.3	A	
Junction temperature	T_j	- 40 to +150	$^\circ\text{C}$	
Storage temperature	T_{stg}	- 40 to +150	$^\circ\text{C}$	
Mass	—	0.26	g	Typical value

Electrical Characteristics

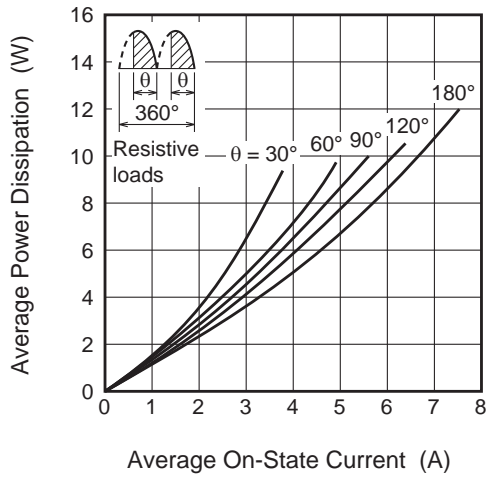
Parameter	Symbol	Min.	Typ.	Max.	Unit	Test conditions
Repetitive peak off-state current	I_{DRM}	—	—	2.0	mA	$T_j = 150^\circ\text{C}$, V_{DRM} applied $R_{GK}=220\Omega$
On-state voltage	V_{TM}	—	—	1.8	V	$T_j = 25^\circ\text{C}$, $I_{TM} = 15\text{ A}$ instantaneous value
Gate trigger voltage	V_{GT}	—	—	0.8	V	$T_j = 25^\circ\text{C}$, $V_D = 6\text{ V}$, $I_T = 0.1\text{ A}$
Gate non-trigger voltage	V_{GD}	0.1	—	—	V	$T_j = 150^\circ\text{C}$, $V_D = 1/2 V_{DRM}$ $R_{GK}=220\Omega$
Gate trigger current	I_{GT}	1	—	100	μA	$T_j = 25^\circ\text{C}$, $V_D = 6\text{ V}$, $I_T = 0.1\text{ A}$
Holding current	I_H	—	3	—	mA	$T_j = 25^\circ\text{C}$, $V_D = 12\text{ V}$ $R_{GK}=220\Omega$
Thermal resistance	$R_{th(j-c)}$	—	—	3.0	$^\circ\text{C/W}$	Junction to case ^{Note2}

Notes: 2. The measurement point for case temperature is at anode tab.

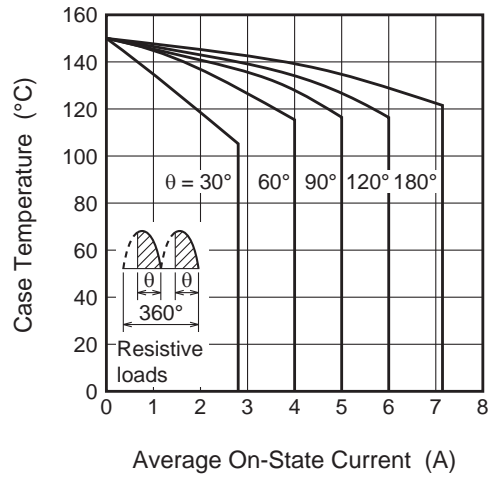
Performance Curves



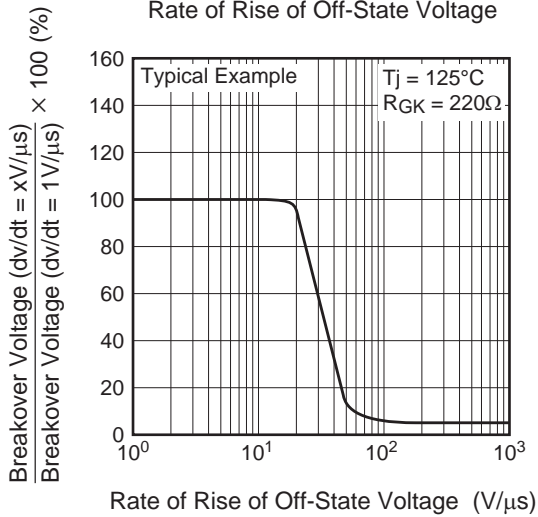
Maximum Average Power Dissipation
(Single-Phase Full Wave)



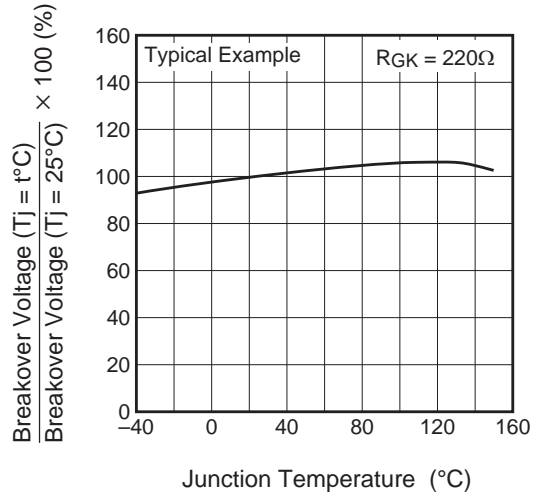
Allowable Case Temperature vs.
Average On-State Current
(Single-Phase Full Wave)



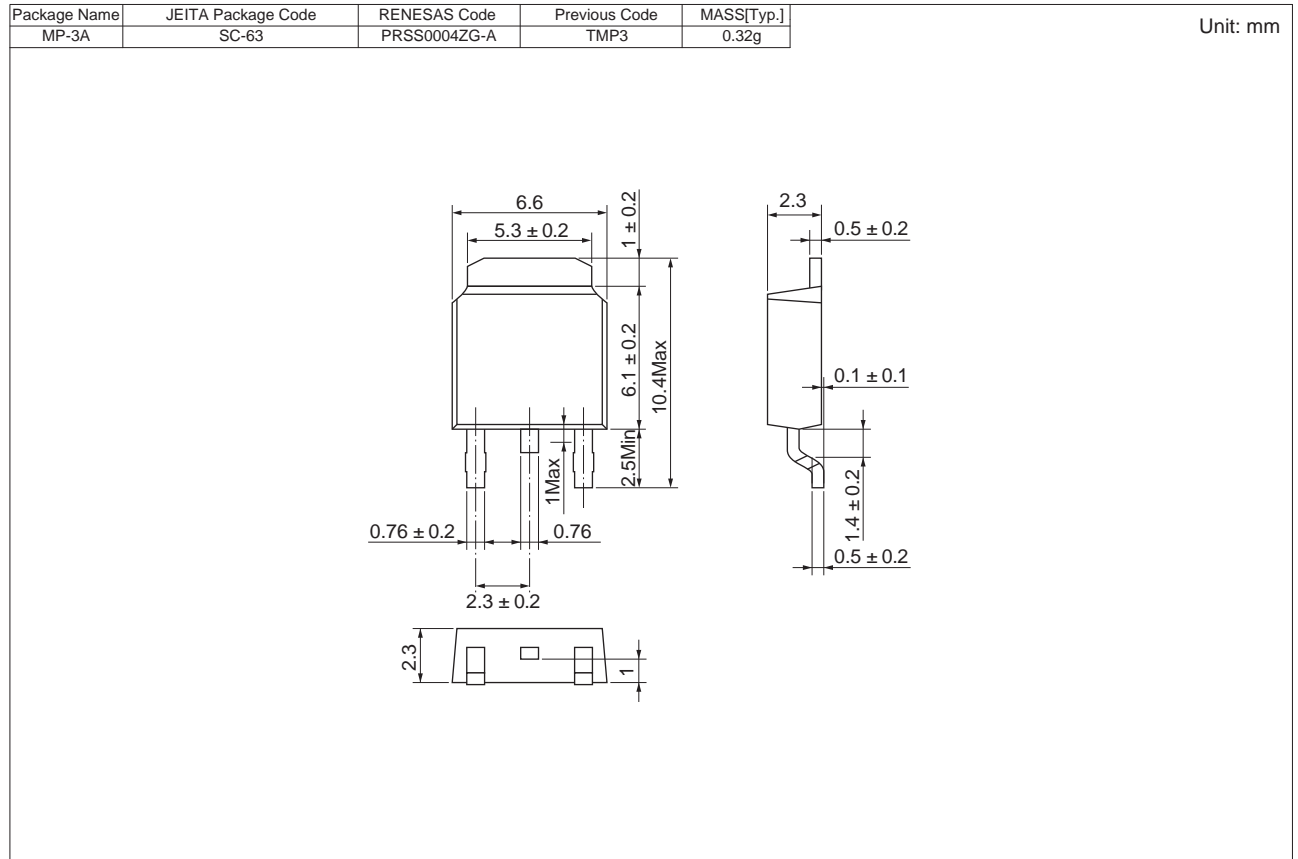
Breakover Voltage vs.
Rate of Rise of Off-State Voltage



Breakover Voltage vs.
Junction Temperature



Package dimensions



Ordering Information

Orderable Part Number	Packing	Quantity	Remark
CRD5AS-12B#B00	Tube	75 pcs.	—
CRD5AS-12B-T13#B00	Embossed Tape	3000 pcs.	Taping direction "T1"

Note : Please confirm the specification about the shipping in detail.

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Renesas Electronics America Inc.
2880 Scott Boulevard Santa Clara, CA 95050-2554, U.S.A.
Tel: +1-408-588-6000, Fax: +1-408-588-6130

Renesas Electronics Canada Limited
1101 Nicholson Road, Newmarket, Ontario L3Y 9C3, Canada
Tel: +1-905-898-5441, Fax: +1-905-898-3220

Renesas Electronics Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: +44-1628-585-100, Fax: +44-1628-585-900

Renesas Electronics Europe GmbH
Arcadiastrasse 10, 40472 Düsseldorf, Germany
Tel: +49-211-65030, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd.
7th Floor, Quantum Plaza, No.27 ZhiChunLu Haidian District, Beijing 100083, P.R.China
Tel: +86-10-8235-1155, Fax: +86-10-8235-7679

Renesas Electronics (Shanghai) Co., Ltd.
Unit 204, 205, AZIA Center, No.1233 Lujiazui Ring Rd., Pudong District, Shanghai 200120, China
Tel: +86-21-5877-1818, Fax: +86-21-6887-7858 / -7898

Renesas Electronics Hong Kong Limited
Unit 1601-1613, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong
Tel: +852-2886-9318, Fax: +852 2886-9022/9044

Renesas Electronics Taiwan Co., Ltd.
13F, No. 363, Fu Shing North Road, Taipei, Taiwan
Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

Renesas Electronics Singapore Pte. Ltd.
1 HarbourFront Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: +65-6213-0200, Fax: +65-6278-8001

Renesas Electronics Malaysia Sdn.Bhd.
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No. 18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: +60-3-7955-9390, Fax: +60-3-7955-9510

Renesas Electronics Korea Co., Ltd.
11F., Samik Lavied' or Bldg., 720-2 Yeoksam-Dong, Kangnam-Ku, Seoul 135-080, Korea
Tel: +82-2-558-3737, Fax: +82-2-558-5141