

386 Series Clock Oscillators

HCMOS Drive - CMOS Compatible

f_o : 24, 32, 40, 50 MHz

FEATURES

- 1) Capable of driving the 80386 and surrounding LSI directly up to 150pF load
- 2) Replaces existing clock generator and/or buffer
- 3) 45/55 symmetry for all standard frequencies even at 150pF load
- 4) Enable/disable function as standard
- 5) 25 pieces per tube
- 6) Sold in increments of 100 pieces

HOW TO ORDER

386-HC 1 - C S E - 40.0000M T

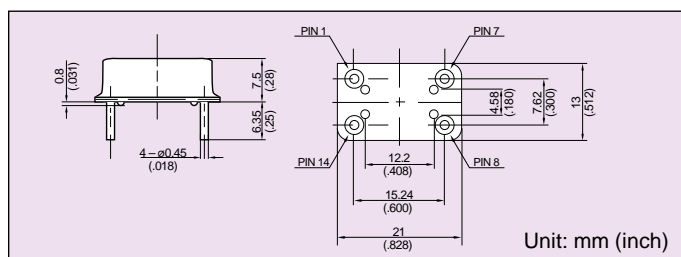
- ① Type: 386 = 14 pin DIP
 ② Frequency precision: 1 = ± 100 ppm
 ③ Output level/Duty cycle
 CS = CMOS compatible/45 to 55%
 ④ Enable/disable function:
 □ = without function, E = with function
 ⑤ Frequency
 ⑥ Packaging: T = Tube



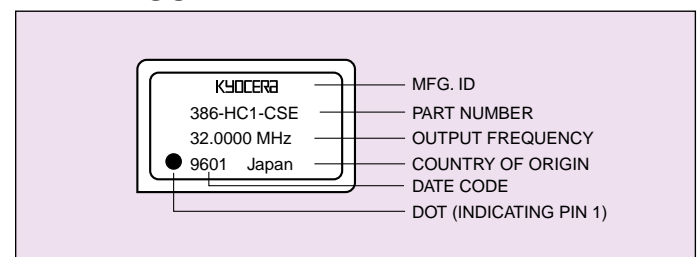
SPECIFICATIONS (386-HC)

Parameters		Code	Rating	Unit	Remarks
Output Frequency		f_{OUT}	24, 32, 40 50	MHz MHz	CL=150pFmax CL=80pFmax
Frequency Precision (Inclusive of Temp. Voltage variation)		$\Delta f/f$	1: ± 100	ppm	Ta=0~70°C
Aging Rate		$\Delta f/f$	± 5	ppm/yr	
Operating Temperature		T _{OPR}	0~+70	°C	
Storage Temperature Range		T _{STR}	-55~+125	°C	
Supply Voltage		V _{DD}	5 ± 0.25	V	
Supply Current		I _{CC}	65 max	mA	Cl=150pF, Ta=25°C
Output	Duty Ratio	Sy	45~55	%	1/2 V _{DD} level
	"0" Level	V _{OL}	0.1V _{DD} max	V	I _{OL} =12mA
	"1" Level	V _{OH}	0.9V _{DD} min	V	I _{OL} =-1mA
	Rise and Fall Time	T _R , T _F	See Clock Time Table	nsec	
Enable/Disable Time			100 max 100 max	nsec nsec	Type E Tristate Output
Input	Current	I _{IH}	10 max	μ A	V _{DD} = 5.25V
		I _{IL}	-150 max	μ A	V _{DD} =5.25V
	Voltage	V _{IH}	2.2 min	V	
		V _{IL}	0.8 max	V	
Fan Out			7	TTL	
Load Capacitance		C _L	150 80	pF pF	f=12.0MHz~40MHz f=40.1MHz~50MHz

DIMENSIONS



MARKINGS

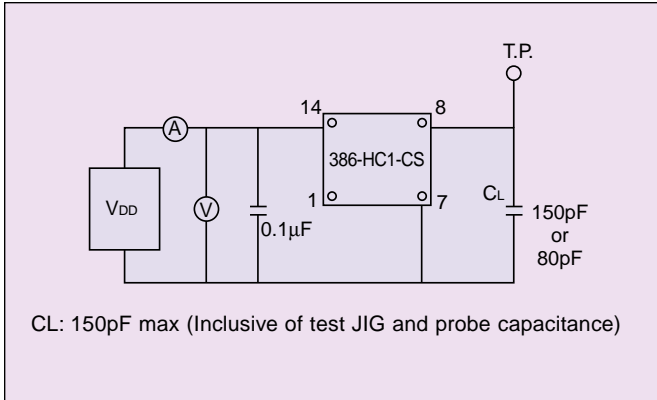


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TEST CIRCUIT



PIN CONNECTION

386	Function
1	N.C. or Control
7	Case GND
8	Output
14	V _{DD}

CLOCK TIME TABLE (32MHz, 40MHz)

Frequency	32MHz		40MHz	
	Min	Max	Min	Max
Clock time (ns)				
Clock high time t _{2a}	9	-	8	-
Clock high time t _{2b}	5	-	5	-
Clock low time t _{3a}	9	-	8	-
Clock low time t _{3b}	7	-	6	-
Clock tall time t ₄	-	7.5	-	8
Clock tall time t ₅	-	7.5	-	8

CLOCK TIME TABLE (50MHz)

Frequency	50MHz	
Clock time(ns)	Min	Max
Clock high time t _{2a}	7	-
Clock high time t _{2b}	4	-
Clock low time t _{3a}	7	-
Clock low time t _{3b}	5	-
Clock tall time t ₄	-	7
Clock tall time t ₅	-	7