RJF0618JPE

60V-40A Silicon N Channel Thermal FET Power Switching

R07DS1069EJ0100 Rev.1.00 May 15, 2013

Datasheet

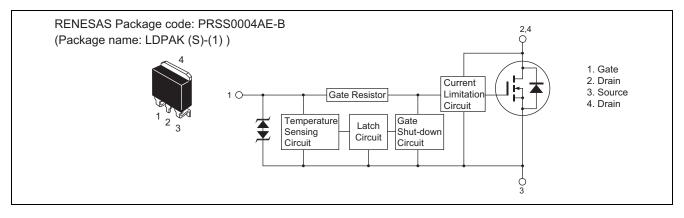
Description

This FET has the over temperature shut-down capability sensing to the junction temperature. This FET has the built-in over temperature shut-down circuit in the gate area. And this circuit operation to shut-down the gate voltage in case of high junction temperature like applying over power consumption, over current etc..

Features

- Logic level operation (4 V Gate drive).
- Built-in the over temperature shut-down circuit.
- High endurance capability against to the short circuit.
- Latch type shut down operation (need 0 voltage recovery).
- Built-in the current limitation circuit.
- Power supply voltage applies 12 V and 24 V.
- AEC-Q101 Compliant
- High endurance capability against to ESD.

Outline



Absolute Maximum Ratings

			$(Ta = 25^{\circ}C)$
Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	60	V
Gate to source voltage	V _{GSS}	16	V
Gate to source voltage	V _{GSS}	-2.5	V
Drain current	I _D ^{Note3}	40	А
Body-drain diode reverse drain current	I _{DR}	40	А
Avalanche current	I _{AP} ^{Note 2}	12	А
Avalanche energy	E _{AR} ^{Note 2}	617	mJ
Channel dissipation	Pch Note 1	50	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. Value at $Tc = 25^{\circ}C$

2. Tch = 25°C, Rg \geq 50 Ω

3. It provides by the current limitation lower bound value.



Typical Operation Characteristics

						$(Ta = 25^{\circ}C)$
ltem	Symbol	Min	Тур	Max	Unit	Test Conditions
Input voltage	V _{IH}	3.5	—	—	V	
	V _{IL}	_	—	1.2	V	
Input current	I _{IH1}	—	—	100	μΑ	Vi = 8 V, V _{DS} = 0
(Gate non shut down)	I _{IH2}	—	—	50	μΑ	$Vi = 3.5 V, V_{DS} = 0$
	IIL	—	—	1	μΑ	Vi = 1.2 V, V _{DS} = 0
Input current	I _{IH(sd)1}	—	0.8	_	mA	$Vi = 8 V, V_{DS} = 0$
(Gate shut down)	I _{IH(sd)2}	_	0.35	_	mA	$Vi = 3.5 V, V_{DS} = 0$
Shut down temperature	Tsd	_	175	_	°C	Channel temperature
Gate operation voltage	Vop	3.5	_	12	V	
Drain current (Current limitation value)	I _{D limt}	40	—	—	A	$V_{GS} = 5 V, V_{DS} = 10 V^{Note 4}$

Note; 4. Pulse test

Electrical Characteristics

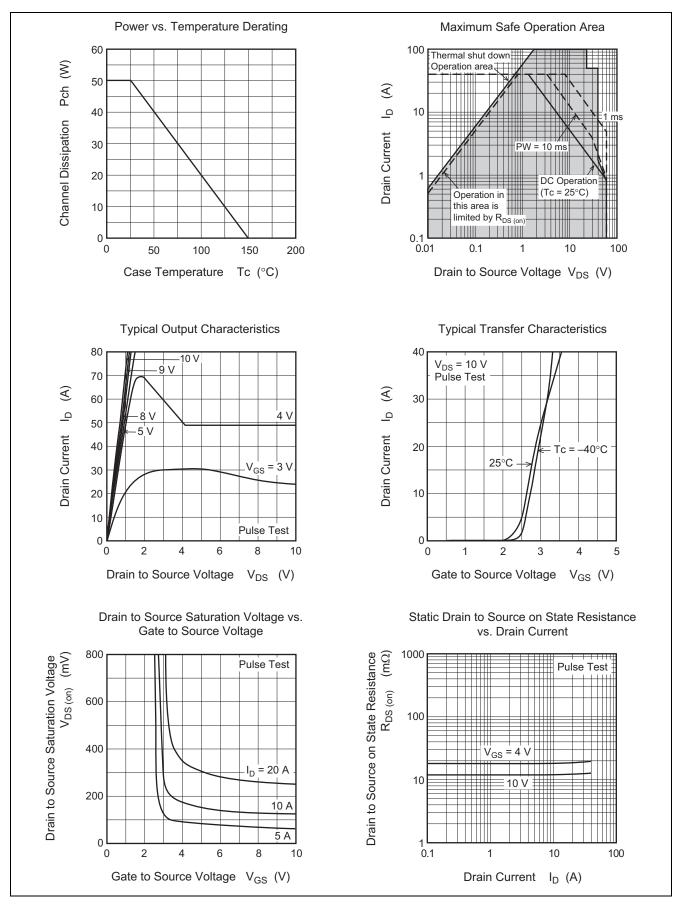
						$(Ta = 25^{\circ}C)$
ltem	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain current	I _{D1}	_	—	67	А	V_{GS} = 3.5 V, V_{DS} = 10 V ^{Note 5}
	I _{D2}	_	—	10	mA	$V_{GS} = 1.2 \text{ V}, V_{DS} = 10 \text{ V}$
	I _{D3}	40	—	—	А	$V_{GS} = 5 \text{ V}, \text{ V}_{DS} = 10 \text{ V}^{\text{Note 5}}$
Drain to source breakdown voltage	V _{(BR)DSS}	60	—	—	V	$I_{D} = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown	V _{(BR)GSS}	16	—	—	V	$I_{G} = 800 \ \mu A, \ V_{DS} = 0$
voltage	V _{(BR)GSS}	-2.5	—	—	V	$I_{G} = -100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I _{GSS1}	_	—	100	μA	$V_{GS} = 8 V, V_{DS} = 0$
	I _{GSS2}	_	—	50	μA	$V_{GS} = 3.5 \text{ V}, V_{DS} = 0$
	I _{GSS3}	_	—	1	μΑ	$V_{GS} = 1.2 \text{ V}, V_{DS} = 0$
	I _{GSS4}	—	—	-100	μA	$V_{GS} = -2.4 \text{ V}, V_{DS} = 0$
Input current (shut down)	I _{GS(OP)1}	_	0.8	—	mA	$V_{GS} = 8 \text{ V}, V_{DS} = 0$
	I _{GS(OP)2}	_	0.35	—	mA	$V_{GS} = 3.5 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	—	10	μΑ	$V_{DS} = 32 \text{ V}, V_{GS} = 0, \text{ Tc} = 110^{\circ}\text{C}$
Gate to source cutoff voltage	V _{GS(off)}	1.1	—	2.1	V	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$
Forward transfer admittance	y _{fs}	20	41	—	S	$I_D = 20 \text{ A}, V_{DS} = 10 \text{ V}^{Note 5}$
Static drain to source on state	R _{DS(on)}	—	17	25	mΩ	$I_D = 20 \text{ A}, V_{GS} = 4 \text{ V}^{Note 5}$
resistance	R _{DS(on)}	—	12	19	mΩ	$I_D = 20 \text{ A}, V_{GS} = 10 \text{ V}^{Note 5}$
Output capacitance	Coss	—	795	—	pF	$V_{DS} = 10 V, V_{GS} = 0, f = 1MHz$
Turn-on delay time	t _{d(on)}	—	6	—	μs	V_{GS} = 10 V, I_{D} = 20 A, R_{L} = 1.5 Ω
Rise time	tr	—	21.6	—	μs	
Turn-off delay time	t _{d(off)}	—	7.2	—	μs	
Fall time	t _f	—	12.5	—	μs	
Body-drain diode forward voltage	V _{DF}	_	0.9	—	V	$I_F = 40 \text{ A}, V_{GS} = 0$
Body-drain diode reverse recovery time	t _{rr}	—	113		ns	$I_F = 40 \text{ A}, V_{GS} = 0$ $di_F/dt = 50 \text{ A}/\mu\text{s}$
Over load shut down	t _{os1}		0.48		ms	$V_{GS} = 5 \text{ V}, \text{ V}_{DD} = 16 \text{ V}$
operation time Note 6	t _{os1}	_	0.31		ms	$V_{GS} = 5 \text{ V}, \text{ V}_{DD} = 24 \text{ V}$

Notes: 5. Pulse test

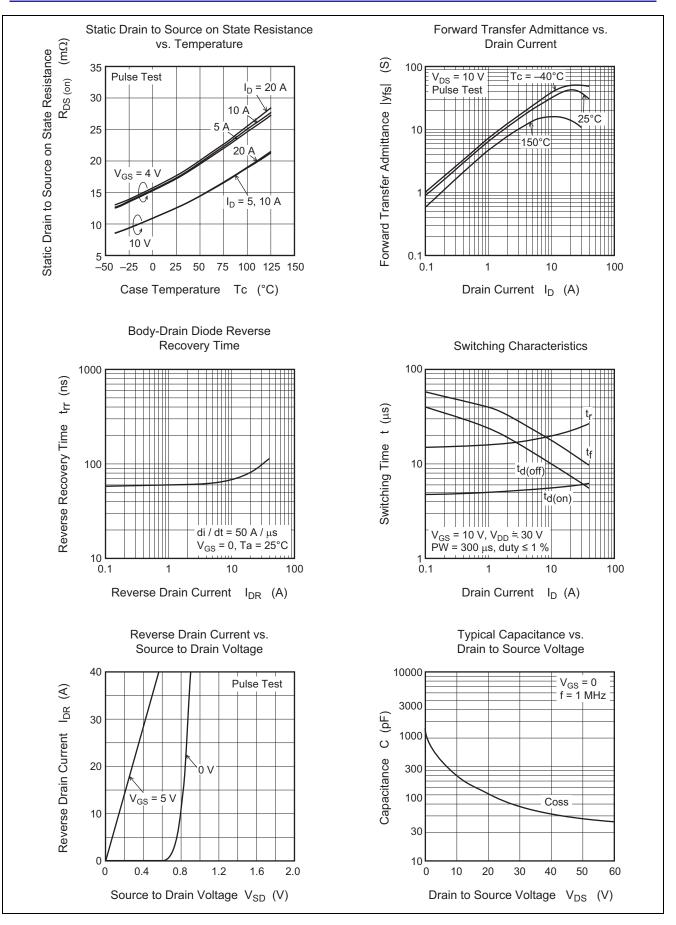
6. Including the junction temperature rise of the over loaded condition.



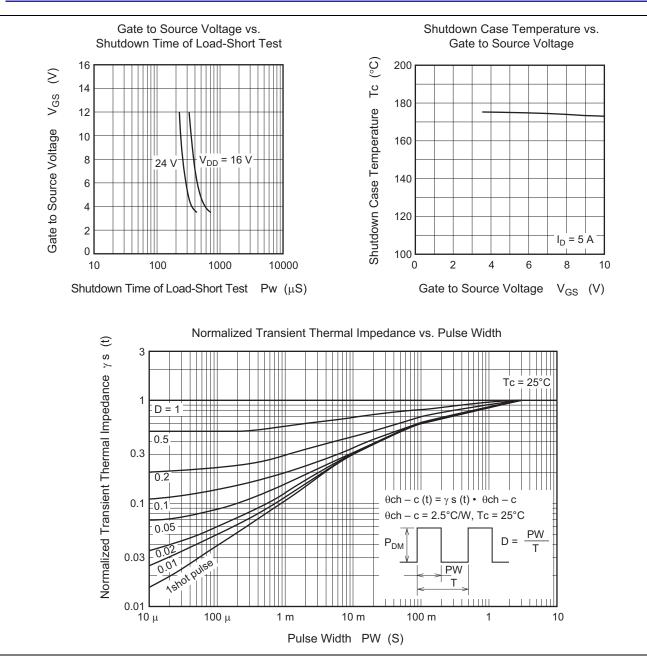
Main Characteristics



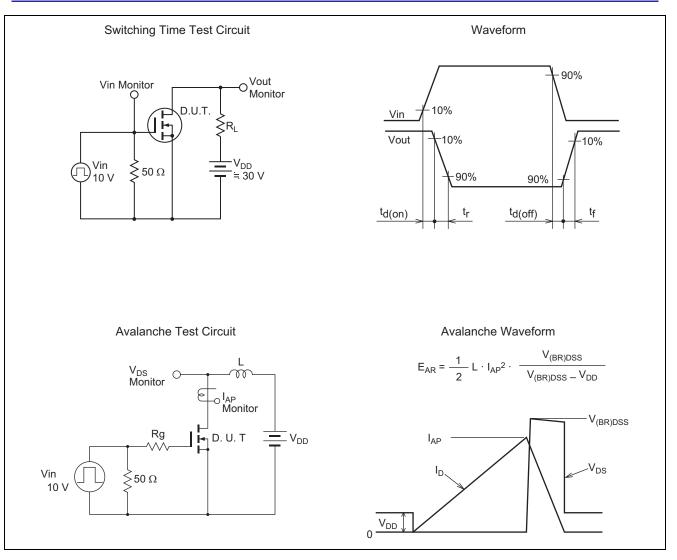






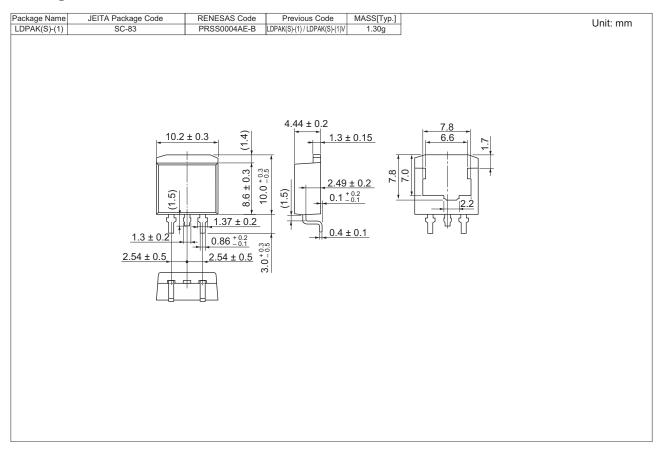








Package Dimensions



Ordering Information

Orderable Part Number	Quantity	Shipping Container
RJF0618JPE-00-J3	1000 pcs	Taping

Note: The symbol of 2nd "-" is occasionally presented as "#".



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