KODENSHI AUK

SDB10D60D2

Schottky Barrier Rectifier

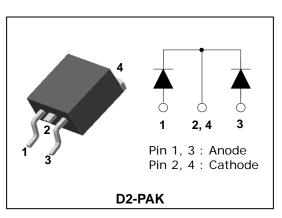
DUAL COMMON CATHODE SCHOTTKY RECTIFIER

Features

- Low forward voltage drop and leakage current
- Low power loss and High efficiency
- · High surge capability
- Dual common cathode rectifier
- Full lead(Pb)-free device and RoHS compliant device

Applications

- Power supply Output rectification
- Converter
- Free-wheeling diode
- Reverse battery protection
- Power inverters



Product Characteristics

I _{F(AV)}	2 X 5A
V _{RRM}	60V
V _{FM} at 125℃	0.55V
I _{FSM}	120A

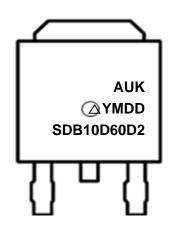
Description

The SDB10D60D2 has two schottky barriers arranged in a common cathode configuration. Typical applications are in switching power supplies, converters, free-wheeling diodes, and reverse battery protection.

Ordering Information

Device	Marking Code	Package	Packaging
SDB10D60D2	SDB10D60D2	D2-PAK	Tape & Reel

Marking Information



AUK = Manufacture Logo
Δ = Control Code of Manufacture
YMDD = Date Code Marking
. Y = Year Code
. M = Monthly Code
. DD = Daily Code

SDB10D60D2 = Specific Device Code

Absolute Maximum Ratings (Limiting Values)

Characteristic		Symbol	Value	Unit	
Maximum repetitive reverse voltage Maximum working peak reverse voltage Maximum DC blocking voltage		V _{RRM} V _{RWM} V _R	60	V	
Maximum average forward restified surrent	per diode	I	5	A	
Maximum average forward rectified current	total device	I _{F(AV)}	10		
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load per diode		I _{FSM}	120	A	
Storage temperature range		T _{stg}	-55 to +150	°C	
Maximum operating junction temperature		Tj	150		

Thermal Characteristics

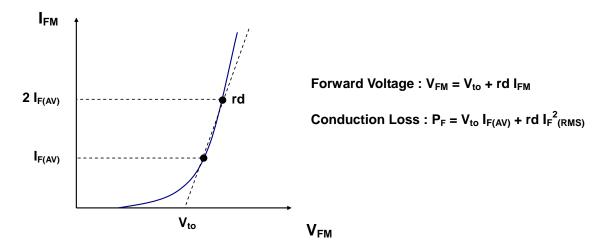
Characteristic		Symbol	Value	Unit
Maximum thermel registered innetion to core	per diode	D	3.0	°C/W
Maximum thermal resistance junction to case	total device	R _{th(j-c)}	2.8	

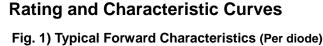
Electrical Characteristics (Per Diode)

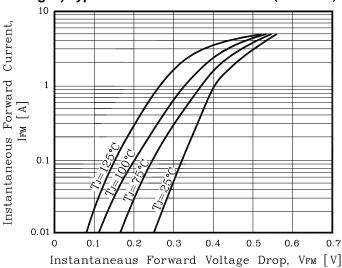
Characteristic	Symbol	Test Condition		Min.	Тур.	Max.	Unit
Peak forward voltage drop	${\sf V_{FM}}^{(1)}$	I _{FM} = 5A	T j =25 ℃	-	-	0.65	V
			Tj =125 ℃	-	-	0.55	
Reverse leakage current	$I_{RM}^{(1)}$	$V_{R} = V_{RRM}$	Tj =25 ℃	-	-	0.5	mA
			Tj =125 ℃	-	-	50	
Junction capacitance	Cj	$V_{R} = 10V_{DC}$, f=1MHz		-	160	-	pF

Note : (1) Pulse test : $t_{P}\!\leq\!380 us,$ Duty cycle $\leq\!2\%$

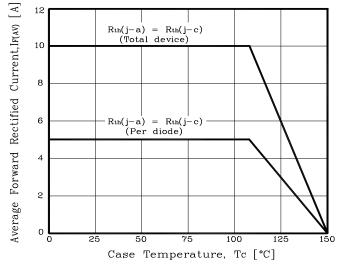
To evaluate the conduction losses use the following equation: $P_F = 0.36 I_{F(AV)} + 0.043 I_{F}^{2}{}_{(RMS)}$

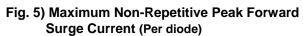












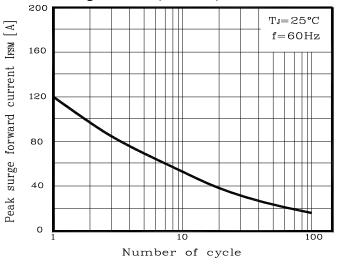


Fig. 2) Typical Reverse Characteristics (Per diode)

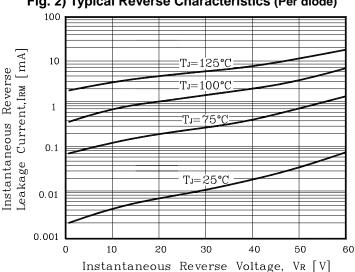


Fig. 4) Forward Power Dissipation (Per diode)

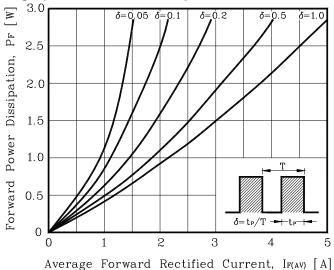
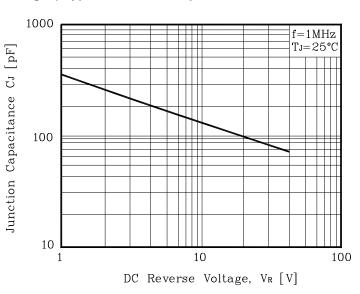
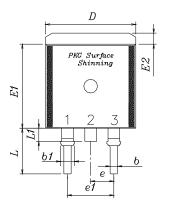


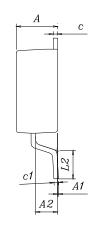
Fig. 6) Typical Junction Capacitance (Per diode)

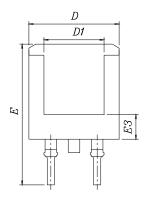


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Package Outline Dimension

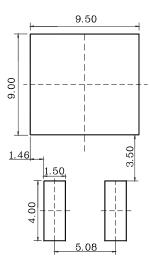






		NOTE			
SYMBOL		MINIMUM NOMINAL MAXIMUI		NOTE	
Α	4.35	4.50	4.65		
A1	—	—	0.15		
A2	2.20	2.40	2.60		
b	0.70	0.80	0.90		
b1	1.17	1.27	1.37		
С	0.40	0.50	0.60		
c1	0.40	0.50	0.60		
D	9.80	10.00	10.20		
D1	6.40	6.60	6.80		
E	15.00	15.40	15.80		
E1	9.05	9.20	9.35		
E2	1.00	1.20	1.40		
E3	2.50	2.70	2.90		
e	2.34	2.54	2.74		
e1	4.88	5.08	5.28		
L	4.60	5.00	5.40		
L1	1.40	1.45	1.50		
L2	2.50	_			

* Recommend PCB solder land (Unit : mm)



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