

3.3Vdc HCMOS SMD VCXO CRYSTAL CLOCK OSCILLATOR



5.0 x 3.2 x 1.2mm

ASFLV SERIES

: PRELIMINARY

* RoHS COMPLIANT

FEATURES:

- Pb-Free/RoHS Compliant
- Smallest industry package
- Tri state function
- Suitable for high density SMT. Reflow capable
- Hermetically sealed

APPLICATIONS:

- CCD clock for VTR camera
- Computers and peripherals
- Portable electronics

STANDARD SPECIFICATIONS:

PARAMETERS	
Frequency Range	1.500 MHz to 50 MHz
Operating Temperature	-10°C to + 70°C (See Options)
Storage Temperature	- 50°C to + 100°C
Overall Frequency Stability	± 100 ppm max. (see options)
Supply Voltage (Vdd)	3.3Vdc ± 10% (see options)
Supply Current (Idd)	8mA max for 1.5 ~ 19.9MHz
	15mA max for 20.0 ~ 40.0MHz
	22mA max for 40.1 ~ 50.0MHz
Symmetry	50% ±10 (see options)
Rise and Fall Time (Tr/Tf)	5ns max
Output Load	15 pF (HCMOS)
Output Voltage	VOH = 0.9* Vdd min.
	VOL = 0.1* Vdd max.
Transfer Function	Positive
Voltage Control	0.3 to 3.0V
Linearity	< ±150ppm frequency deviation: 10% max.; 6% typical.
	> ±150ppm frequency deviation: 20% max.; 15% typical.
Start-up Time	5mS max for 1.5 ~ 32MHz
	10mS max for f > 32MHz
Aging at 25°C/year	± 5ppm @ 25°C
Phase Jitter RMS	10pS typical
Pullability	±50ppm (see options)

PIN	FUNCTION
1	Voltage Control
2	Ground
3	Output
4	VDD

MARKING:

- TUH : Frequency (T= First " ten " digit of freq; U = First " unit " of freq; H = First " tenth " of freq; Ex: 100 for 10.0 MHz)
- AZYX : (Z: month A to L; Y: year 4 for 2004; X: Traceability code)

OPTIONS AND PART IDENTIFICATION (Left blank if standard):

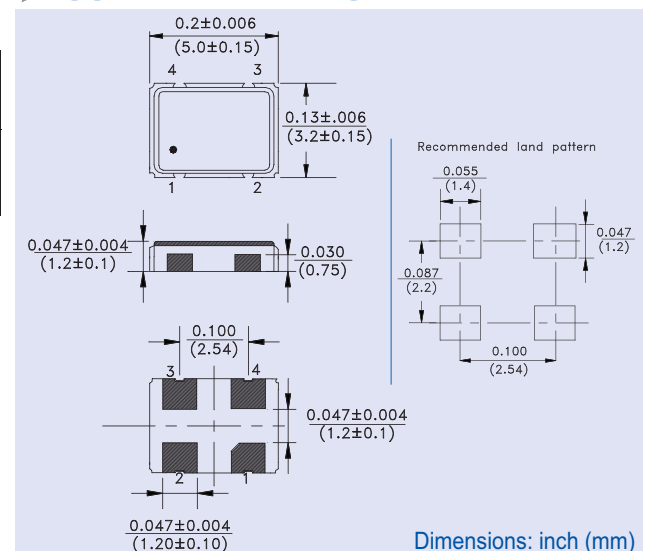
ASFLV - Frequency - Temperature - Overall Frequency Stability - Duty Cycle - Packaging

Temperature options: L for -40°C to +85°C
Overall Frequency stability options: R for ± 25 ppm max. C for ± 50 ppm max.

Pullability options: N100 for ±100ppm N150 for ±150ppm
Symmetry option: - S for 50% at ±55%

Packaging option: T for Tape and Reel (1,000pcs/reel)

OUTLINE DRAWING:



ABRACON IS
ISO 9001 / QS 9000
CERTIFIED

rev1.1-9/05



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